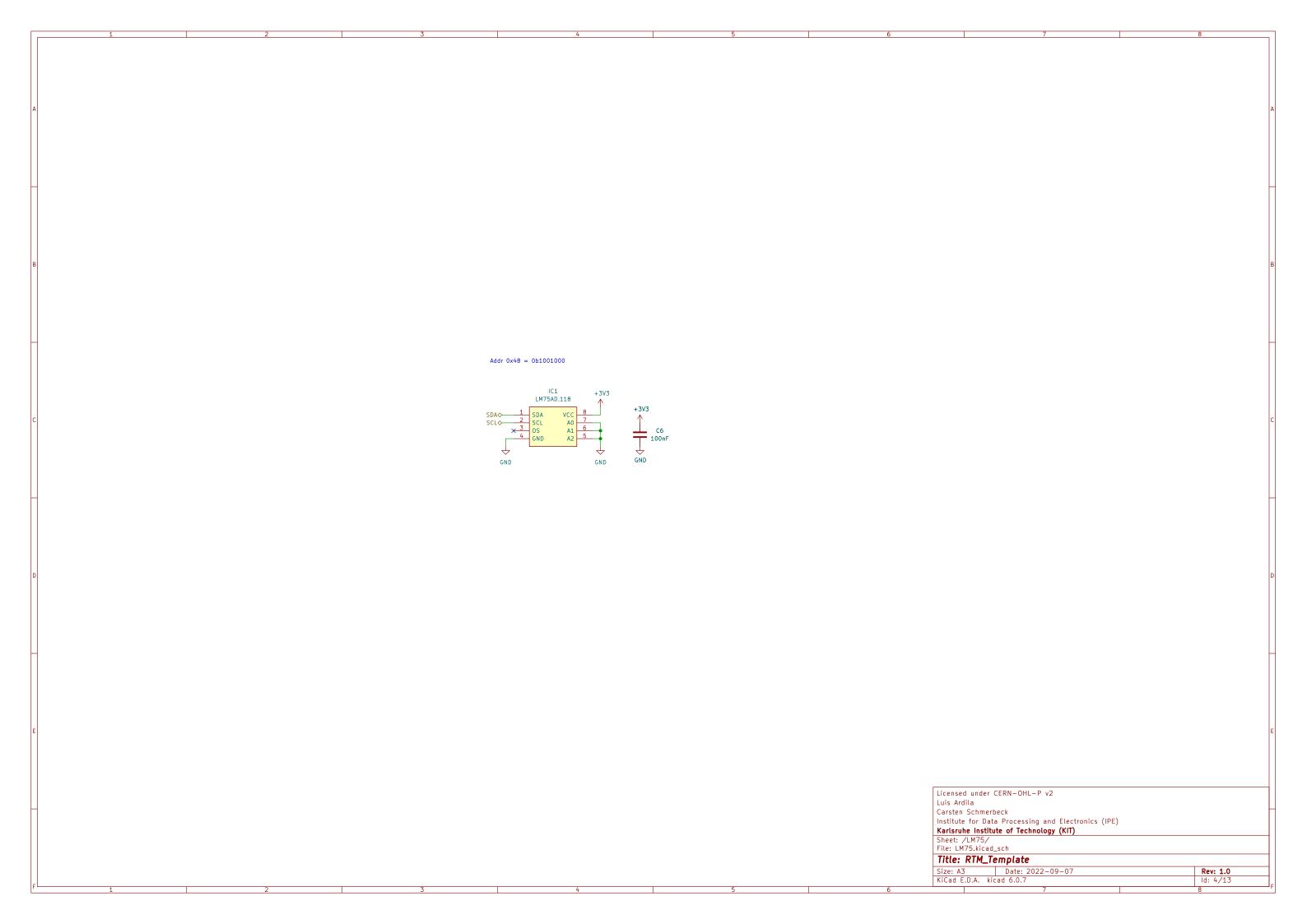
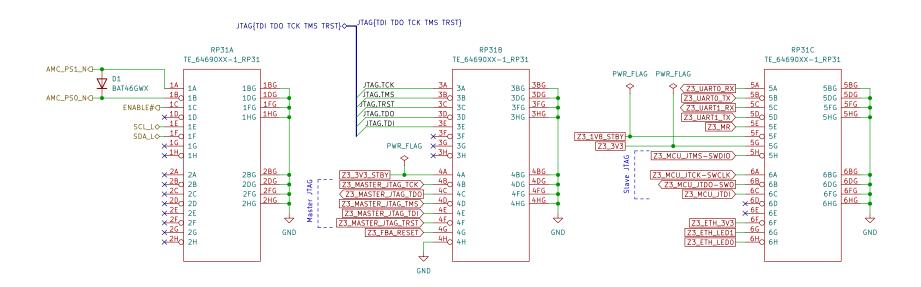
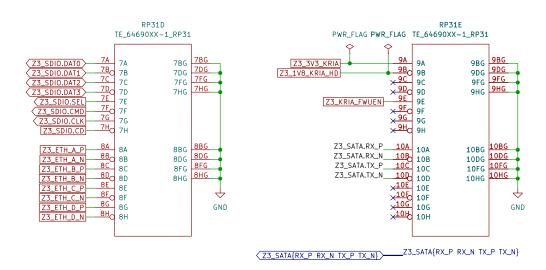


 $C_T = 50k \text{ pull-up } -> 300 \text{ ms delay}$ TPS3808G33 -> Sense = 3.3 V -> Threshold = 3.07V

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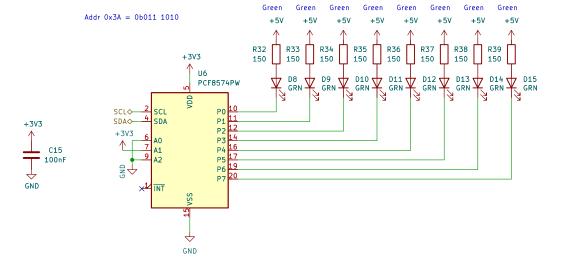
3 4

If the DRC gives an error because of the Thermal Pad, manually connect the pad in the Layout to GND +3V3 | I2C Address 8'hA0 -> 8'b1010_000(r/w) 7'h50 -> 7'b101_0000 Licensed under CERN-OHL-P v2
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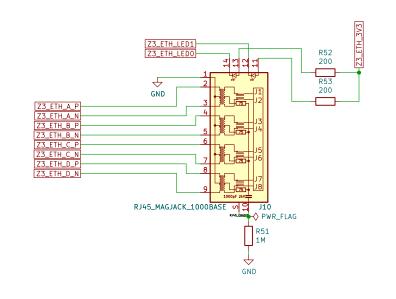
AMC_USR_LED

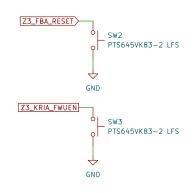


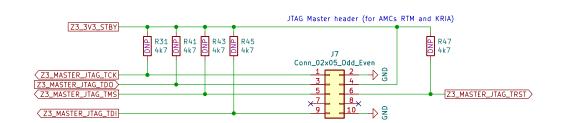
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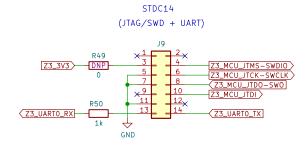
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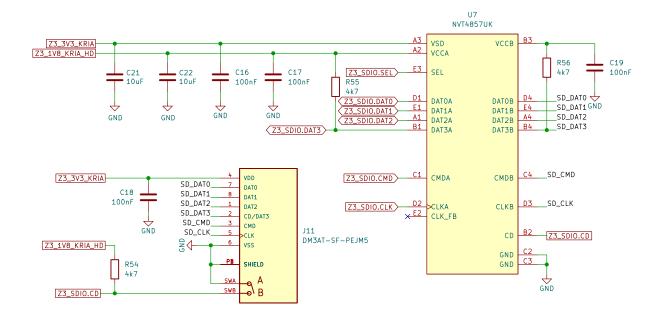




Frontpanel Pin header connector for STLINK V3 MINI

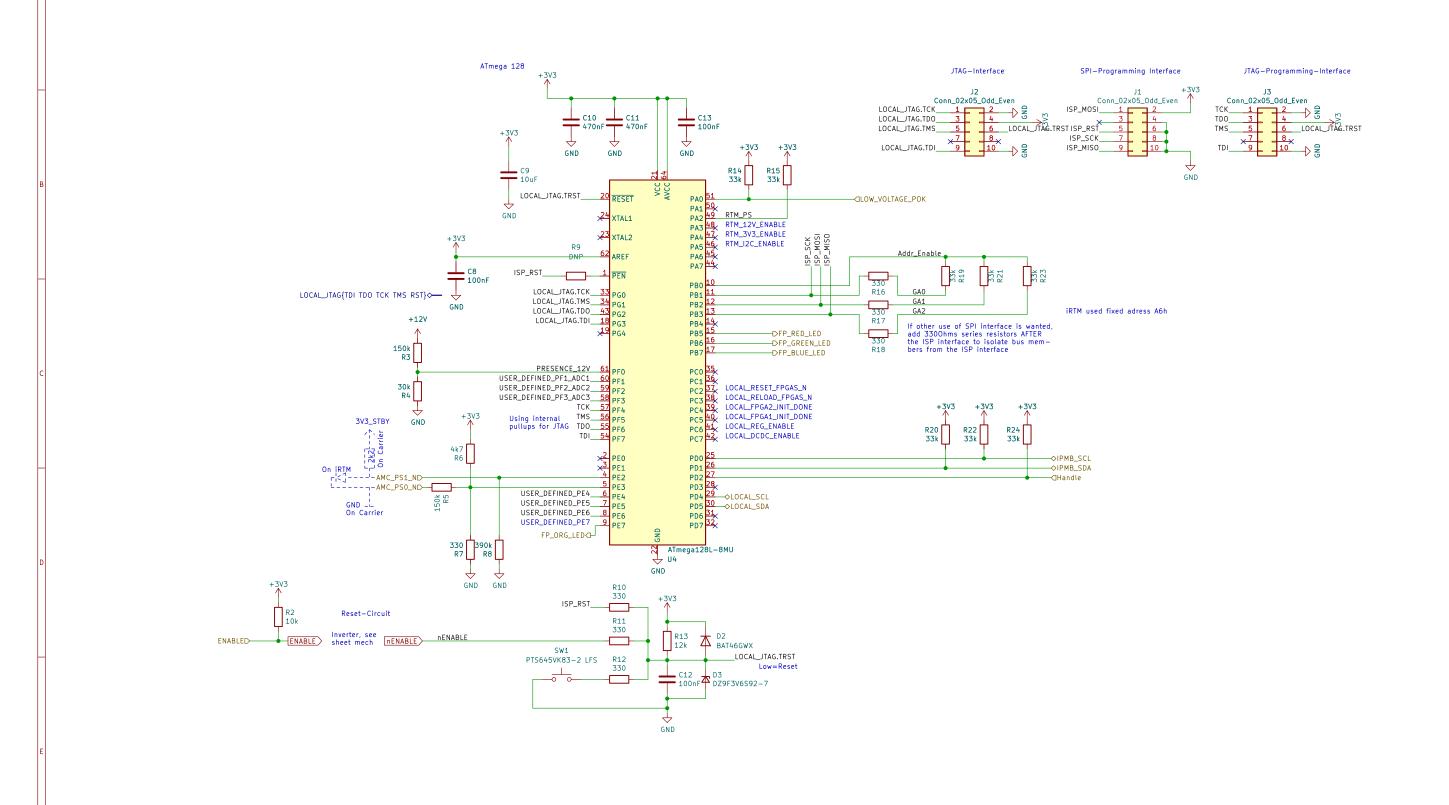
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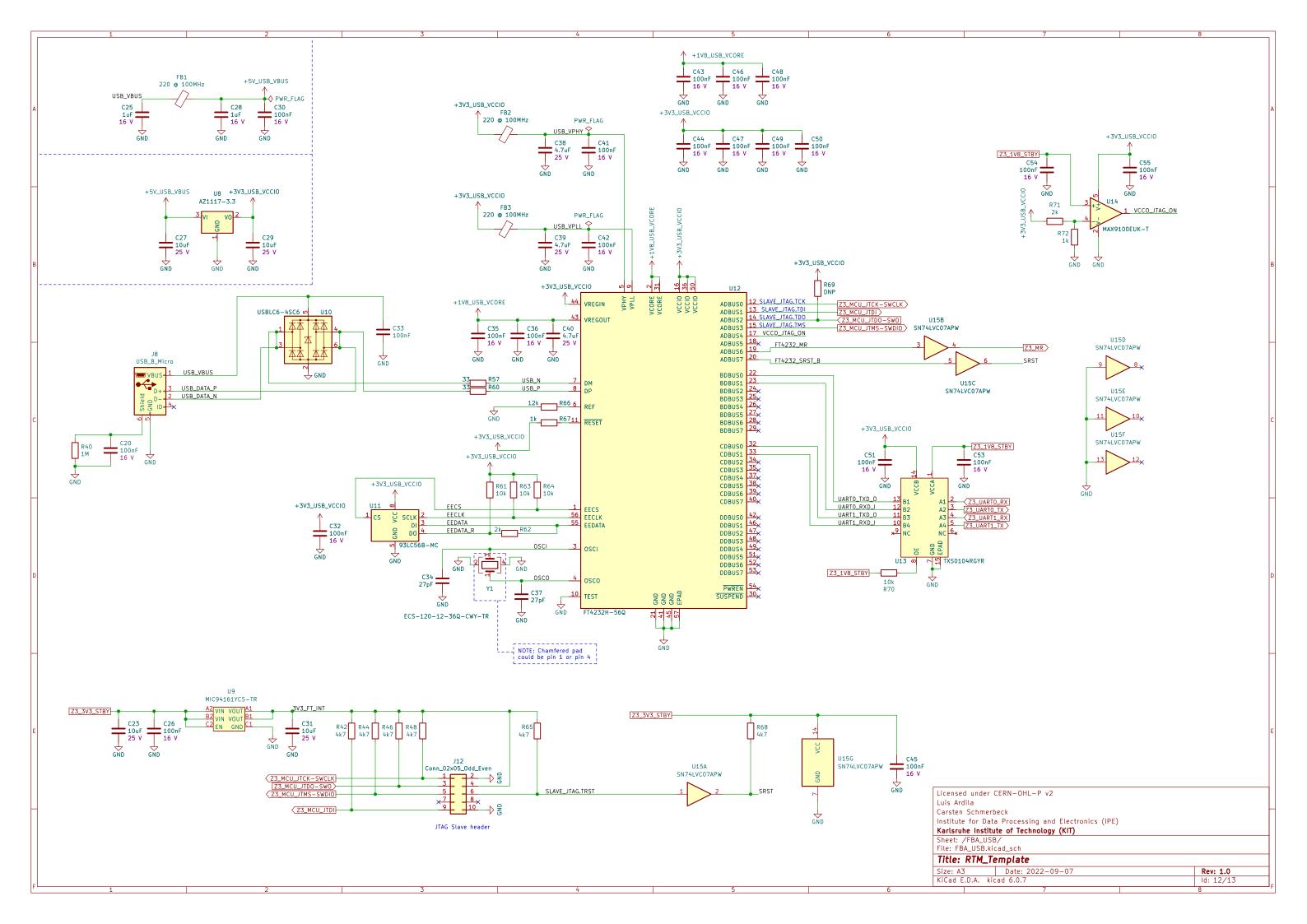
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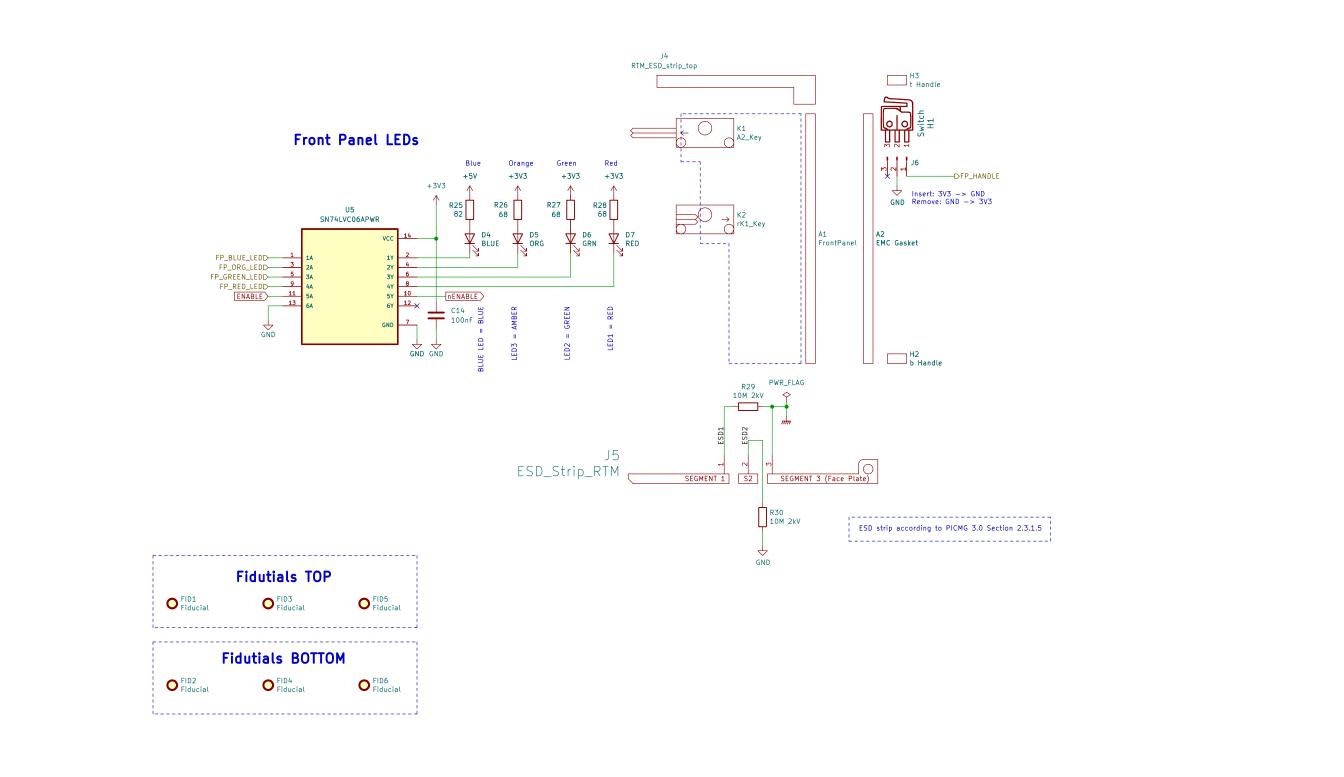


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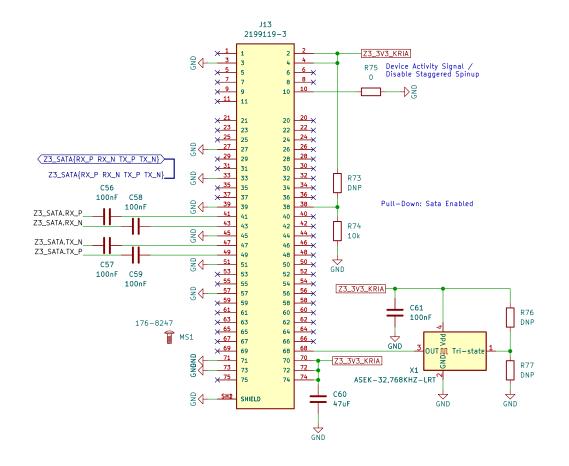
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3 4 5



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