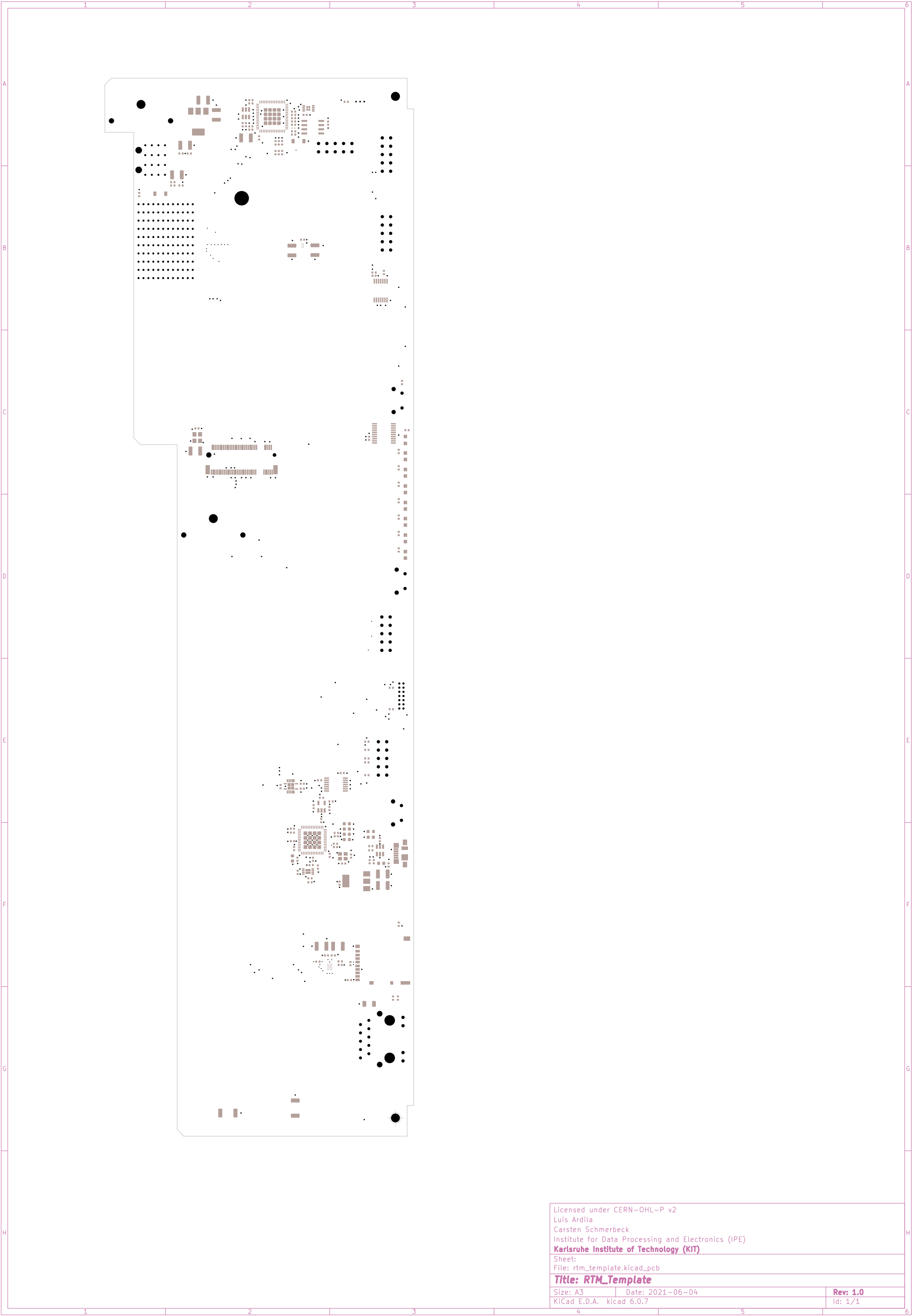


Licensed under CERN-OHL-P v2		
Luis Ardila		
Carsten Schmerbeck		
Institute for Data Processing and Electronics (IPE)		
Karlsruhe Institute of Technology (KIT)		
Sheet:		
File: rtm_template.kicad_pcb		
Title: RTM_Template		
Size: A3	Date: 2021-06-04	Rev: 1.0
KiCad E.D.A. kicad 6.0.7		Id: 1/1



licensed under CERN-OHL-P v2  
 Luis Ardila  
 Carsten Schmerbeck  
 Institute for Data Processing and Electronics (IPE)  
**Karlsruhe Institute of Technology (KIT)**

---

Sheet:  
 File: rtm\_template.kicad\_pcb

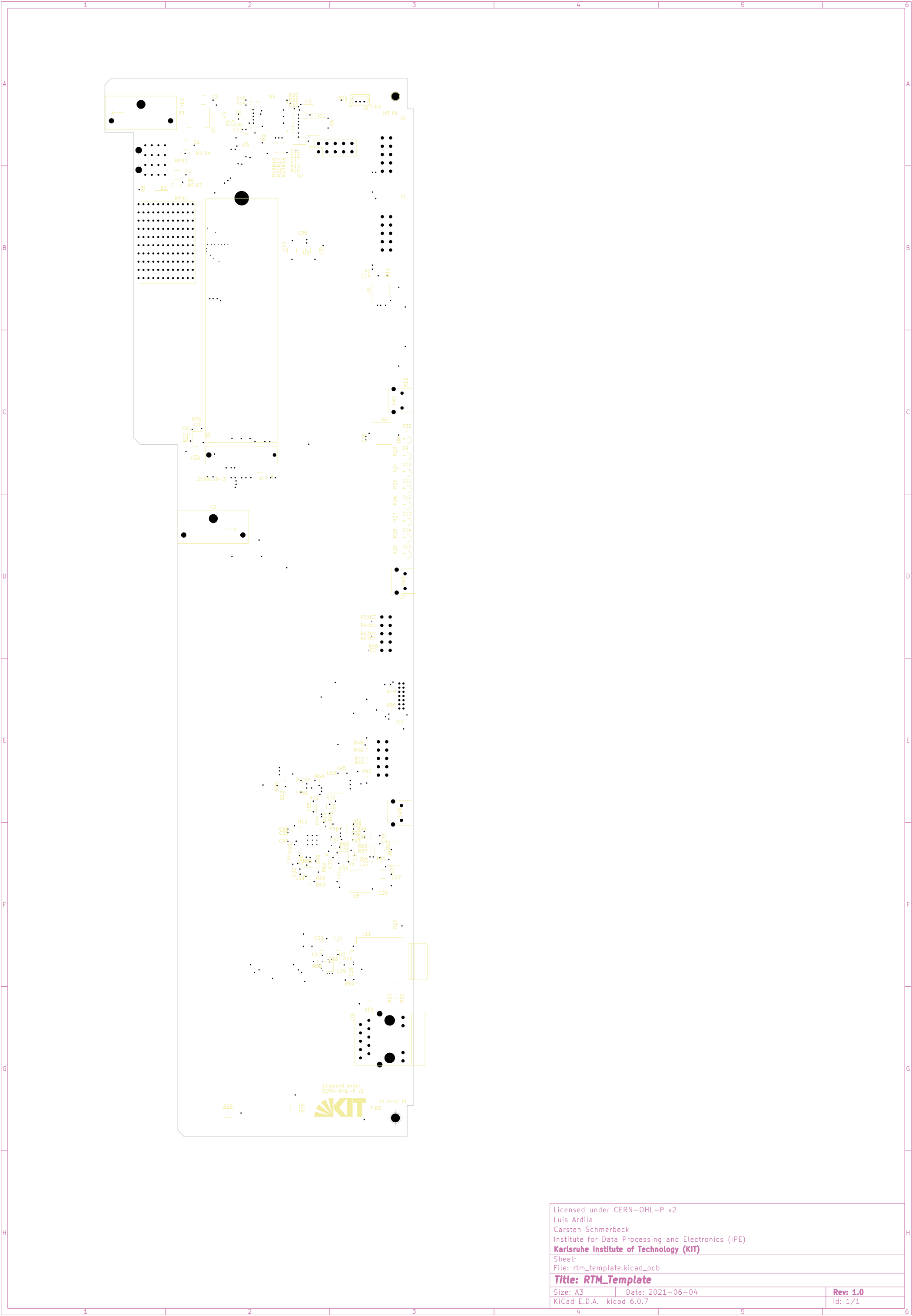
---

**Title: RTM\_Template**

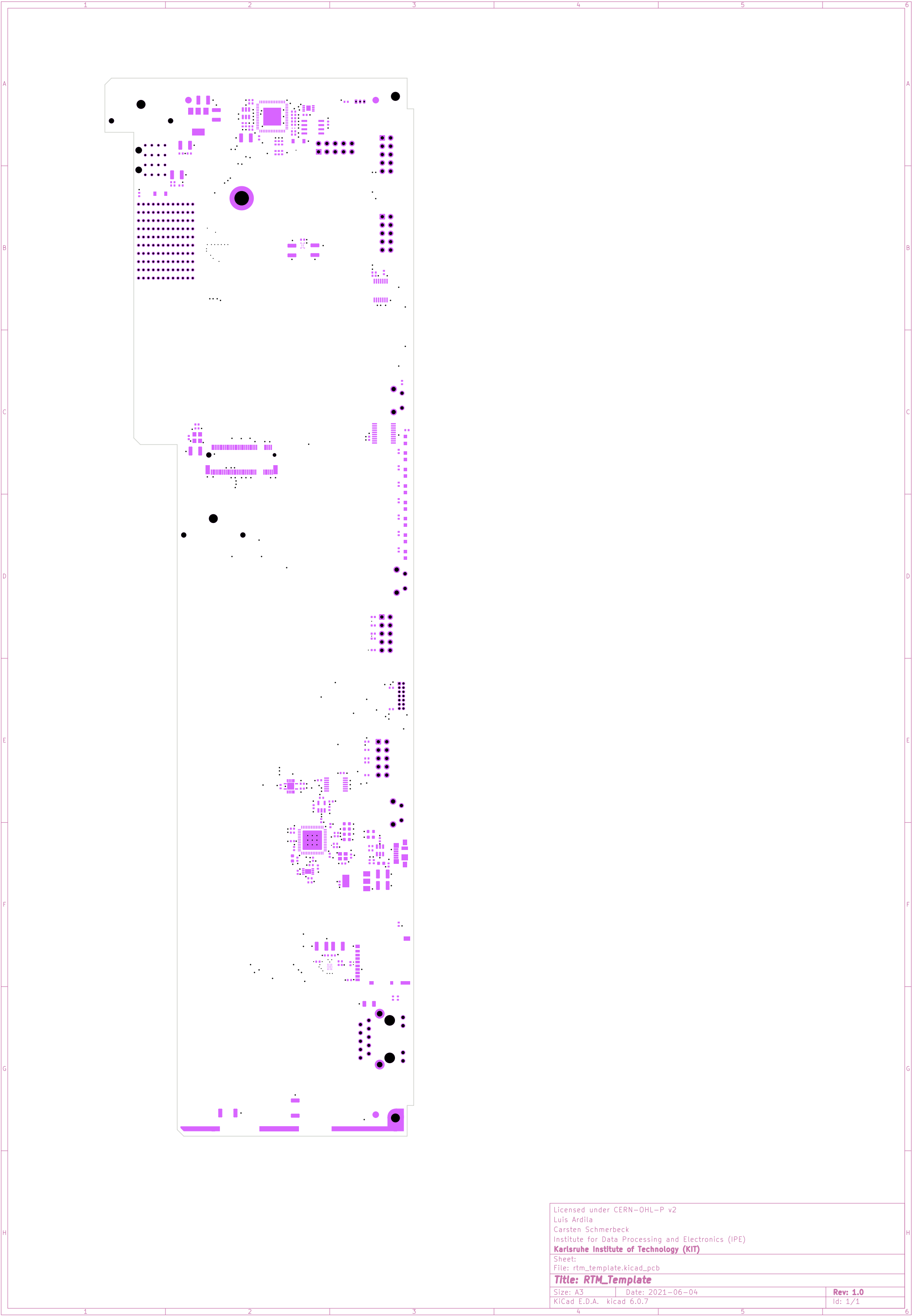
---

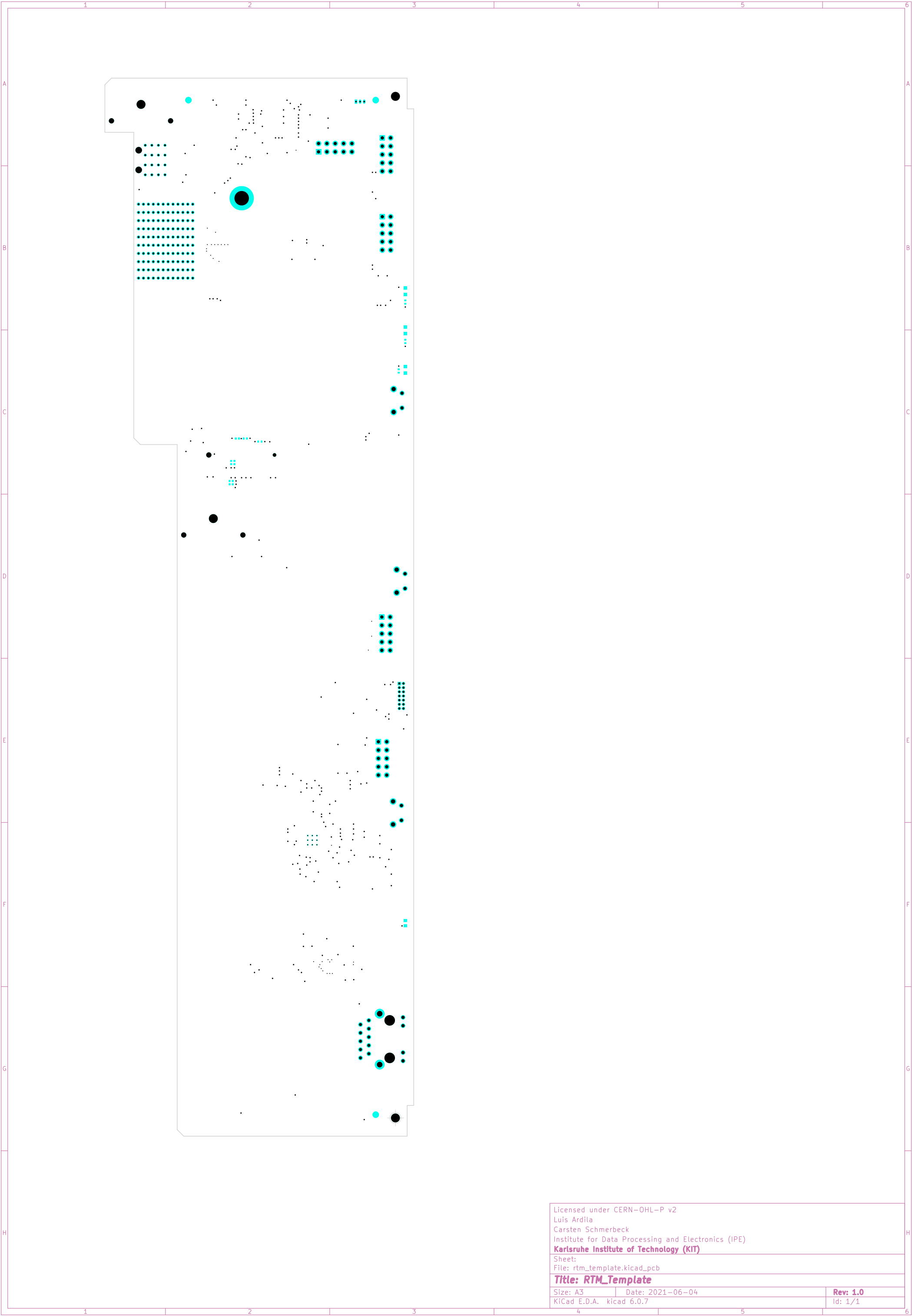
Size: A3	Date: 2021-06-04	<b>Rev: 1.0</b>
KiCad E.D.A. kicad 6.0.7		Id: 1/1

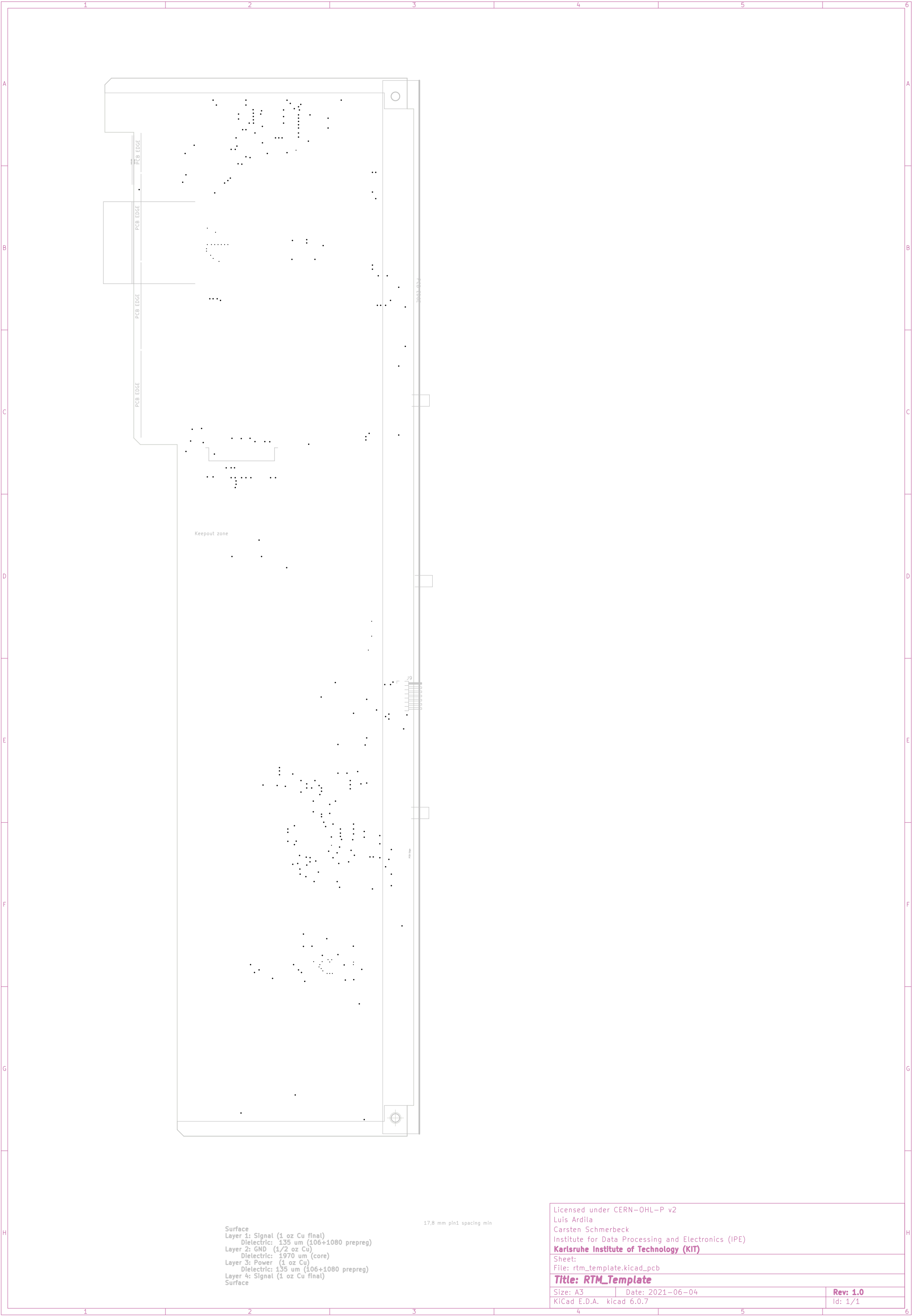




Sheet:		
File: rtm_template.kicad_pcb		
<b>Title: RTM_Template</b>		
Size: A3	Date: 2021-06-04	<b>Rev: 1.0</b>
KiCad E.D.A. kicad 6.0.7		Id: 1/1



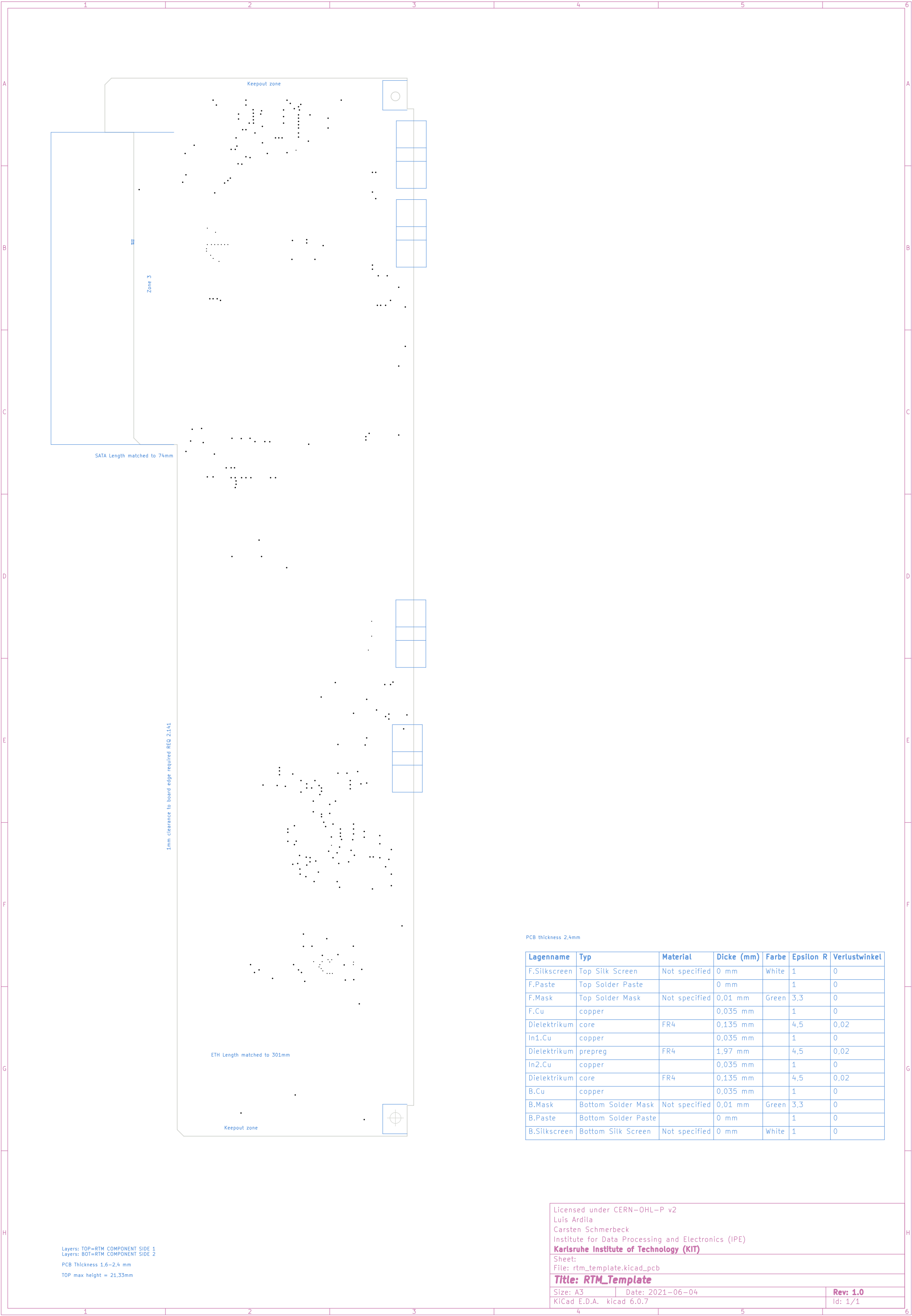




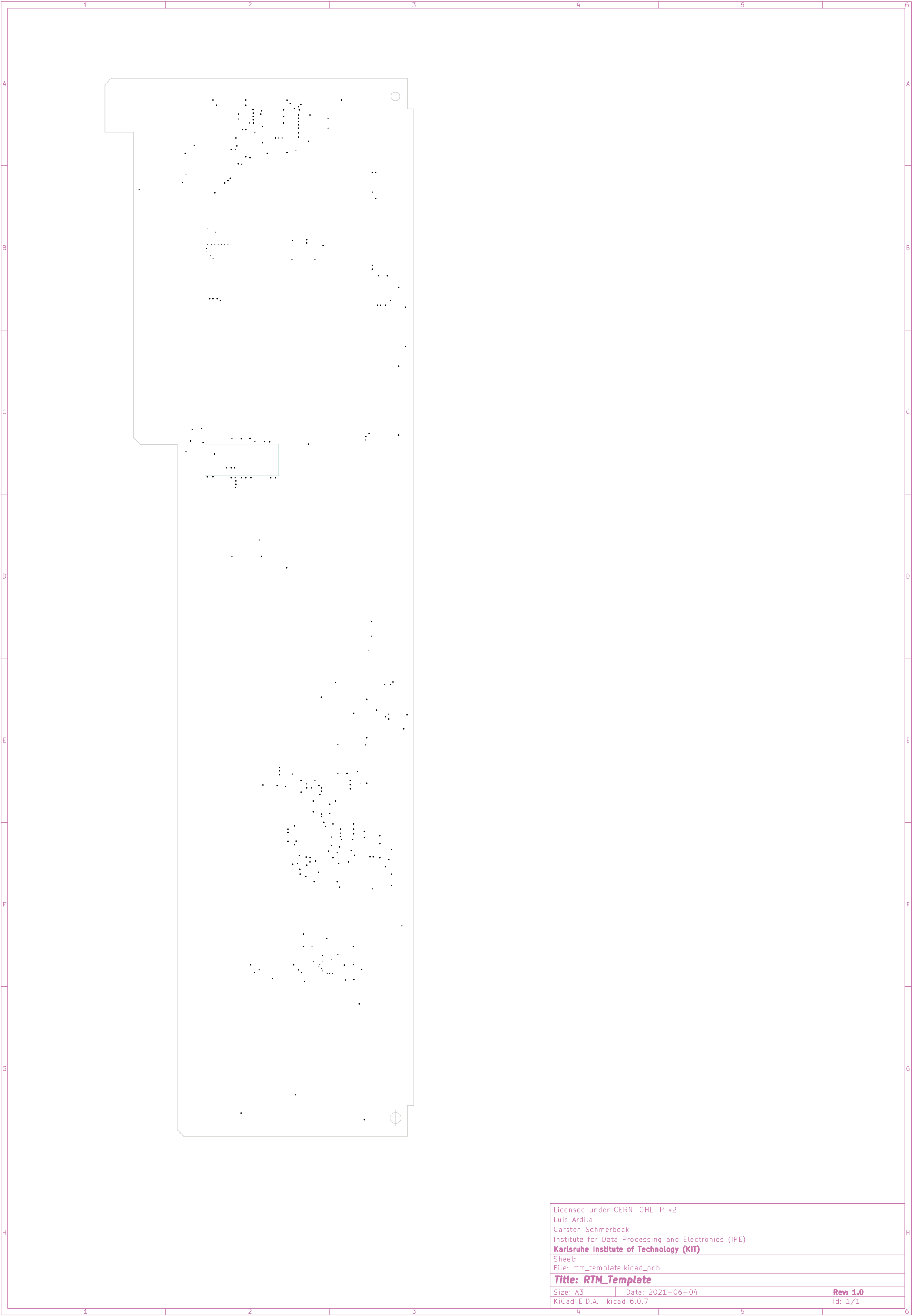
Surface  
Layer 1: Signal (1 oz Cu final)  
Dielectric: 135 um (106+1080 prepreg)  
Layer 2: GND (1/2 oz Cu)  
Dielectric: 1970 um (core)  
Layer 3: Power (1 oz Cu)  
Dielectric: 135 um (106+1080 prepreg)  
Layer 4: Signal (1 oz Cu final)  
Surface

17.8 mm pin1 spacing min

Licensed under CERN-OHL-P v2		
Luis Ardila		
Carsten Schmerbeck		
Institute for Data Processing and Electronics (IPE)		
Karlsruhe Institute of Technology (KIT)		
Sheet:		
File: rtm_template.kicad_pcb		
Title: RTM_Template		
Size: A3	Date: 2021-06-04	Rev: 1.0
KiCad E.D.A. kicad 6.0.7	Id: 1/1	



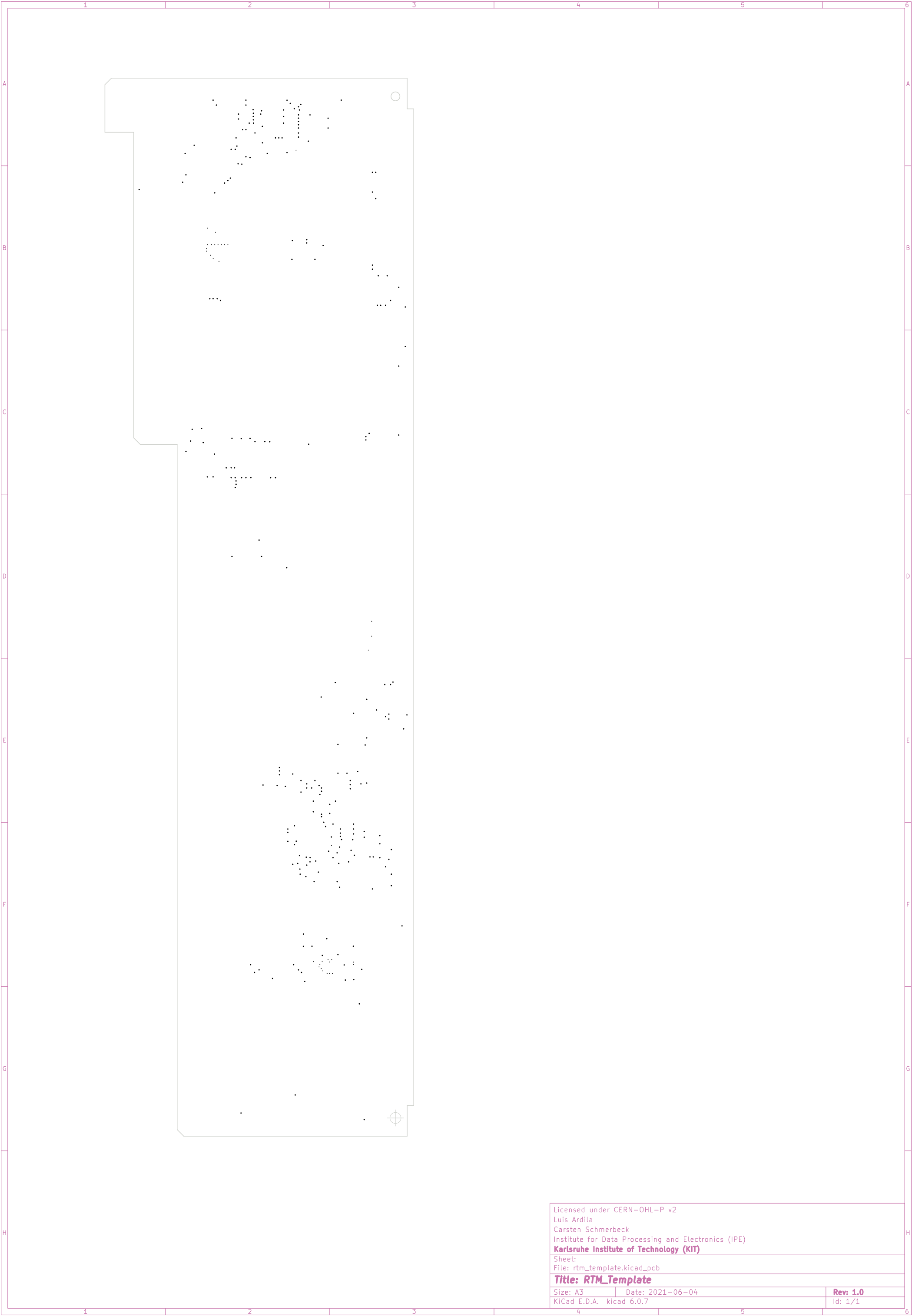
Lagenname	Typ	Material	Dicke (mm)	Farbe	Epsilon R	Verlustwinkel
F.Silkscreen	Top Silk Screen	Not specified	0 mm	White	1	0
F.Paste	Top Solder Paste		0 mm		1	0
F.Mask	Top Solder Mask	Not specified	0,01 mm	Green	3,3	0
F.Cu	copper		0,035 mm		1	0
Dielektrikum	core	FR4	0,135 mm		4,5	0,02
In1.Cu	copper		0,035 mm		1	0
Dielektrikum	prepreg	FR4	1,97 mm		4,5	0,02
In2.Cu	copper		0,035 mm		1	0
Dielektrikum	core	FR4	0,135 mm		4,5	0,02
B.Cu	copper		0,035 mm		1	0
B.Mask	Bottom Solder Mask	Not specified	0,01 mm	Green	3,3	0
B.Paste	Bottom Solder Paste		0 mm		1	0
B.Silkscreen	Bottom Silk Screen	Not specified	0 mm	White	1	0



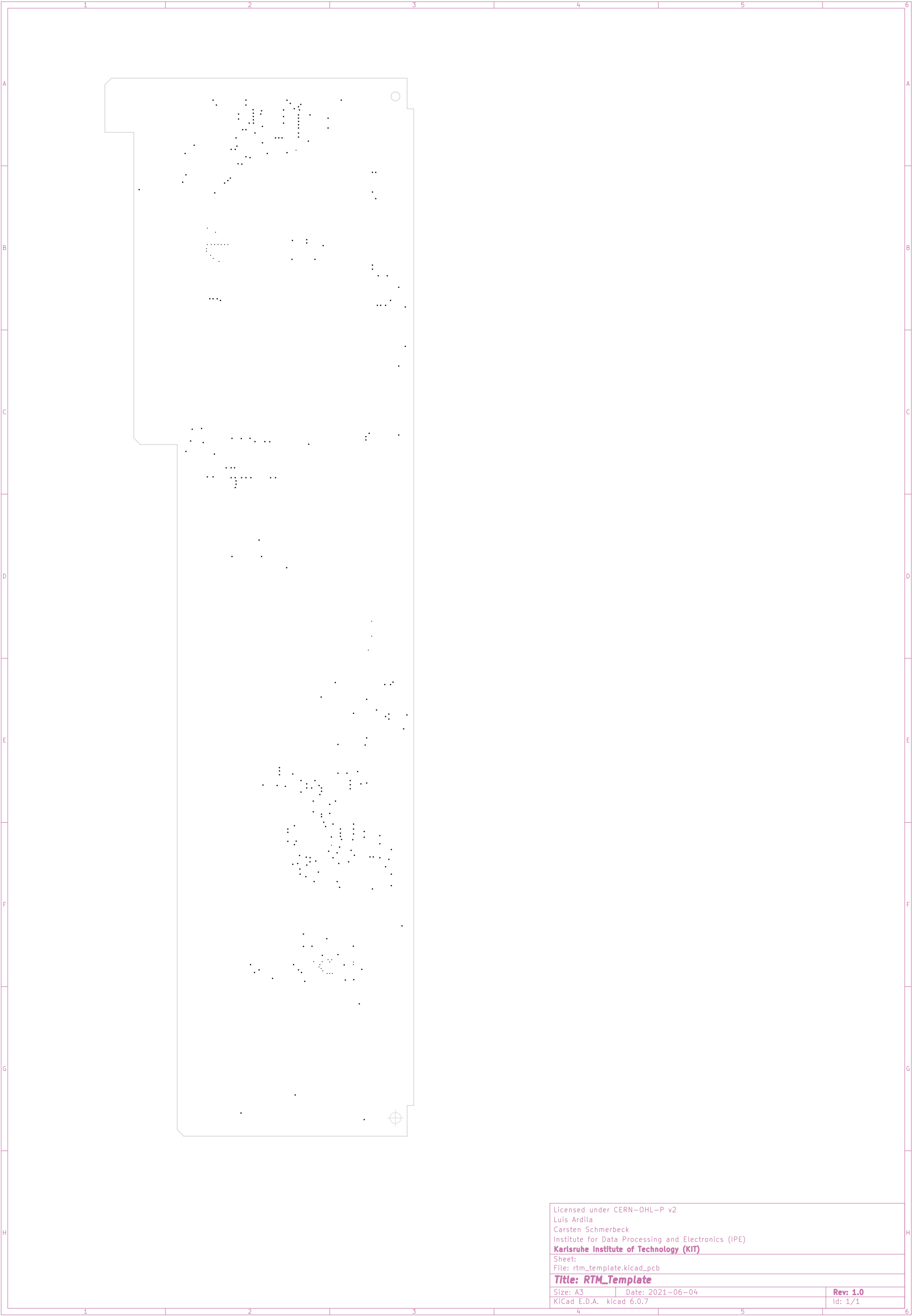
Licensed under CERN-OHL-P v2 Luis Ardila Carsten Schmerbeck Institute for Data Processing and Electronics (IPE) <b>Karlsruhe Institute of Technology (KIT)</b>		
Sheet: File: rtm_template.kicad_pcb		
<b>Title: RTM_Template</b>		
Size: A3	Date: 2021-06-04	Rev: 1.0
KiCad E.D.A. kicad 6.0.7		Id: 1/1

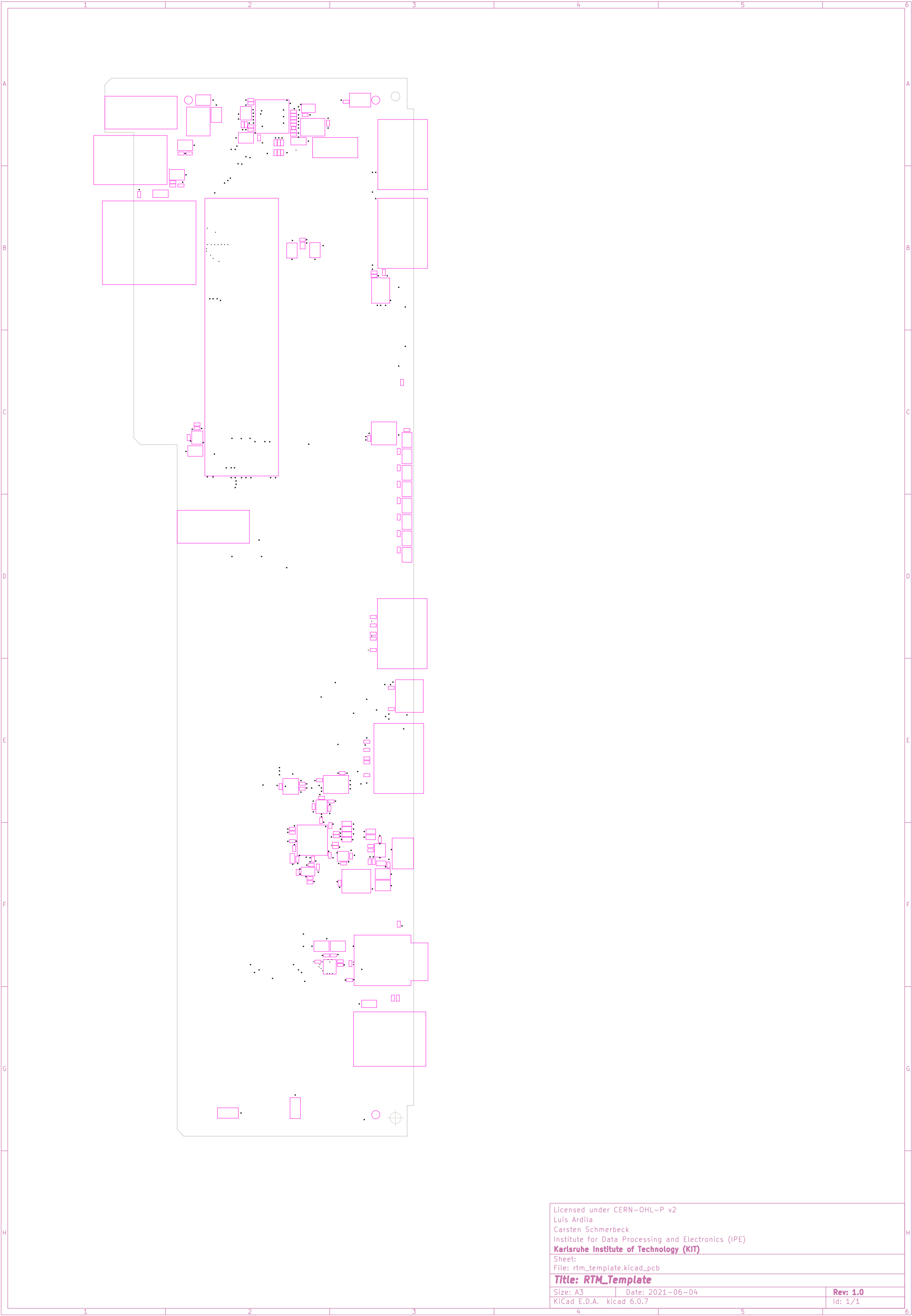


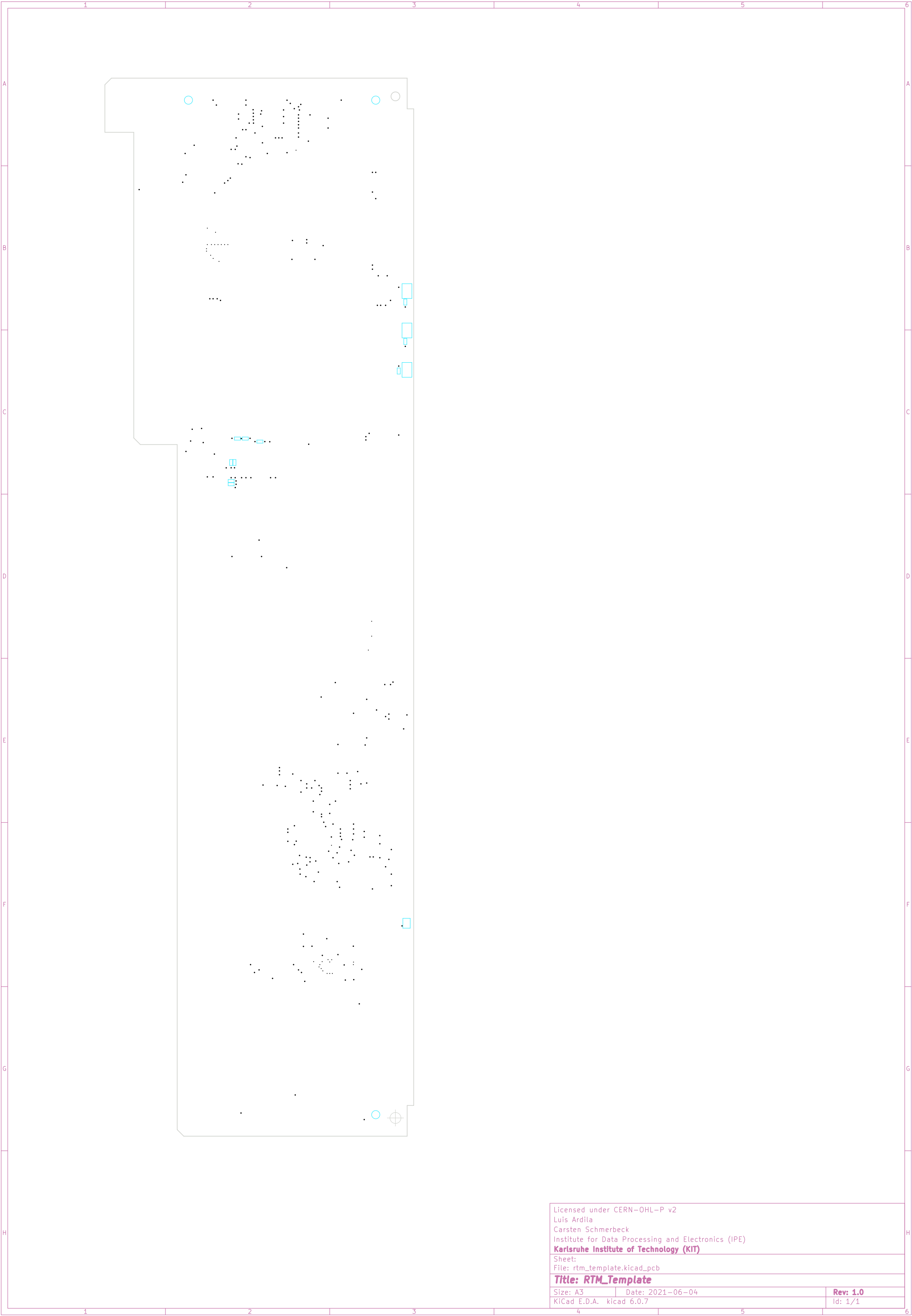




Licensed under CERN-OHL-P v2 Luis Ardila Carsten Schmerbeck Institute for Data Processing and Electronics (IPE) <b>Karlsruhe Institute of Technology (KIT)</b>		
Sheet: File: rtm_template.kicad_pcb		
<b>Title: RTM_Template</b>		
Size: A3	Date: 2021-06-04	Rev: 1.0
KiCad E.D.A. kicad 6.0.7		Id: 1/1







Licensed under CERN-OHL-P v2 Luis Ardila Carsten Schmerbeck Institute for Data Processing and Electronics (IPE) <b>Karlsruhe Institute of Technology (KIT)</b>		
Sheet: File: rtm_template.kicad_pcb		
<b>Title: RTM_Template</b>		
Size: A3	Date: 2021-06-04	Rev: 1.0
KiCad E.D.A. kicad 6.0.7		Id: 1/1



