

Luis Ardila, Marvin Fuchs,
Robert Gartmann, Lukas Scheller
Institute for Data Processing and Electronics (IPE)
Karlsruhe Institute of Technology (KIT)

Sheet:

File: fmc+_sma.kicad_pcb

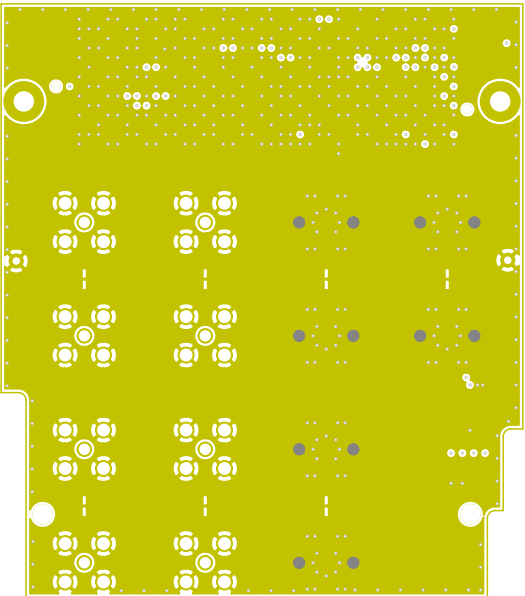
Title: FMC+ SMA breakout

Size: A4

Date: 2022-12-09

KiCad E.D.A. KiCad 5.1.7-a382d34a888ubuntu16.04.1

Rev: 1.0
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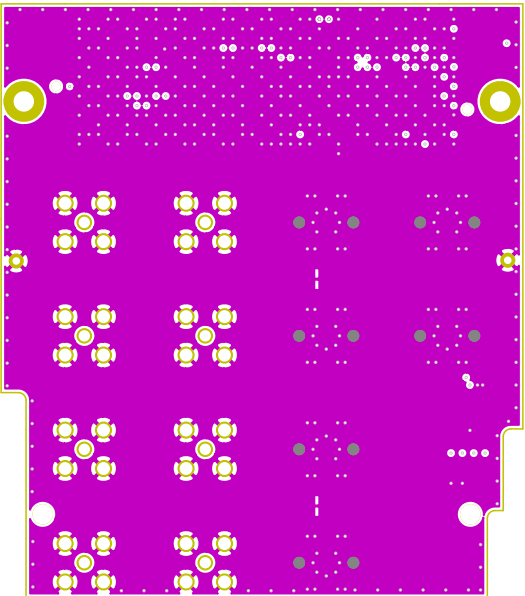
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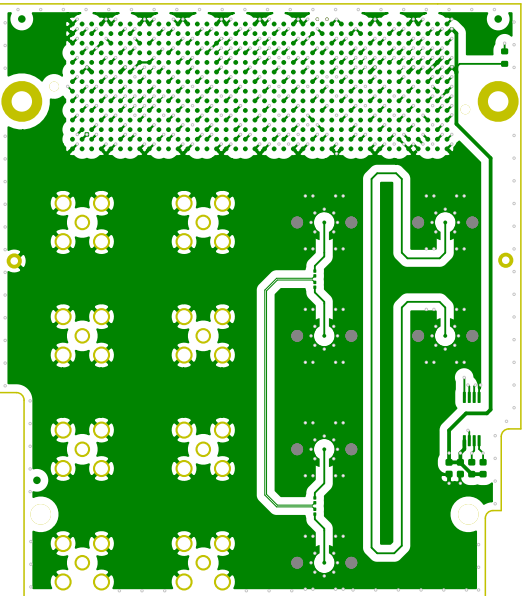
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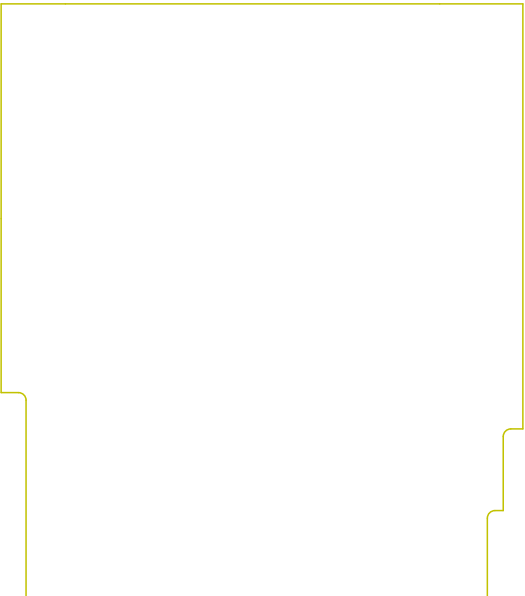
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B					5
					6
C					
D					



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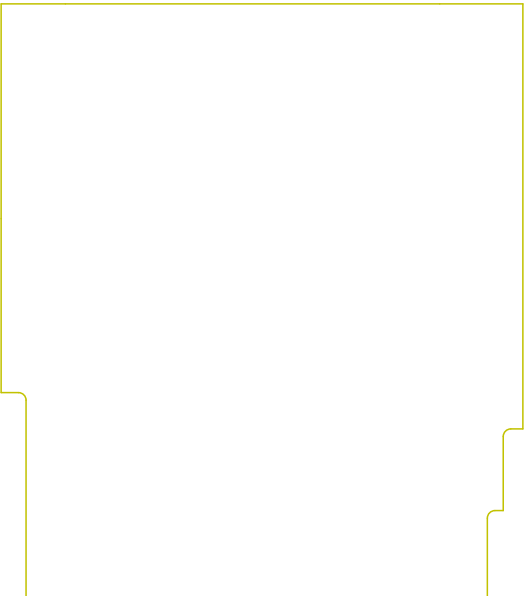
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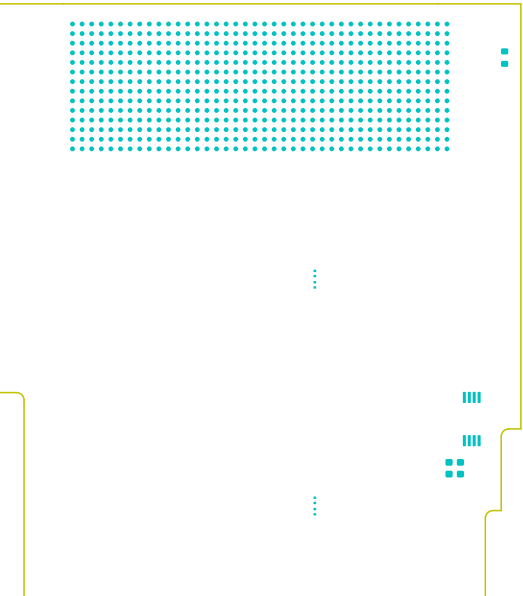
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A									A
B									B
C									C
D					1	2	3	4	D



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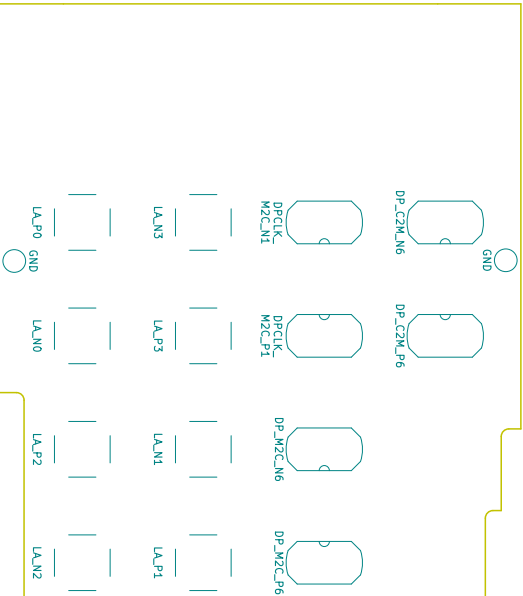
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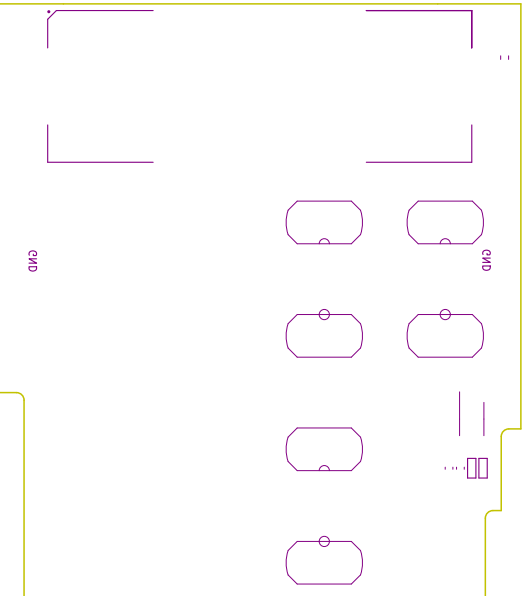
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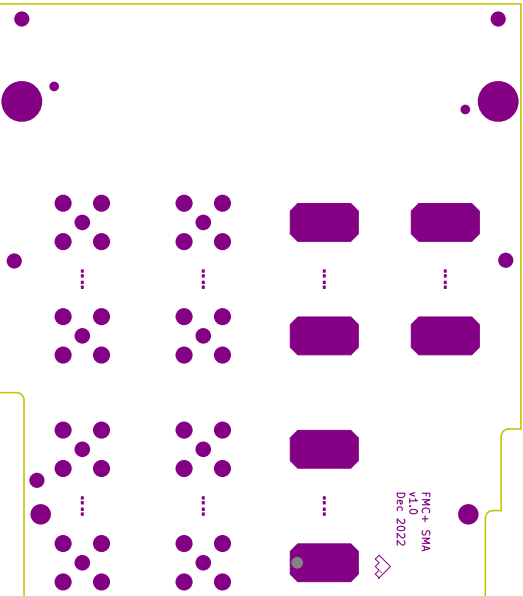
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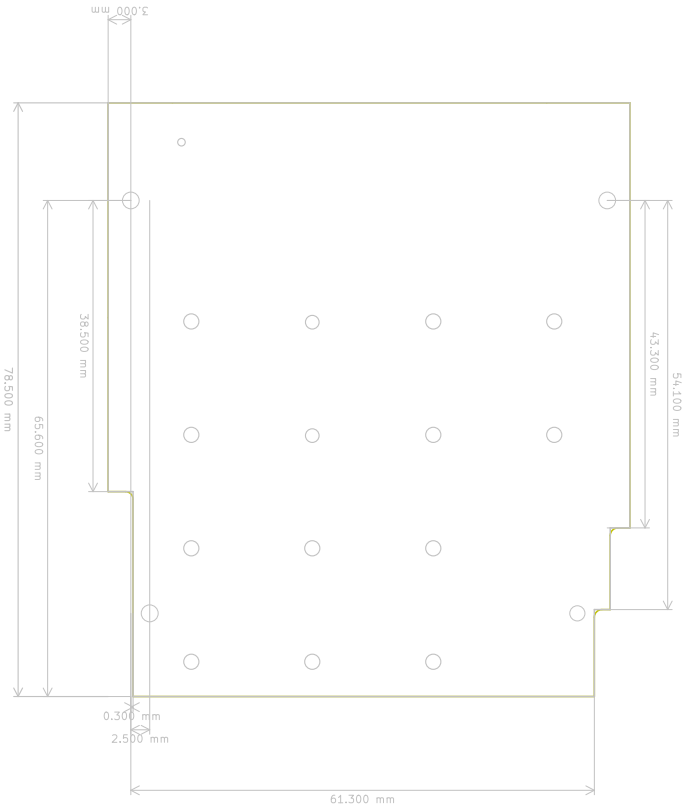
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Board Stackup				Single Ended Impedance		Differential Impedance			
#	Layer (Title) Name	Layer (Material) Type	Thickness (um)	Dk	Trace Width (um)	Impedance (Ohms)	Trace Width (um)	Trace Space (um)	Impedance (Ohms)
	F.Silks	Silkscreen							
	F.Mask	Soldermask	35		245	50 +/- 10/	120	115	100 +/- 10/
1	F.Cu	2x Prepreg 1080	140	4.3					
			35						
2	Int.Cu	Core	1200	4.3					
			35						
3	Int.Cu	2x Prepreg 1080	140	4.3			120	115	100 +/- 10/
			35		245	50 +/- 10/			
4	B.Cu	Soldermask							
	B.Mask	Silkscreen							

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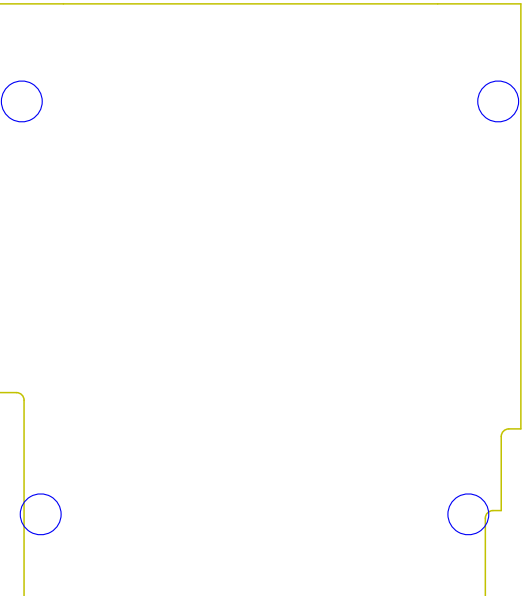
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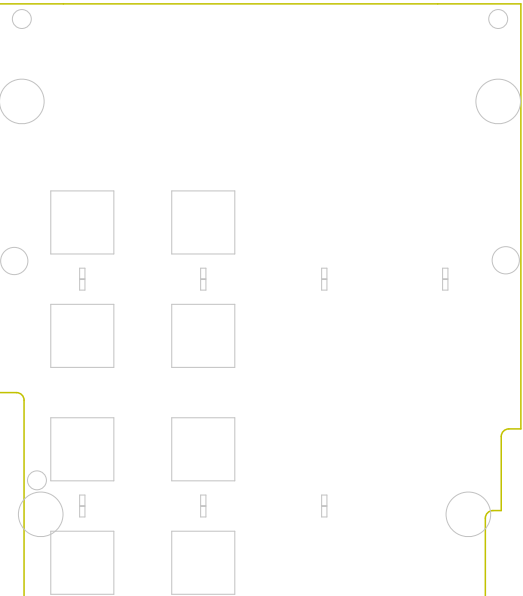
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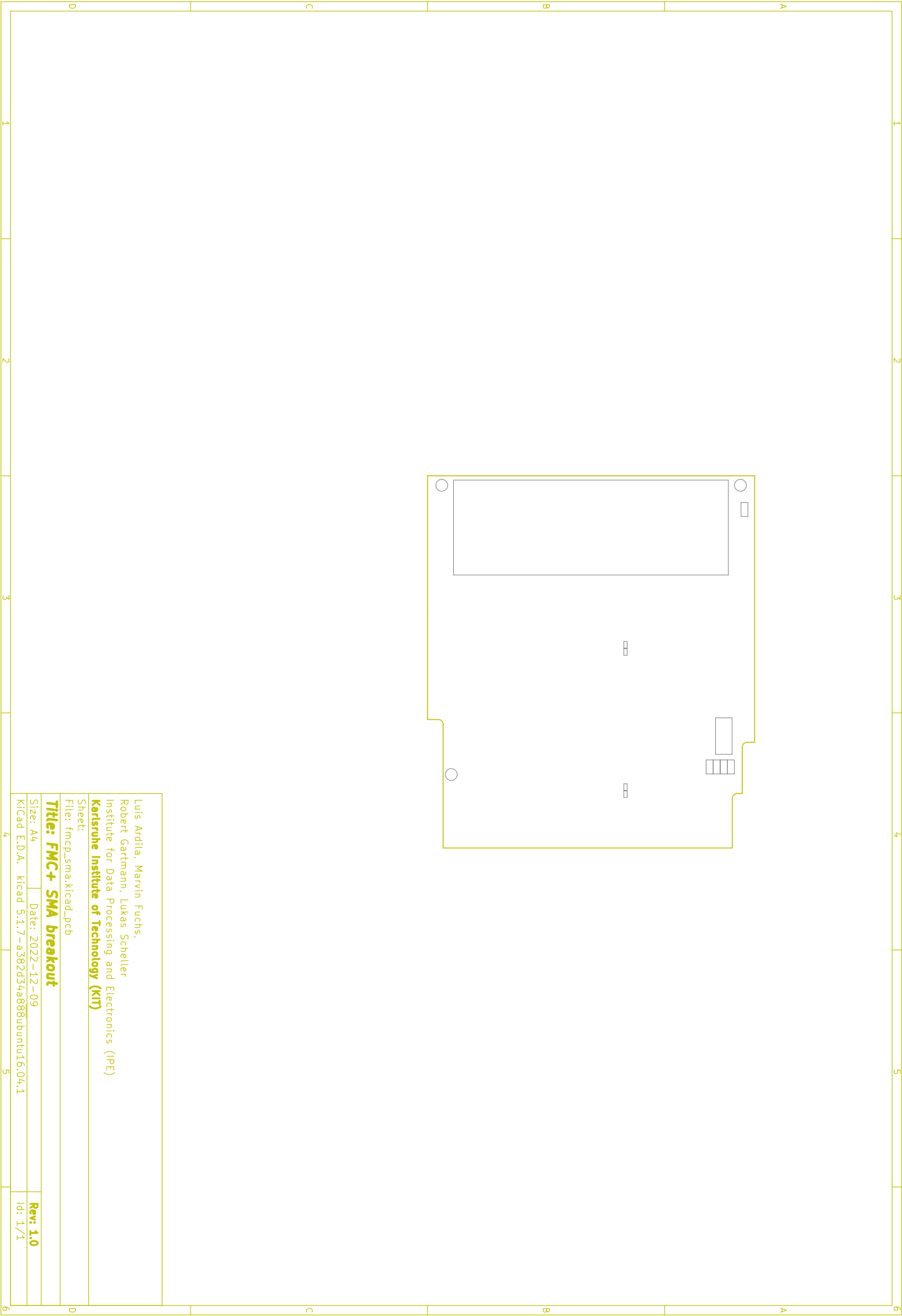
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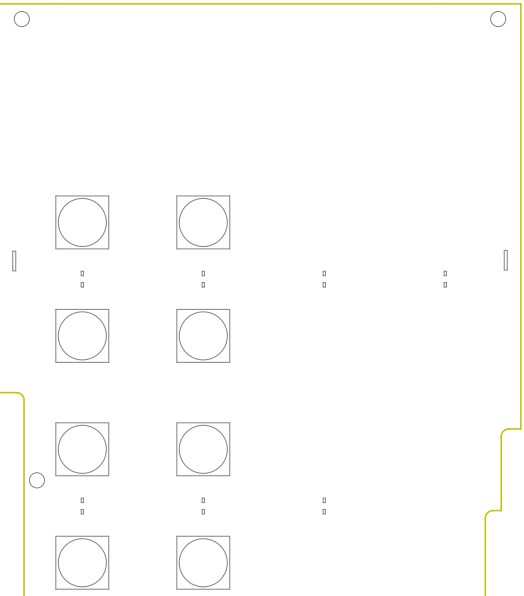
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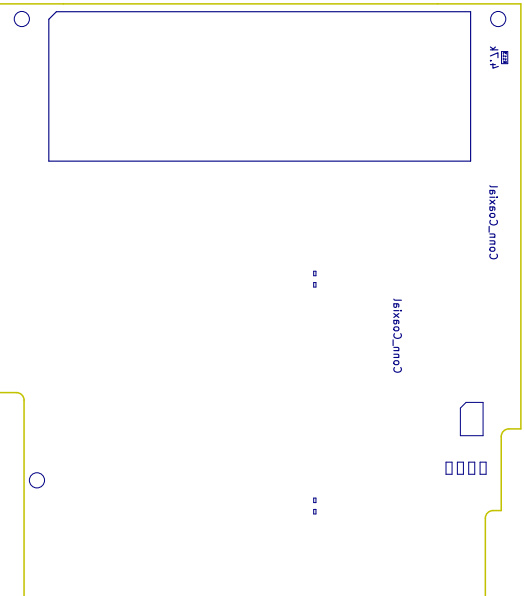
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Sheet:

File: fmc2_sma.kicad_pcb

Title: FMC+ SMA breakout

Size: A4

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