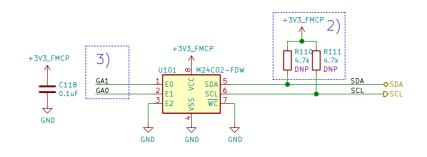NOTES:
1) ZCU111 and ZCU216 have +3V3AUX connected to +3V3
2) I2C (SDA and SCL) is pulled up at the EVA Board to +3V3
3) VITA 57.1— Observation 5.22:
   GA[0] is connected to Address bit [1]
   and GA[1] is connected to Address bit[0].
4) PG_M2C signal is pulled up in VCU118
5) FPGA_CLK needs capacitors in the mezzanine
6) DPCLKs have capacitors in the EVA Board

Mounting Holes
H101 M2.5_Pad
H103 M2.5_Pad
H102 M2.5
H104 M2.5

Fiducials
FID101 Fiducial
FID104 Fiducial
FID102 Fiducial
FID105 Fiducial
FID103 Fiducial
FID106 Fiducial

Heatsink
Heatsink

TestPoints
GND GND

Title: QSolid FMC+ SMA breakout
Size: A2   Date: 2022-11-30   Rev: 1.0
KiCad E.D.A. kicad 5.1.12—84ad8e8a8692ubuntu18.04.1   Id: 1/1

J103G FMCP_VITA57.4
J103A FMCP_VITA57.4
J103B FMCP_VITA57.4
J103C FMCP_VITA57.4
J103D FMCP_VITA57.4
J103E FMCP_VITA57.4
J103F FMCP_VITA57.4