



The schematic diagram shows a 12-bit SAR ADC core (U101) connected to a DAC (M24C02-FDW). The SAR core has pins for GA1, GA0, E1, E0, VCC, SDA, SCL, and WC. The DAC has pins for SDA and SCL. A 3V3_FMCAP capacitor is connected to the SDA and SCL lines. The circuit is powered by +3V3_FMCAP and GND.

