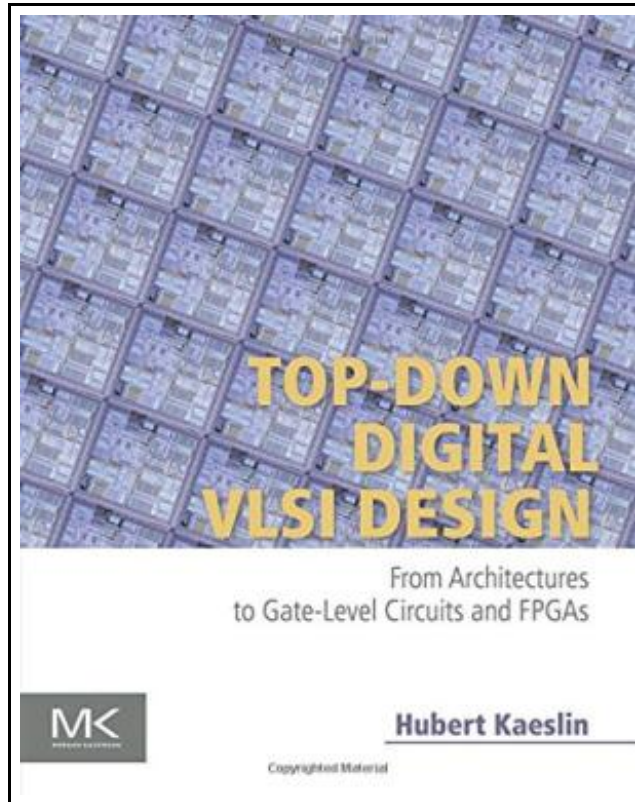# Top-Down Digital VLSI Design



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# TOP-DOWN DIGITAL VLSI DESIGN



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