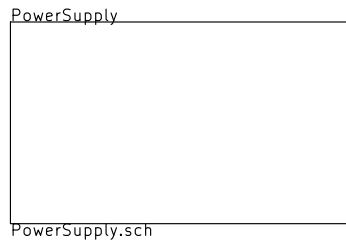
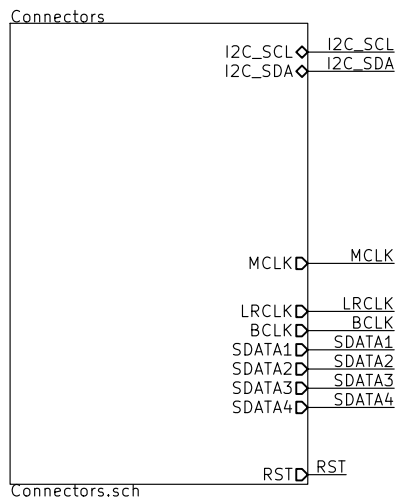
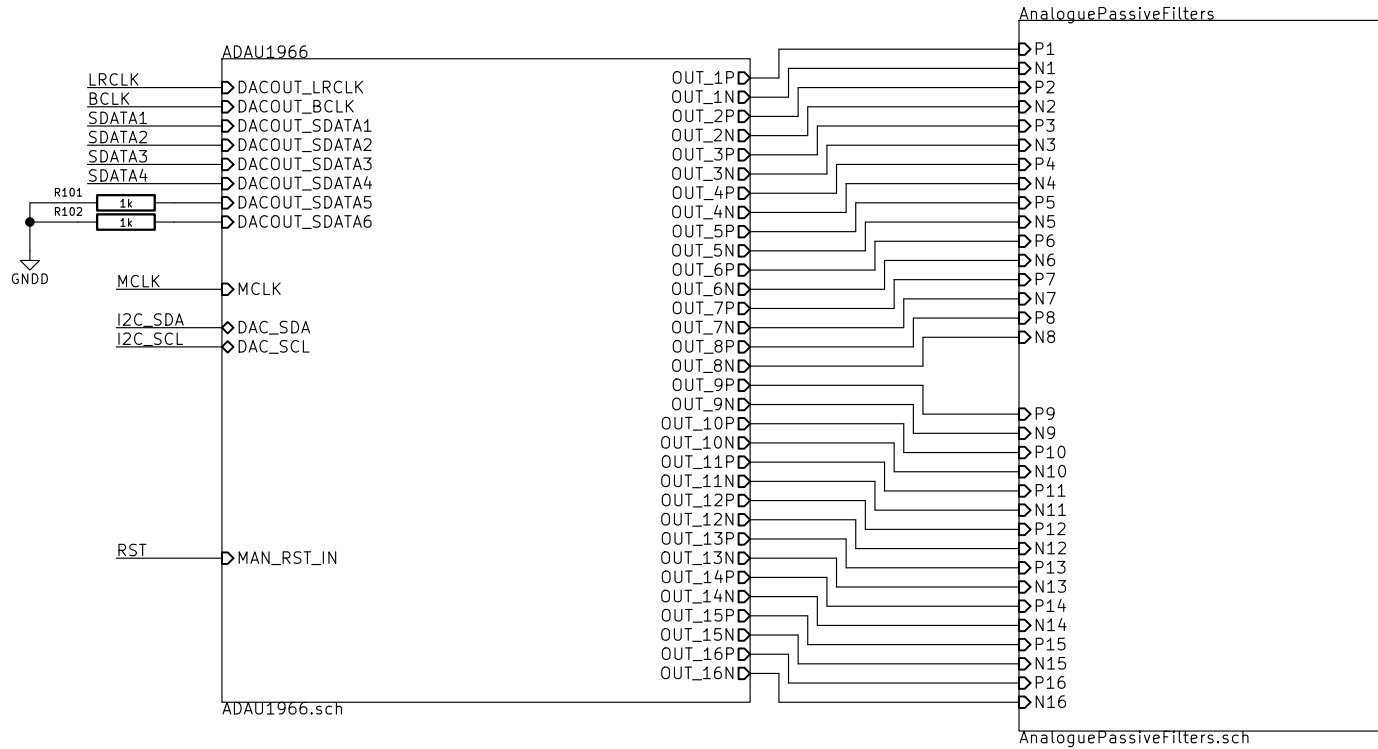


OHDSP ADAU1966 DAC

Note:
All digital I/O is 3V3 only. Use outside this voltage can cause damage.



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Sheet: /
File: DAC-ADAU1966-PassiveFilters.sch

Title: OHDSP DAC-ADAU1966-PassiveFilters

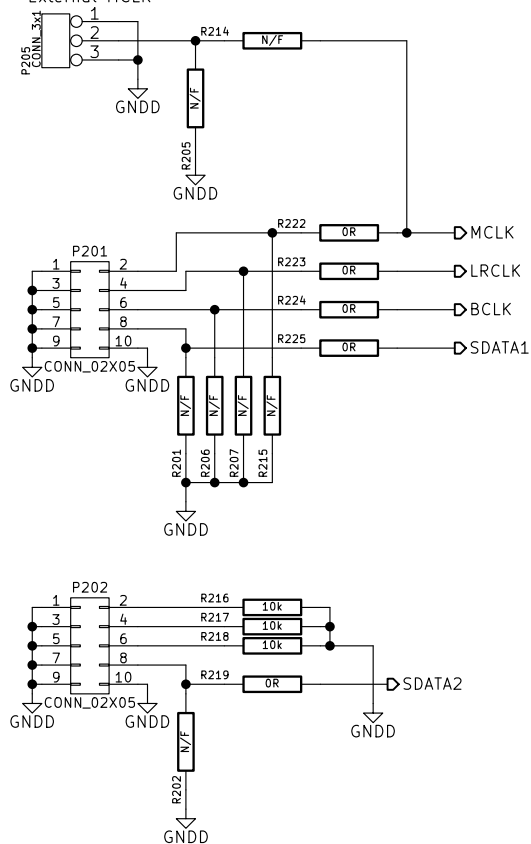
Size: A4 Date: 18 Jan 2016
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Rev: 1.0
Id: 1/5

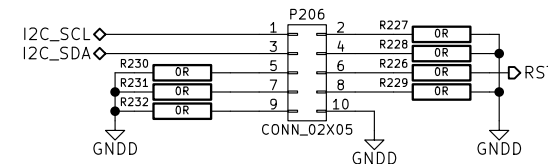
I2S Connectors

For external audio data. Only 4 of the 8 SDATA lines are routed.
Minimum input for 16 channels is 4 TDM4 data streams.

External MCLK



I2C Connector



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Sheet: /Connectors/

File: Connectors.sch

Title: OHDSP DAC-ADAU1966-PassiveFilters

Size: A4 Date: 18 Jan 2016

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Rev: 1.0

Id: 2/5

9V input, this generates 5V



5V input, this generates 3V3 and 2V5
9V must power up before or at the same time as 5V.
Recommend deriving 5V from 9V externally.

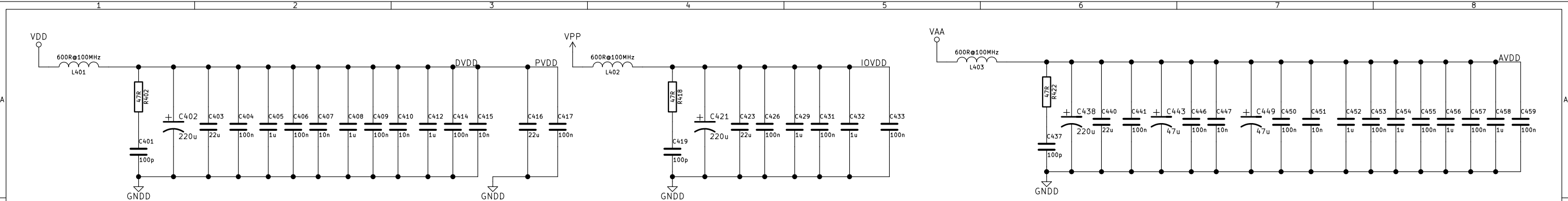


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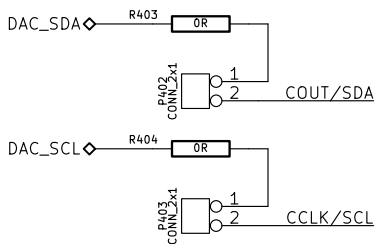
Rev: 1.0

Id: 3/5



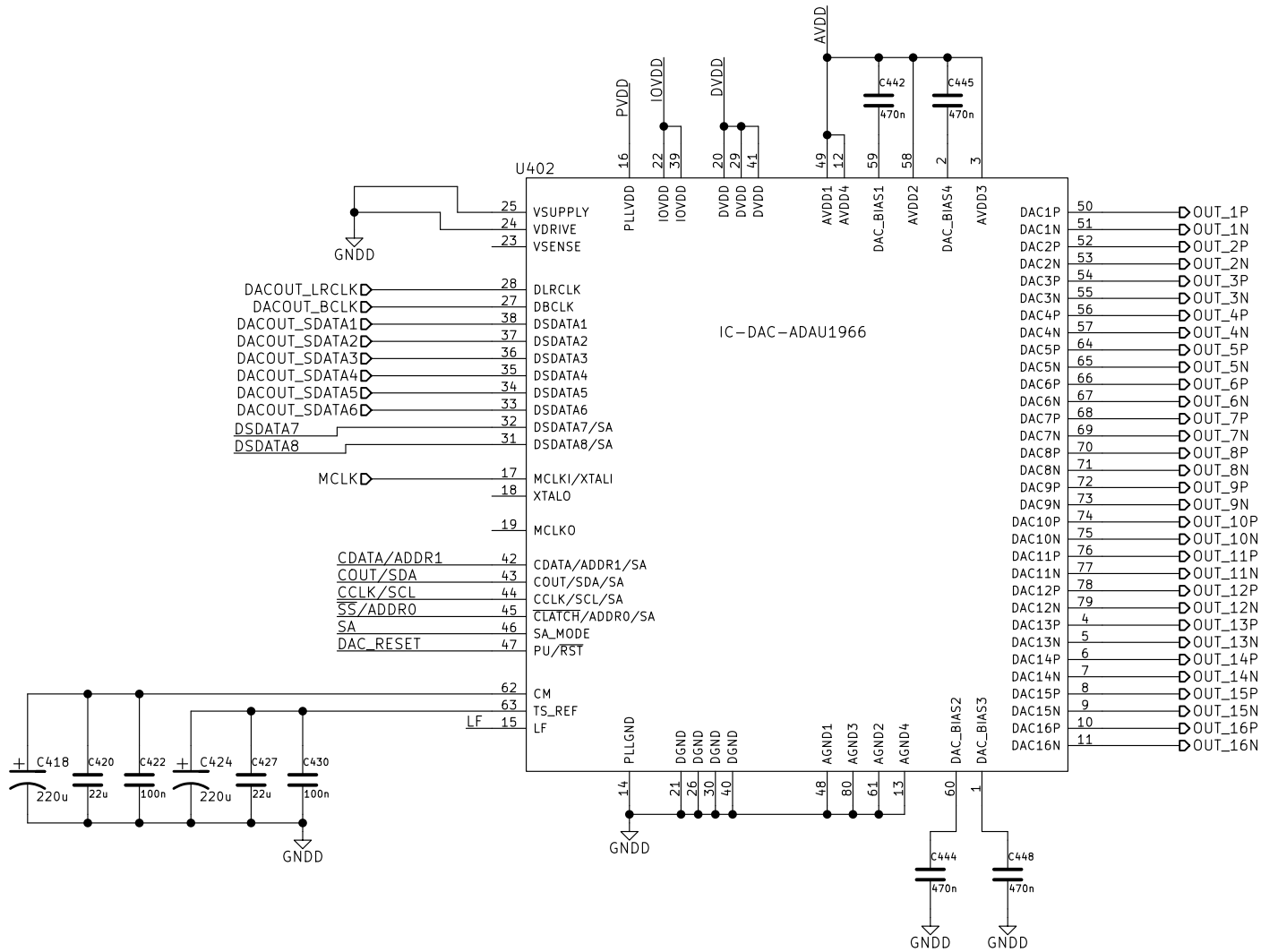
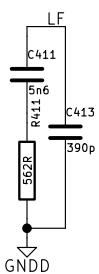
External I2C

External I2C
These jumpers should be open for SA mode



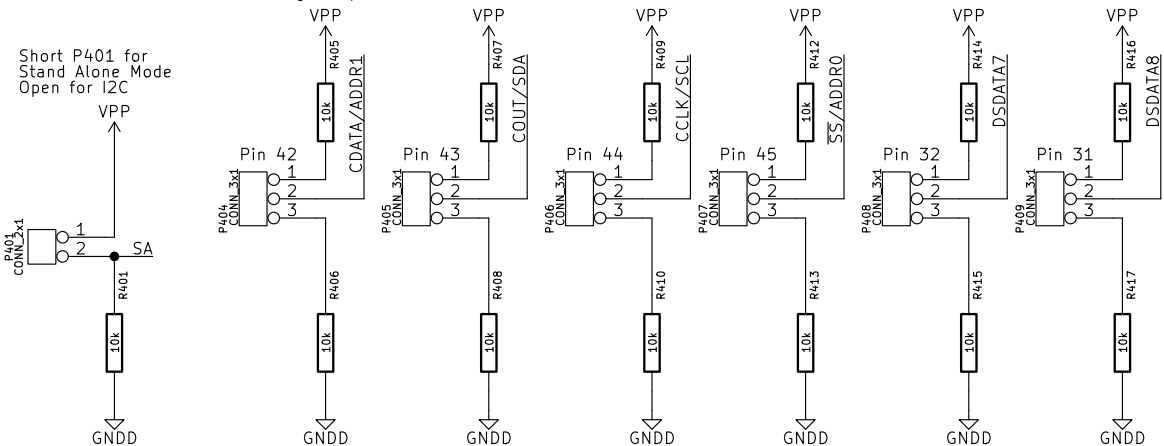
Loop Filter

Values for MCLK mode



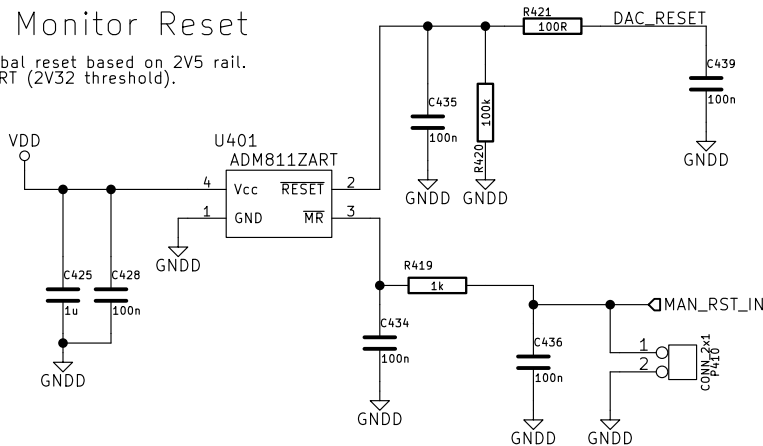
Stand Alone Control

These jumpers choose/configure SA mode settings.
See datasheet for deatiled settings of pins 42-45 and 31-32.

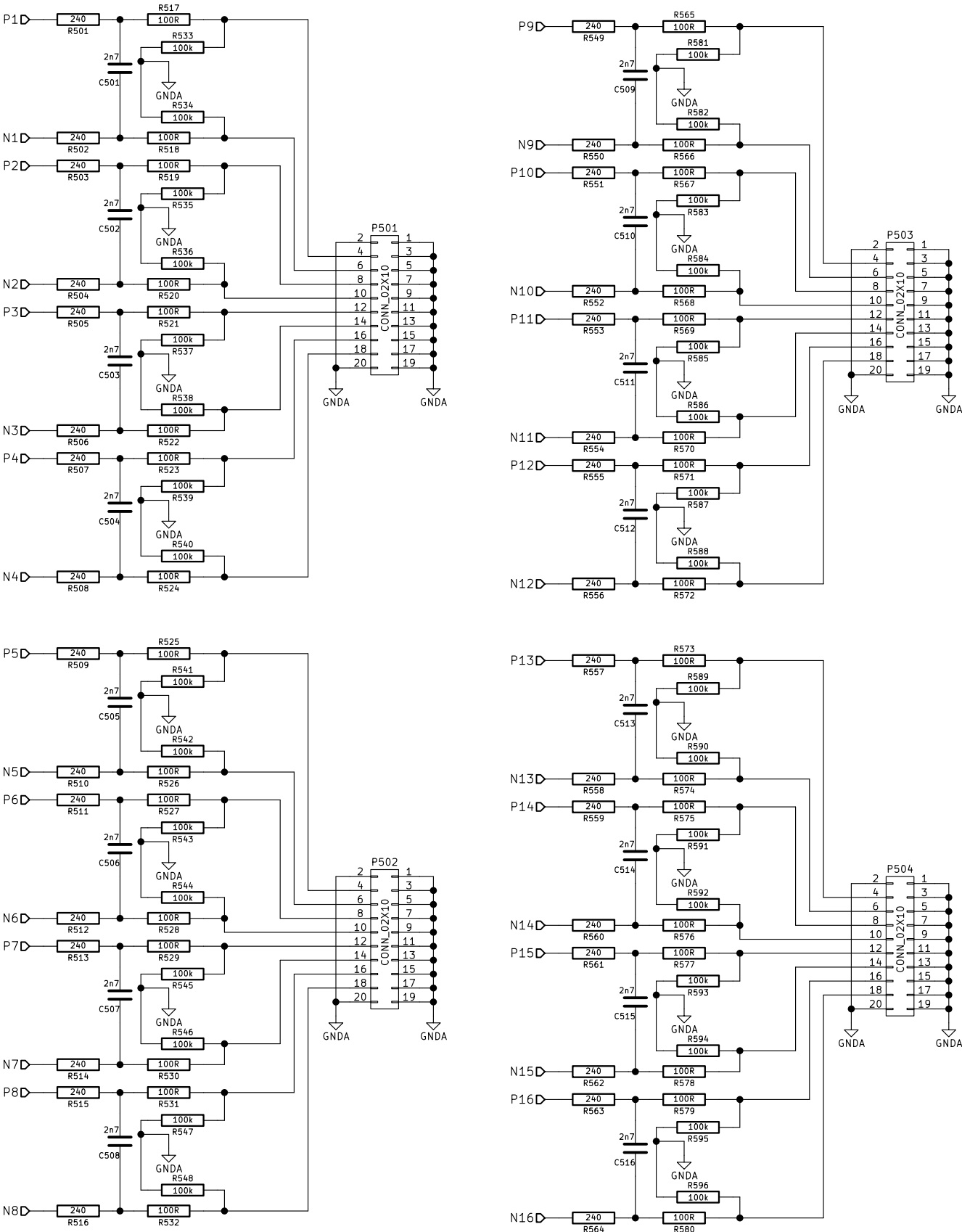


Voltage Monitor Reset

Generate a global reset based on 2V5 rail.
Use ADM811ZART (2V32 threshold).



Differential Outputs with Passive Filters



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Sheet: /AnaloguePassiveFilters/
File: AnaloguePassiveFilters.sch

Title: OHDSP DAC-ADAU1966-PassiveFilters

Size: A3 Date: 18 Jan 2016 Rev: 1.0
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