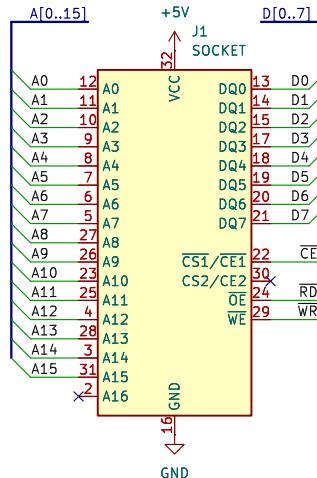
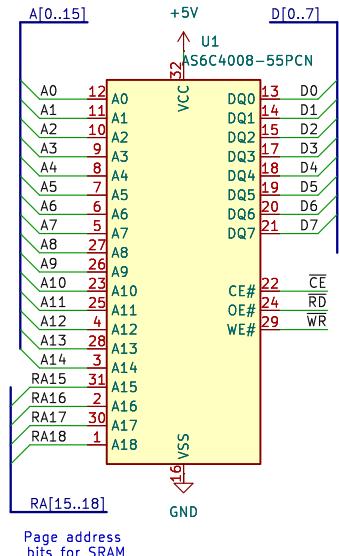
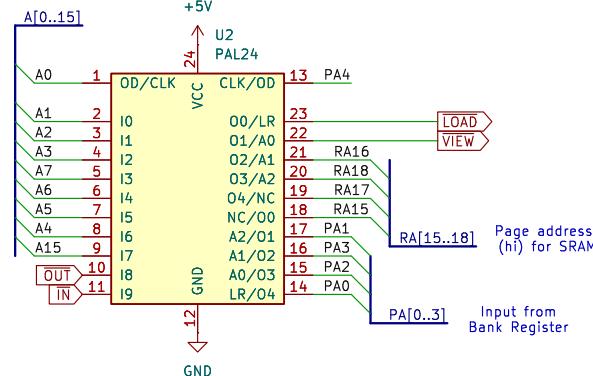
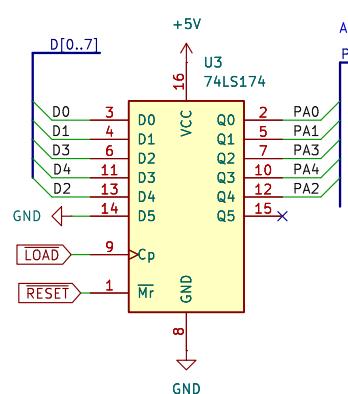
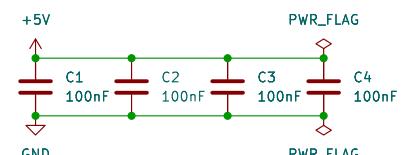
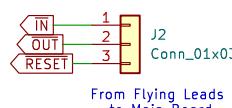
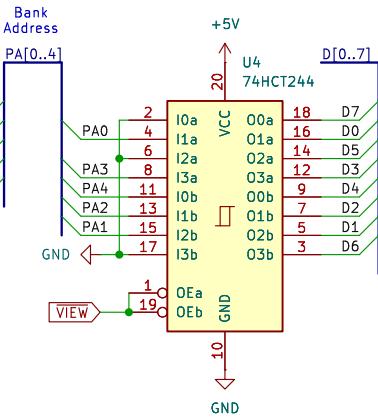


1 2 3 4 5 6

**CONNECTOR TO EXISTING RAM****512KB SRAM****Control Logic****Bank Register****Read Buffer**

512 KB Paged Memory board for TRS-80 Model 1k  
kiwisincebirth

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File: XMEM-M1K.kicad\_sch

**Title: SuperMEM for the Model 1k**

Size: A4      Date: 2025-12-06  
KiCad E.D.A. 9.0.4

Rev: 1.0a  
Id: 1/1