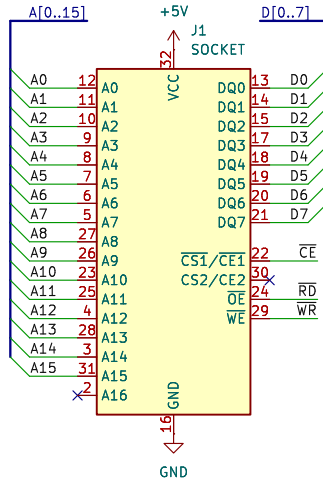
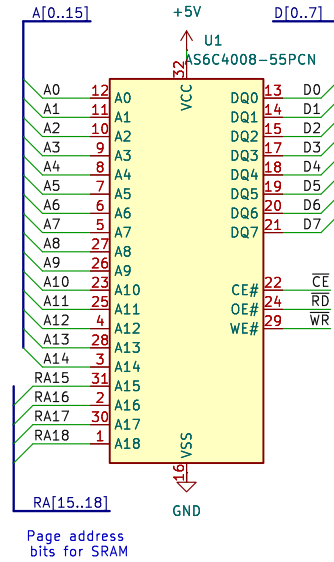


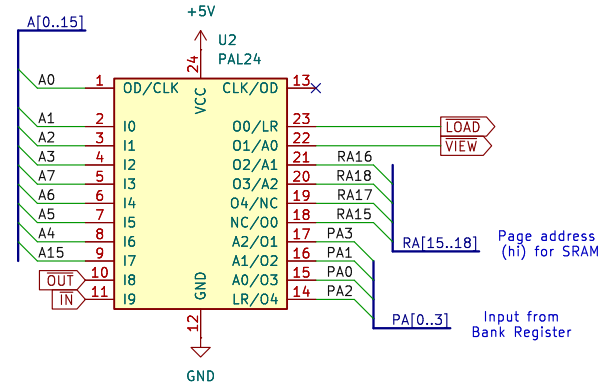
# CONNECTOR TO EXISTING RAM



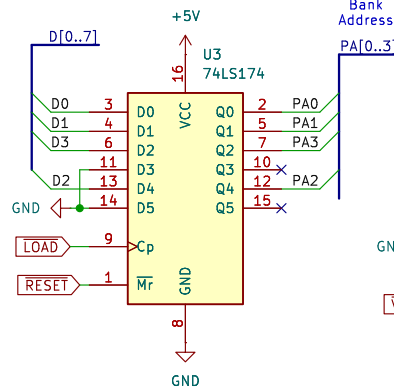
# 512KB SRAM



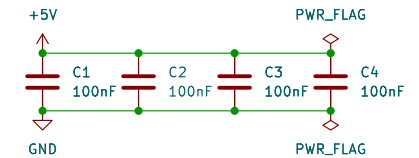
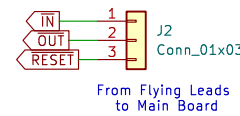
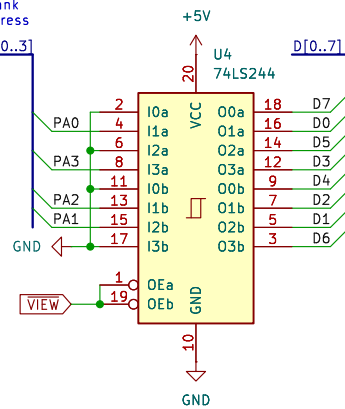
# Control Logic



# Bank Register



# Read Buffer



512 KB Paged Memory board for TRS-80 Model 1k  
kiwisincebirth

Sheet: /  
File: XMEM-M1K.kicad\_sch

**Title: SuperMEM for the Model 1k**

Size: A4 Date: 2025-09-29

KiCad E.D.A. 9.0.4

Rev: 1

Id: 1/1