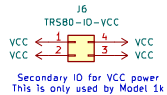
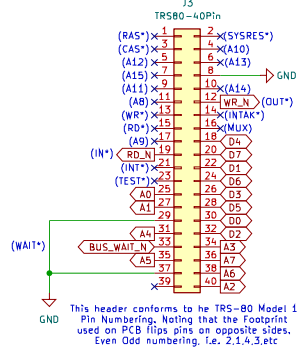
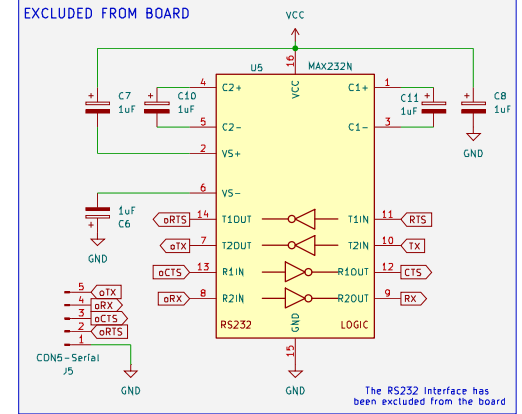
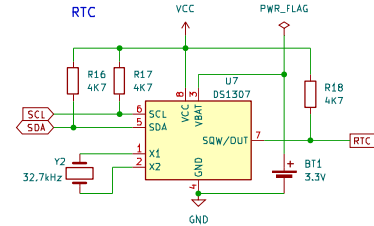
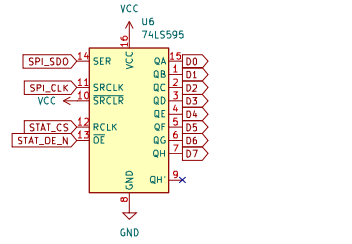
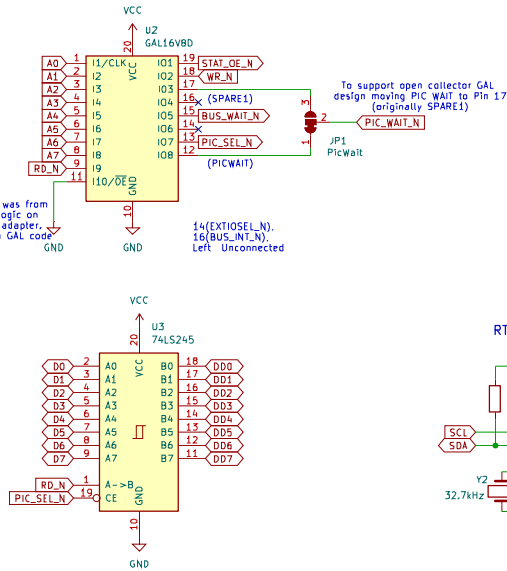


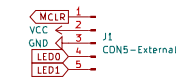
MODEL 1 EXPANSION INTERFACE CONN



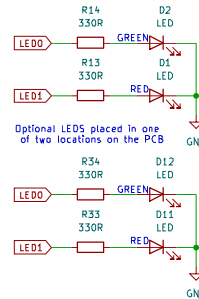
11 (IORQ_N) was from
translator logic on
40->50 HD adapter.
Not needed in GAL code



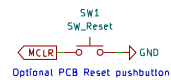
OPTIONAL I/O AND PROGRAMMING HEADER



OPTIONAL LED's



OPTIONAL RESET



ERATTA

EXTIOSEL_N - OUTPUT OF GAL (PIN 14) -> PIN 43 OF HD 50 PIN
NOT CONNECTED TO TRS-80 MODEL 1 BUS - LEFT UNCONNECTED

BUS_INT_N - CONNECTED TO GAL (PIN 16) - NOT USED BY GAL
PIN 39 OF HD 50PIN - NOT CONNECTED TO MODEL 1 BUS
LEFT ALL UNCONNECTED

SPARE2 - CONNECTED ONLY TO PIC (PIN 10) IN SCHEMATIC
IN PHOTOS GOES TO UNUSED PIN - LEFT UNCONNECTED

THREE PINS ON PCB, POSSIBLE JUMPER, N/C IN PHOTOS

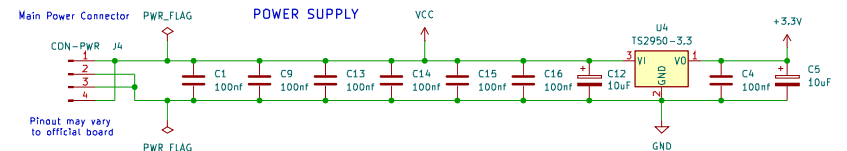
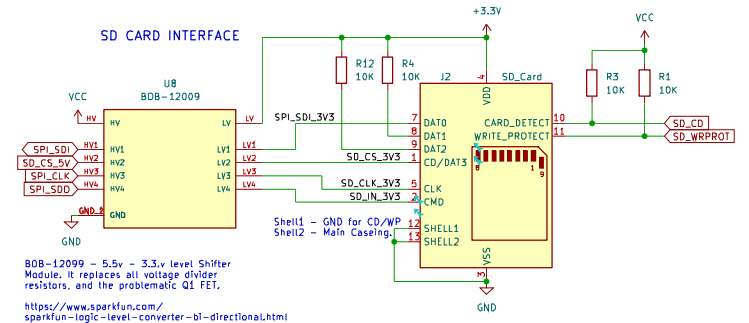
RESET_N - HD Pin 37 (P2 TRS-80) SYSRES* - N/C IN SCHEMA

M1_N - HD Pin 47 (P14 TRS-80) INTAK* - N/C IN SCHEMA

SPARE1 - CONNECTED TO GAL (PIN 17) - BUT NOT USED INTERNALLY
* CONNECTED TO PIC, BUT PROBABLY UN-USED ????

In HD Adapter cable doc, TRS-80 Pins 1 (RAS), and 4 (A1)
are shorted, Both are outputs! IGNORED THIS!

SD CARD INTERFACE



TRS-80 Model 1 compatible with 40 pin connector
kiwinscibirth

Sheet: /
File: FREHD.kicad_sch

Title: FREHD Model 1 - 40 Pin Expansion Connector

Size: A3 Date: 2025-05-11 Rev: 1.0
KiCad E.D.A. B.0.8 Id: 1/1