

# Real Time and Embedded Systems (RTES)

## INDIVIDUAL ASSIGNMENT ON

### “Real Time and Embedded System Architecture”

The assignment is designed to take a closer look at the overview of Real Time and Embedded System Architecture, Design and Development Lifecycle Model, methodologies for creating RTES, and its pragmatic approach and major stages of creating a real time and embedded System Architecture.

Thus, each student required to prepare a paper not more than 5-7 pages on the given topic.

#### Submission criteria:

- Size 5-7 pages (1.5 spacing in Times New Roman font).
- Rename file with <YOURFULLNAME> before submitting.
- Copying others work will lead to rejection, it will be checked.
- Clarity of explanations and discussions including language used will be checked.
- Include at least 3 references.
- Due date: April 21 -28, 2024 (only softcopy).
- Submit to: Addisu Yohannes < yaddis07@gmail.com >.
- Early submission encouraged and late submission is forbidden.