

Source/Destination register

Base register

Load/Store bit

0 = Store to memory
1 = Load from memory

Write-back bit

0 = no write-back
1 = write address into base

Byte/Word bit

0 = transfer word quantity
1 = transfer byte quantity

Up/Down bit

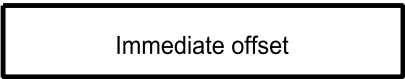
0 = down; subtract offset from base
1 = up; add offset to base

Pre/Post indexing bit

0 = post; add offset after transfer
1 = pre; add offset before transfer

Immediate offset

0 = offset is an immediate value



Unsigned 12 bit immediate offset

1 = offset is a register



shift applied to Rm

Offset register

Condition field