TOSHIBA Photocoupler GaAlAs IRED + Photo IC

# **TLP351**

Inverter for Air Conditioner
IGBT/Power MOS FET Gate Drive
Industrial Inverter

The TOSHIBA TLP351 consists of a GaAlAs light emitting diode and a integrated photodetector.

This unit is 8-lead DIP package.

TLP351 is suitable for gate driving circuit of IGBT or power MOS FET. Especially TLP351 is capable of "direct" gate drive of lower Power IGBTs.

- Peak output current: ±0.6 A (max)
- Guaranteed performance over temperature: -40 to 100°C
- Supply current: 2 mA (max)
- Power supply voltage: 10 to 30 V
- Threshold input current :  $I_F = 5 \text{ mA (max)}$
- Switching time (t<sub>pLH</sub>/t<sub>pHL</sub>): 700 ns (max)
- Common mode transient immunity: ±10 kV/μs
- Isolation voltage: 3750 Vrms
- UL approved: UL1577, File No.E67349
- cUL approved :CSA Component Acceptance Service No. 5A, File No.E67349
- Option(D4)

VDE Approved: DIN EN60747-5-5

 $\label{eq:maximum operating Insulation Voltage : 890 $V_{PK}$} \\ \mbox{Highest Permissible Over Voltage} : 4000 $V_{PK}$ \\ \mbox{: 4000 $V_{PK}$} \\ \mbox{: 4000 $V_{PK}$$ 

(Note): When a EN60747-5-5 approved type is needed, Please designate "Option(D4)"

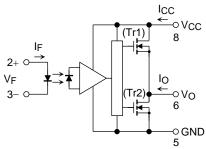
# Unit: mm 8 7 6 5 92 0 + 1 92

Weight: 0.54 g (typ.)

## **Truth Table**

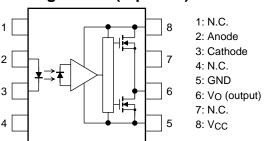
Input	LED	Tr1	Tr2	Output
Н	ON	ON	OFF	Н
L	OFF	OFF	ON	L

### **Schematic**



A 0.1  $\mu$ F bypass capacitor must be connected between pin 8 and 5.

# Pin Configuration (top view)



Start of commercial production 2002-05

### **Absolute Maximum Ratings (Ta = 25°C)**

	Characteristics	Symbol	Rating	Unit	
	Forward current	lF	20	mA	
	Forward current derating (Ta ≥ 85°C)		ΔΙϝ/ΔΤα	-0.54	mA/°C
	Peak transient forward current	(Note 1)	IFP	1	Α
ED.	Reverse voltage		VR	5	V
	Power Dissipation		PD	40	mW
	Power Dissipation Derating (Ta ≥ 85°C)		ΔP <sub>D</sub> /°C	-1.0	mW/°C
	Junction temperature		Tj	125	°C
	"H" peak output current		Іорн	-0.6	Α
	"L" peak output current	(Note 2)	IOPL	0.6	Α
or	Output voltage		Vo	35	V
Detector	Supply voltage		Vcc	35	V
ă	Output Power Dissipation		Ро	260	mW
	Output Power Dissipation Derating (Ta ≥ 8	35°C)	ΔPo /°C	-6.5	mW/°C
	Junction temperature		Tj	125	°C
Oper	ating frequency	(Note 3)	f	25	kHz
Storage temperature range			T <sub>stg</sub>	−55 to 125	°C
Operating temperature range		Topr	-40 to 100	°C	
Lead	soldering temperature (10 s)	(Note 4)	T <sub>sol</sub>	260	°C
Isola	tion voltage (AC, 1 minute, R.H. ≤ 60%)	(Note 5)	BVs	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width PW  $\leq$  1  $\mu$ s, 300 pps

Note 2: Exponential waveform pulse width PW  $\leq$  10  $\mu$ s, f  $\leq$  15 kHz

Note 3: Exponential waveform IOPH  $\leq -0.4$  A ( $\leq 2.0$   $\mu$ s), IOPL  $\leq +0.4$  A ( $\leq 2.0$   $\mu$ s), Ta = 100°C

Note 4: It is 2 mm or more from a lead root.

Note 5: Device considered a two terminal device: pins 1, 2, 3 and 4 shorted together, and pins 5, 6, 7 and 8 shorted together.

## **Recommended Operating Conditions**

Characteristics		Symbol	Min	Тур.	Max	Unit
Input current, ON	(Note 7)	IF (ON)	7.5	_	10	mA
Input voltage, OFF		VF (OFF)	0	_	0.8	V
Supply voltage		Vcc	10	_	30	V
Peak output current		IOPH/IOPL	_	_	±0.2	Α
Operating temperature		Topr	-40	_	100	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note 7: Input signal rise time (fall time)  $< 0.5 \mu s$ 

2 2017-11-29

# Electrical Characteristics (Ta = -40 to 100°C, unless otherwise specified)

Characteristics		Symbol	Test Circuit	Test C	ondition	Min	Тур.*	Max	Unit
Forward voltage		VF	_	$I_F = 5 \text{ mA}, Ta = 25^{\circ}C$		_	1.55	1.70	V
Temperature coefficient of forward voltage		ΔVϝ/ΔΤα	_	I <sub>F</sub> = 5 mA		_	-2.0	_	mV/°C
Input reverse current		I <sub>R</sub>	_	V <sub>R</sub> = 5 V, Ta = 25°C		_	_	10	μА
Input capacitance		Ст	_	$V=0\ V$ , $f=1\ MH$	z,Ta = 25°C	_	45	_	pF
	"H" Level	IOPH1	1	V <sub>C</sub> C = 15 V	V <sub>8-6</sub> = 4 V	_	-0.4	-0.2	A V
Output current (Note 8)	n Level	I <sub>OPH2</sub>		$I_F = 5 \text{ mA}$	V <sub>8-6</sub> = 10 V	_	-0.67	-0.4	
	"L" Level	I <sub>OPL1</sub>	2	V <sub>CC</sub> = 15 V I <sub>F</sub> = 0 mA	V <sub>6-5</sub> = 2 V	0.2	0.35	5 —	
		I <sub>OPL2</sub>	~		V <sub>6-5</sub> = 10 V	0.4	0.63	_	
	"H" Level	Voн	3	- V <sub>CC</sub> = 10 V	$I_O = -100 \text{ mA},$ $I_F = 5 \text{ mA}$	6.0	8.5	_	
Output voltage	"L" Level	V <sub>OL</sub>	4		$I_O = 100 \text{ mA}, \ V_F = 0.8 \text{ V}$	_	0.4	1.0	
Complete	"H" Level	Icch	5	5 V <sub>CC</sub> = 10 to 30 V V <sub>O</sub> open	IF = 10 mA	_	1.4	2.0	mA
Supply current	"L" Level	ICCL	6		I <sub>F</sub> = 0 mA	_	1.3	2.0	
Threshold input current	L → H	I <sub>FLH</sub>	_	V <sub>CC</sub> = 15 V, V <sub>O</sub> > 1 V		_	2.5	5	mA
Threshold input voltage	H → L	V <sub>FHL</sub>	_	V <sub>CC</sub> = 15 V, V <sub>O</sub> < 1 V		0.8	_	_	V
Supply voltage		Vcc	_	-	_	10	_	30	V

<sup>\*:</sup> All typical values are at Ta = 25°C

Note 8: Duration of IO time  $\leq$  50  $\mu$ s

Note 9: This product is more sensitive than the conventional product to static electricity (ESD) because of a lowest power consumption design.

General precaution to static electricity (ESD) is necessary for handling this component.

# Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Conditions	Min	Тур.	Max	Unit
Capacitance input to output	Cs	$V_S = 0V, f = 1MHz$ (Note5)		1.0	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60% (Note5	) 1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
	BVS	AC,1 minute	3750	_	_	V
Isolation voltage		AC,1 second, in oil	_	10000	_	V <sub>rms</sub>
		DC,1 minute, in oil	_	10000	-	Vdc

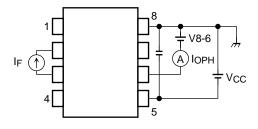
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# Switching Characteristics (Ta = -40 to $100^{\circ}$ C, unless otherwise specified)

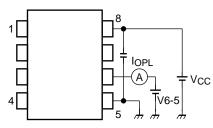
Characteristics		Symbol	Test Circuit	Test Condition		Min	Тур.*	Max	Unit
	L → H	t <sub>pLH</sub>		$R_g = 47 \Omega$	$I_F = 0 \rightarrow 5 \text{ mA}$	100	ı	700	
Propagation delay time	H → L	t <sub>pHL</sub>			$I_F = 5 \rightarrow 0 \text{ mA}$	100	ı	700	ns
Propagation delay difference between any two parts or channels		PDD  t <sub>pHL</sub> -t <sub>pLH</sub>	7	$VCC = 30 \text{ V},$ $R_g = 47 \Omega$ $C_g = 3 \text{ nF}$		-500	1	500	ns
Output rise time (10-90%)		t <sub>r</sub>		VCC = 30 V	$I_F = 0 \rightarrow 5 \text{ mA}$		50	-	
Output fall time (90-10%)		t <sub>f</sub>		$R_g = 47 \Omega$ $C_g = 3 nF$	$I_F = 5 \rightarrow 0 \text{ mA}$	-	50	_	ns
Common mode transient immunity at high level output  Common mode transient immunity at low level output		СМН	- 8	V <sub>CM</sub> = 1000 V <sub>p</sub> -p	$I_F = 5 \text{ mA}$ $V_{O \text{ (min)}} = 26 \text{ V}$	-10000	ı	ı	\//c
		CML		Ta = 25°C V <sub>CC</sub> = 30 V	I <sub>F</sub> = 0 mA V <sub>O (max)</sub> = 1 V	10000	_	_	V/μs

<sup>\*:</sup> All typical values are at Ta = 25°C

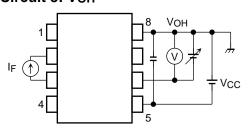
### **Test Circuit 1: IOPH**



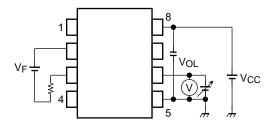
Test Circuit 2: IOPL



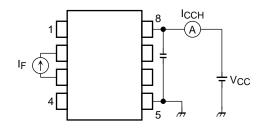
Test Circuit 3: Voн



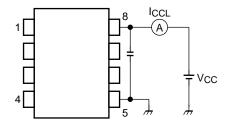
**Test Circuit 4: Vol** 



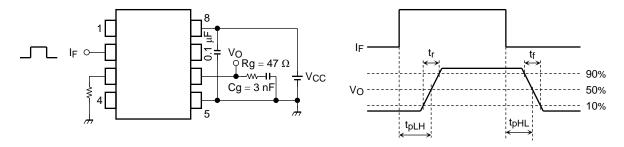
**Test Circuit 5: ICCH** 



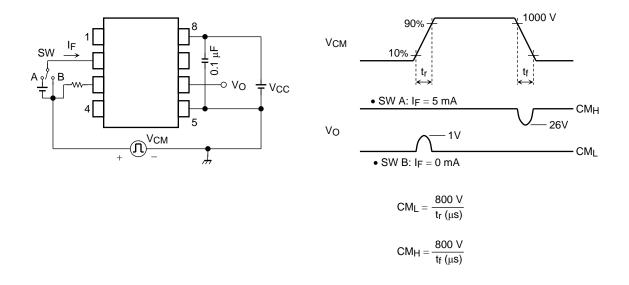
**Test Circuit 6: ICCL** 



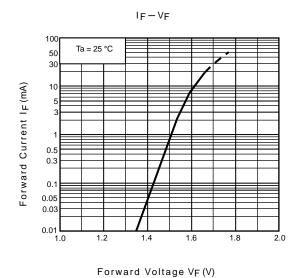
# Test Circuit 7: tpLH, tpHL, tr, tf, PDD

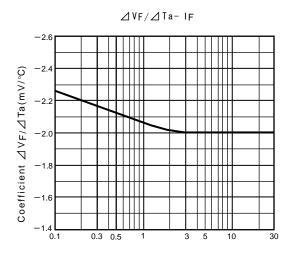


# Test Circuit 8: CMH, CML

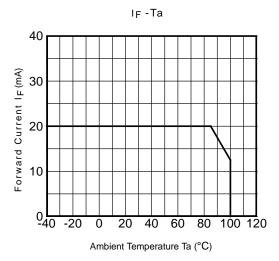


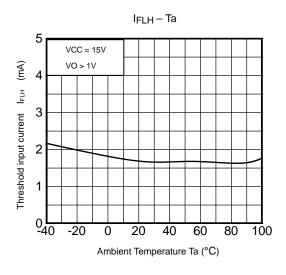
 $\mathrm{CM_L}\left(\mathrm{CM_H}\right)$  is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.

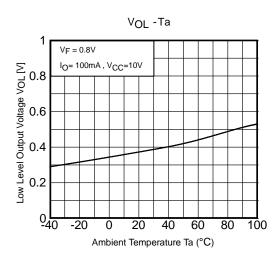


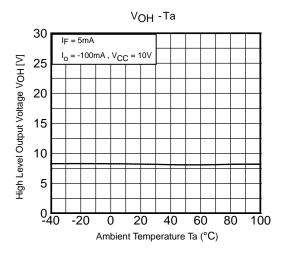


Forward Current I<sub>F</sub> (mA)



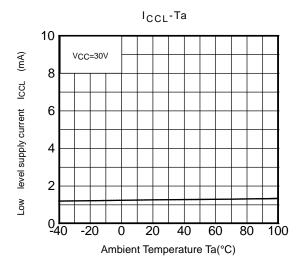


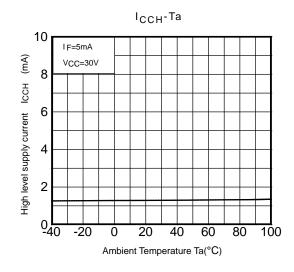


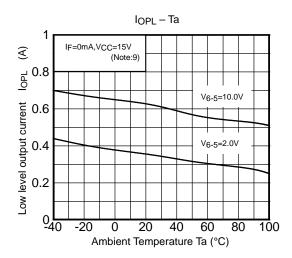


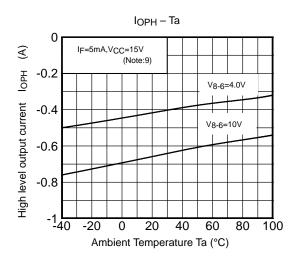
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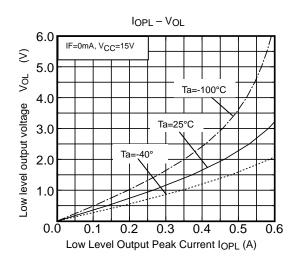
<sup>\*:</sup> The above graphs show typical characteristics.

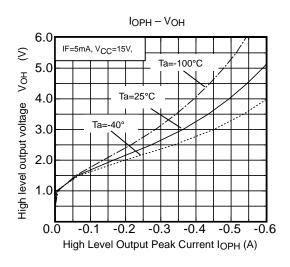




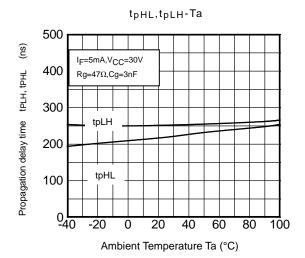








<sup>\*:</sup> The above graphs show typical characteristics.



 $<sup>\</sup>ast \raisebox{-0.15ex}{:}$  The above graphs show typical characteristics.

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