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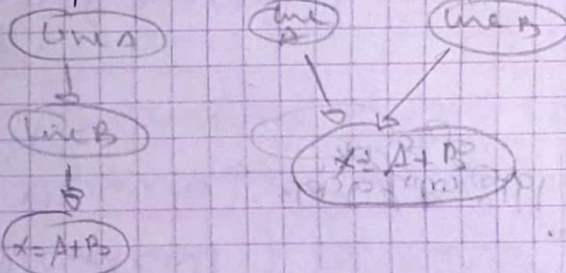
Processus: programme en cours exécution

task et thread:

ensemble exécutif élémentaire  
ayant cohérence

Processus

Sequential Parallel



thread:

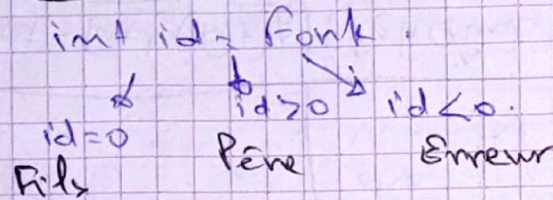
défini comme un fil d'exécution

appartenant à un processus.

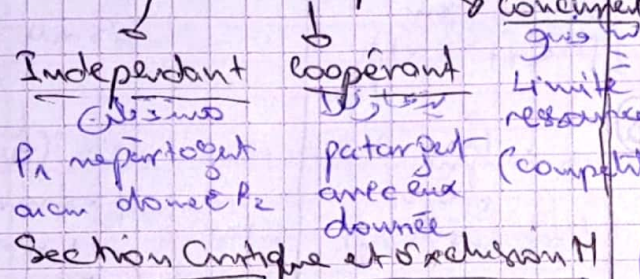
Creation thread Java

peut être créé en étendant  
 classe Thread ou implémentant  
 runnable (réécrit la méthode run)

Creation thread Fork



Interaction processus

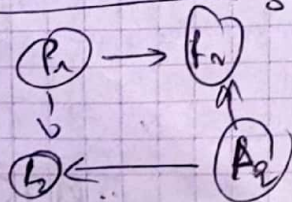


Problème: Accès Concurrent ressource

SC: suite d'instructions où  
 ressource critique est accessible  
 par un seul

On dit que la ressource est en  
 exclusion et les processus EM  
 (exclusion mutuelle)

interblocage



Realisation EM

Propriété de Dijkstra

Exclusion Mutuel:  $\forall E, mP \leq 1$

Absence I.B:  $\forall E, mP \leq 1$

Progression:  $\forall E, mP \leq 1$

Absence processus livrés

blocage P hors se ne doit  
 pas empêcher autre autre se.

se doit être même pour  
 tous proces

sol Attente Passive

Semaphore (sol se).

Semaphore initial = 1 (pour  
 protéger variable)

P: demander pignon ( $e(s) = e(s) - 1$ )

V: libérer pignon ( $e(s) = e(s) + 1$ )

(restaurant TD 3)

⑦



⑤ RDU:

```
int cpt;
Semaphore S = 0;
Semaphore mutex = 1;
const N = 10;

P(mutex);
cpt++;
if (cpt < N) {
    V(mutex);
    P(S);
}
else {
    while (cpt < N) {
        V(S);
        cpt--;
    }
}
```

Semaphore mutex = 1;

DLU:

```
P(mutex);
if (S) {
    mlatt++;
    V(mutex);
    P(S);
}
else {
    ml++;
    V(mutex);
}
```

Lecture

DLU:

```
P(mutex);
if (ml < 0) {
    mlatt++;
    V(mutex);
    P(S);
}
else {
    S = true;
    V(mutex);
}
```

FLU:

```
P(mutex);
if (ml == 0) {
    if (mlatt > 0) {
        V(mutex);
        V(S);
    }
    V(mutex);
}
```

Ecriture

FLU (voit Priorité)

```
P(mutex);
S = false;
if (mlatt > 0) {
    mlatt--;
    ml++;
    S = true;
}
else {
    if (mlatt > 0) {
        mlatt--;
        S = true;
        V(S);
    }
    V(mutex);
}
```

Semaphore mutex, RFP = 1;

Semaphore S = 0;

DLU:

```
P(RFP);
P(mutex);
if (S) {
    P(S);
    V(mutex);
    P(S);
}
else {
    ml++;
    V(mutex);
    V(RFP);
}
```

FLU:

```
P(mutex);
S = false;
P(RFP);
P(S);
V(S);
V(mutex);
```

FLU:

```
P(mutex);
ml--;
if (ml == 0) {
    P(S);
    P(mutex);
    if (S || ml > 0) {
        P(S);
        P(mutex);
    }
    S = true;
    V(mutex);
    P(S);
    P(mutex);
}
```

FLU:

```
P(mutex);
S = true;
V(mutex);
V(RFP);
```

lect-Red (RFP): (chaîne Semaphore  
n (RFP))

DLU:

```
int ml, = 0;
int mlatt = mlatt + 20;
bool S;
Semaphore S;
```

① (Promis Redact)



# ① Server Hello Infix

```

SH() {
P(SH);
write("H");
V(SE);
}
    
```

```

SE() {
P(SE);
write("E");
V(SL);
V(SL);
}
    
```

```

SL() {
P(SL);
P(SL);
write("L");
V(SP);
V(SP);
}
    
```

```

SP() {
P(SP);
P(SP);
write("P");
V(SH);
}
    
```

# ② medecin : U + F + H

```

bool medOupe = false;
Semaphore SH = 0;
" SF = 0;
" SH = 0;
" mutex = 1;
Uatt & fAtt = UAtt = 0;
    
```

```

EU(): E F() EH()
P(mutex);
if (medOupe == True) {
Uatt++;
V(mutex);
P(SH);
}
else {
medOupe = False;
V(mutex);
}
    
```

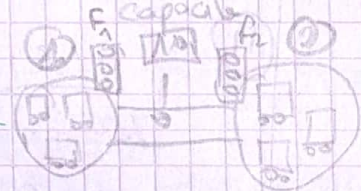
```

SU() = SF() = SH()
P(mutex);
Uatt--;
if (Uatt > 0) {
Uatt--;
V(SU);
}
else {
if (mAtt > 0) {
Patt--;
V(SF);
}
else {
if (mAtt > 0) {
mAtt--;
V(SH);
}
else {
medOupe = false;
V(mutex);
}
}
}
    
```

# Pont avioie Unique

```

DT1(): DT2()
P(mutex);
if (P1 = 'R' / mAtt > 0) {
}
mAtt++;
V(mutex);
P(DT1);
}
else if (mAtt > 0) {
P2 = 'R';
}
    
```



Variable

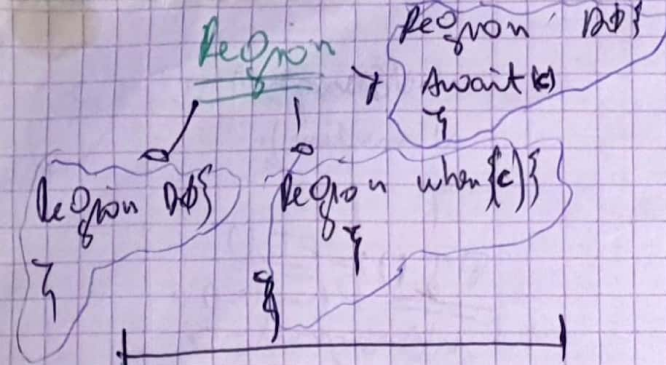
```

P1 = 'R';
P2 = 'R';
mAtt = 0;
semaphore DT1 = 0;
" DT2 = 0;
mutex = 1;
const N = 10;
if (mAtt, mAtt2 > 0) V(mutex);
    
```

```

nbVcarpoint++;
V(mutex);
}
FT1(): FT2()
P(mutex);
nbVcarpoint--;
if (mAtt > 0) {
mAtt--;
nbVcarpoint++;
V(DT1);
}
else {
if (nbVcarpoint == 0) {
P2 = 'R';
if (mAtt2 > 0) {
P1 = 'R';
}
int k = min(P1, P2);
while (nbVcarpoint < k) {
mAtt--;
nbVcarpoint++;
V(SATT);
}
}
}
    
```





① RDV

```
int shared cpt = 0;
Region cpt do {
    cpt++;
    Await (cpt == N);
}
```

② lect Red

```
struct lectRed {
    int nl, nlatt, nlattt;
    bool E;
}
```

shared int LR: {0, 1, 0, False};  
 (LR.nl = 0 | LR.nlatt = 0 | LR.nlattt = 0 | LR.E = False)

④

DLC :  
 Region LR where {!E} do  
 {  
 nl++;  
 }  
 Lecture  
FLC  
 Region LR do  
 {  
 nl--;  
 }

DLC  
 Region LR do {  
 nl--;  
 }

③ Priorite lecture  
DLC  
 Region LR do {  
 nlattt++;  
 Await (E = False);  
 nlattt--;  
 nl++;  
 }  
 Lecture  
FLC  
 Region LR do {  
 nl--;  
 }

DLC  
 Region LR where {E = False} do  
 {  
 nl = 0; // (nlattt = 0)  
 E = true;  
 }  
 Ecrire  
 Region LR do {  
 E = False;  
 }

med-Opere = True;

DEU  
 Region LR where  
 {!E || nl == 0}  
 {  
 E = true;  
 }  
 Ecrire  
FLC  
 Region LR do {  
 E = False;  
 }

DEU  
 Region LR where {E = False} do  
 {  
 nl = 0; // (nlattt = 0)  
 E = true;  
 }  
 Ecrire  
 Region LR do {  
 E = False;  
 }

DEU  
 Region LR where {E = False} do  
 {  
 nl = 0; // (nlattt = 0)  
 E = true;  
 }  
 Ecrire  
 Region LR do {  
 E = False;  
 }

DEU  
 Region LR where {E = False} do  
 {  
 nl = 0; // (nlattt = 0)  
 E = true;  
 }  
 Ecrire  
 Region LR do {  
 E = False;  
 }

med-Opere = True;

⑤ Cabinet Medical  
 struct CabMed {  
 bool medOpere;  
 int nlatt = nfact = nHatt;  
 }  
 shared CabMed CB;  
 CB.medOpere = False;  
 CB.nlatt = nfact = nHatt = 0;

FLC = FLC = FLC  
 Region CB do {  
 CB.nlattt++;  
 Await (CB.medOpere = False);  
 CB.nlattt--;  
 CB.medOpere = True;  
 }  
 Ecrire  
 Region CB do {  
 CB.medOpere = False;  
 }

FLC = FLC = FLC  
 Region CB do {  
 CB.medOpere = False;  
 }  
 Region CB do {  
 nlattt++;  
 Await (medOpere = False) || (nl = 0) || (nfact = 0) || (nHatt = 0);  
 }