FPGA Assignment

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November 3, 2022

1 Abstract

-Through this manual, we learn how to display sum of two numbers on LCD using fpga $\,$

2 components

components	values	quantity
Vaman Board	-	1
LCD	16x2	1
bread board	-	1
jump wires	-	20

TABLE I

step 1

-Connect the 5V pin of the Vaman to an extreme pin of the Breadboard Let this pin be V $\rm cc$.

step 2

-Connect the GND pin of the Vaman to the opposite extreme pin of the Breadboard.

step 3

-plug the LCD in fig.7 to breadboard

step 4

-make the connections of vaman board and LCD according to table II $\,$

Pygmy	LCD pins	LCD pin	LCD pin
		label	Descrip-
			tion
GND	1	GND	
5V	2	Vcc	
GND	3	Vee	Contrast
10	4	RS	Register
			Select
GND	5	R/W	read/write
9	6	EN	Enable
14	11	DB4	Serial
			connec-
			tion
13	12	DB5	Serial
			connec-
			tion
12	13	DB6	Serial
			connec-
			tion
11	14	DB7	Serial
			connec-
			tion
5V	15	LED+	Backlight
GND	16	LED-	Backlight

CD 16X2 VCCC VCCC VEEE RS RW EN EN DB0 DB1 DB2 DB3 DB4 DB82 DB82 DB82 LED+ LED+ LED LED-

CODE

-After connection write the following verilog code https://github.com/kkousar/KOUSAR_FWC/tree/main/fpga/cod