

Preethis ROIC analysis

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1 setup

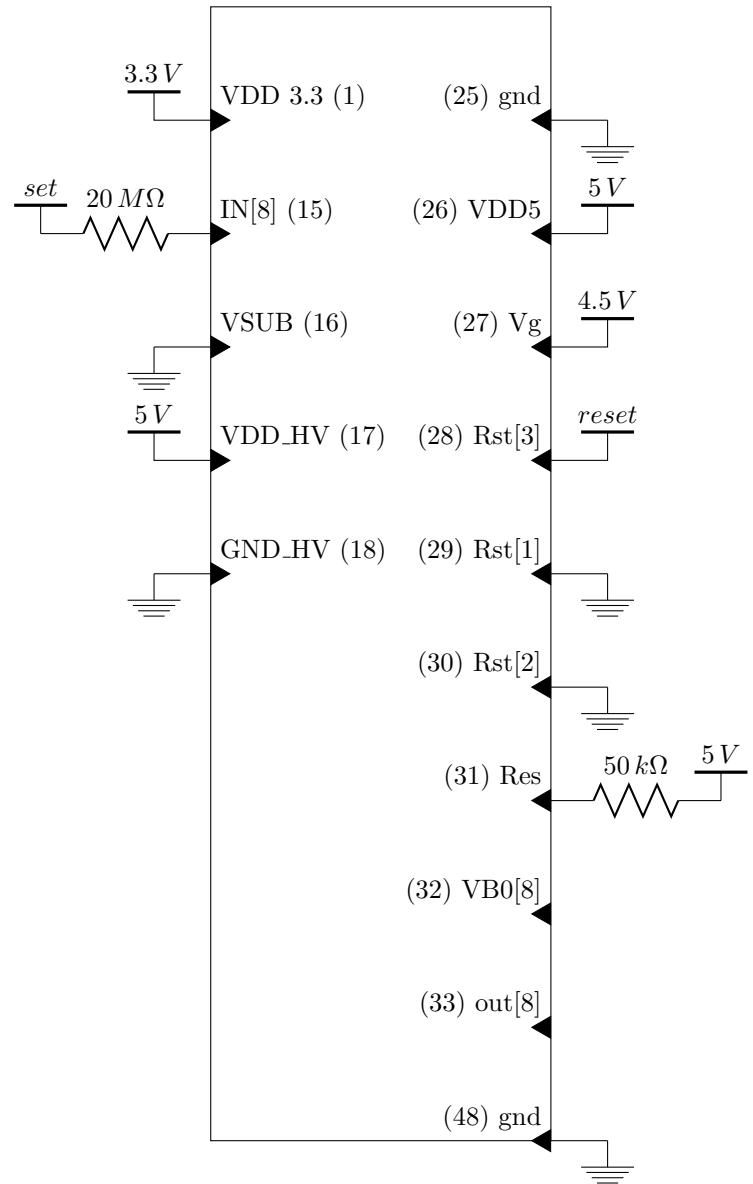


Figure 1: Schematic of breadboard

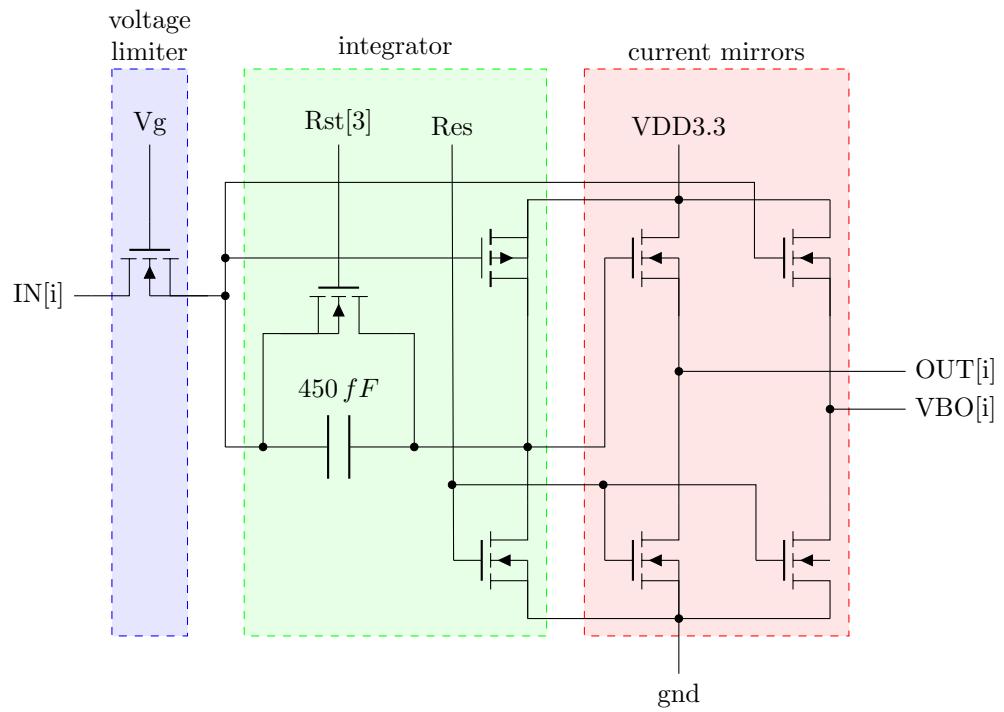


Figure 2: Schematic of ROIC channel

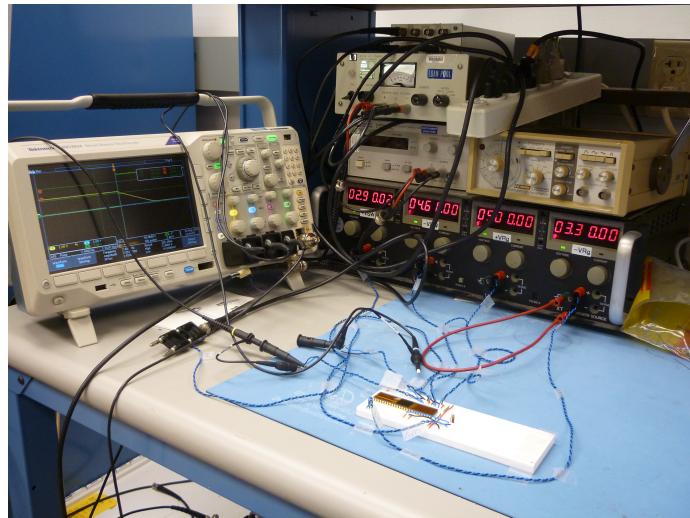


Figure 3: setup overview

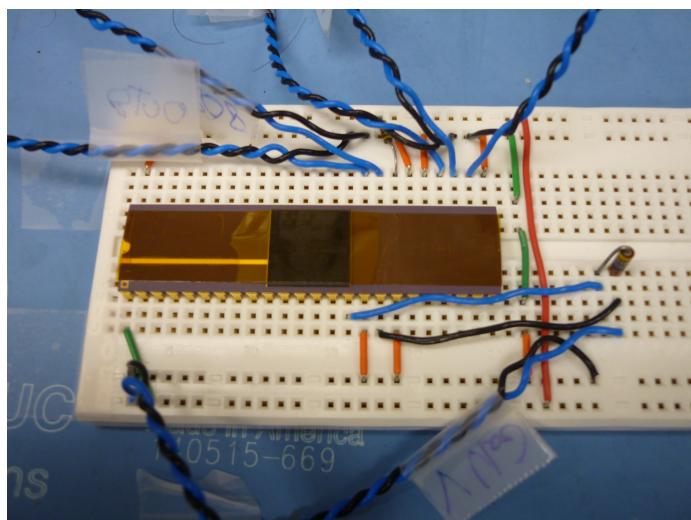


Figure 4: close-up

2 Reset mode

This test addresses the behavior of the circuit in reset mode. Figure 5 shows the measured values during reset mode. Note that the input voltage is 2.4 V, which is important when calculating the input current.

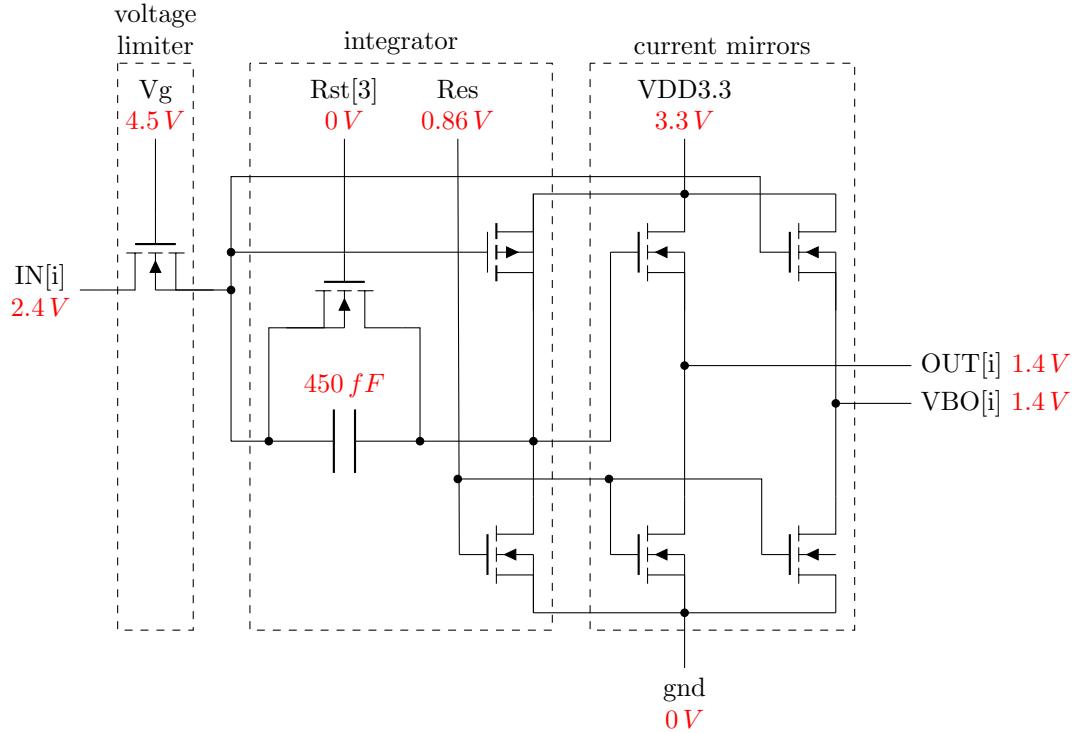


Figure 5: Schematic of ROIC channel template

3 Normal operation mode

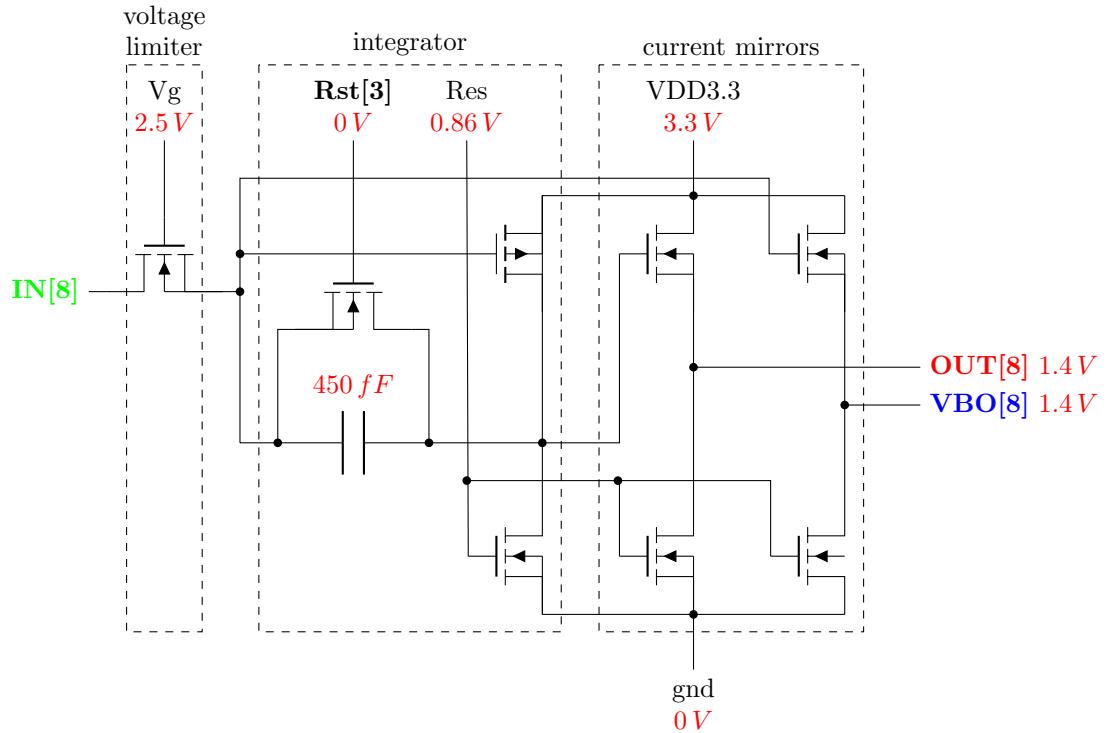


Figure 6: Schematic of ROIC channel template

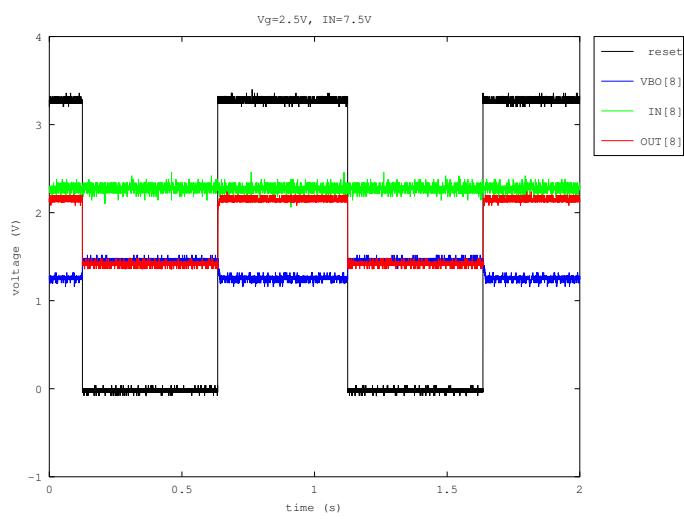


Figure 7: Behavior of a single channel at a lowered V_g