

Certificate of Completion

*This is to certify that Karan Shah successfully
completed 8 hours of Embedded System Design
with Xilinx Zynq FPGA and VIVADO online course
on Jan. 22, 2018*

Krishna Gaihre

Krishna Gaihre, Instructor

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Certificate no: UC-R247YPLA
Certificate url: ude.my/UC-R247YPLA

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