

FPGA-powered Image reconstruction

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https://github.com/kl996/xacc_presentation

► Project Summary:

- The project is part of ongoing research.
- Image reconstruction from sensor array signal.
- Kernel: Multidimensional matrix multiplication.
- Intermediate matrices are too large, so we apply tiling to compute them inside the FPGA.

► What we achieved:

- Getting familiar with Vitis environment.
- Extract HLS ready C, from original MatLab code.
- Synthesize kernel code.
- Test HLS Pragmas.
- Getting busy with debugging.