

Engbedded Atmel AVR® Fuse Calculator

Device selection

Select the AVR device type you want to configure. When changing this setting, default fuse settings will automatically be applied. Presets (hexadecimal representation of the fuse settings) can be reviewed and even be set in the last form at the bottom of this page.

AVR part name: 

ATmega168

Select

 (141 parts currently listed)

Feature configuration

This allows easy configuration of your AVR device. All changes will be applied instantly.

Features

Ext. Crystal Osc.; Frequency 8.0- MHz; Start-up time PWRDWN/RESET: 16K CK/14 CK + 0 ms; [CKSEL=1111 SUT=01]

☐ Clock output on PORTB0; [CKOUT=0]

☐ Divide clock by 8 internally; [CKDIV8=0]

Brown-out detection level at VCC=1.8 V; [BODLEVEL=110]

☒ Preserve EEPROM memory through the Chip Erase cycle; [EESAVE=0]

☐ Watch-dog Timer always on; [WDTON=0]

☒ Serial program downloading (SPI) enabled; [SPIEN=0]

☐ Debug Wire enable; [DWEN=0]

☐ Reset Disabled (Enable PC6 as i/o pin); [RSTDISBL=0]

☒ Boot Reset vector Enabled (default address=\$0000); [BOATRST=0]

Boot Flash section size=1024 words Boot start address=\$1C00; [BOOTSZ=00] ; default value

Manual fuse bits configuration

Apply feature settings

This table allows reviewing and direct editing of the AVR fuse bits. All changes will be applied instantly.

Note: ☐ means unprogrammed (1); ☒ means programmed (0).

Bit	Low	High	Extended
7	<input type="checkbox"/> <b>CKDIV8</b> Divide clock by 8	<input type="checkbox"/> <b>RSTDISBL</b> External reset disable	
6	<input type="checkbox"/> <b>CKOUT</b> Clock output	<input type="checkbox"/> <b>DWEN</b> debugWIRE Enable	
5	<input checked="" type="checkbox"/> <b>SUT1</b> Select start-up time	<input checked="" type="checkbox"/> <b>SPIEN</b> Enable Serial programming and Data Downloading	
4	<input type="checkbox"/> <b>SUT0</b> Select start-up time	<input type="checkbox"/> <b>WDTON</b> Watchdog Timer Always On	
3	<input type="checkbox"/> <b>CKSEL3</b> Select Clock Source	<input checked="" type="checkbox"/> <b>EESAVE</b> EEPROM memory is preserved through chip erase	
2	<input type="checkbox"/> <b>CKSEL2</b> Select Clock Source	<input type="checkbox"/> <b>BODLEVEL2</b> Brown-out Detector trigger level	<input checked="" type="checkbox"/> <b>BOOTSZ1</b> Select boot size
1	<input type="checkbox"/> <b>CKSEL1</b> Select Clock Source	<input type="checkbox"/> <b>BODLEVEL1</b> Brown-out Detector trigger level	<input checked="" type="checkbox"/> <b>BOOTSZ0</b> Select boot size
0	<input type="checkbox"/> <b>CKSEL0</b> Select Clock Source	<input checked="" type="checkbox"/> <b>BODLEVEL0</b> Brown-out Detector trigger level	<input checked="" type="checkbox"/> <b>BOATRST</b> Select reset vector

Current settings

Apply manual fuse bit settings

These fields show the actual hexadecimal representation of the fuse settings from above. These are the values you have to program into your AVR device. Optionally, you may fill in the numerical values yourself to preset the configuration to these values. Changes in the value fields are applied instantly (taking away the focus)!

Low

High

Extended

Action

0x

DF

0x

D6

0x

00

Apply values

Defaults

Apply manual changes to the values on the left side, or load factory default values for the selected device.

AVRDUDE arguments

-U lfuse:w:0xdf:m -U hfuse:w:0xd6:m -U efuse:w:0x00:m

Select (try triple-click) and copy-and-paste this option string into your avrdude command line. You may specify multiple -U arguments within one call of avrdude.

References

All information based on database **ATmega168.xml** build **201**.  
**Unreviewed original XML backend database from Atmel. Probably buggy! Please report.**

No responsibility is taken for the correctness of the presented information.  
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User interface version: 0.9.0.

If you find bugs in the user interface or the database backend(s), please report them.