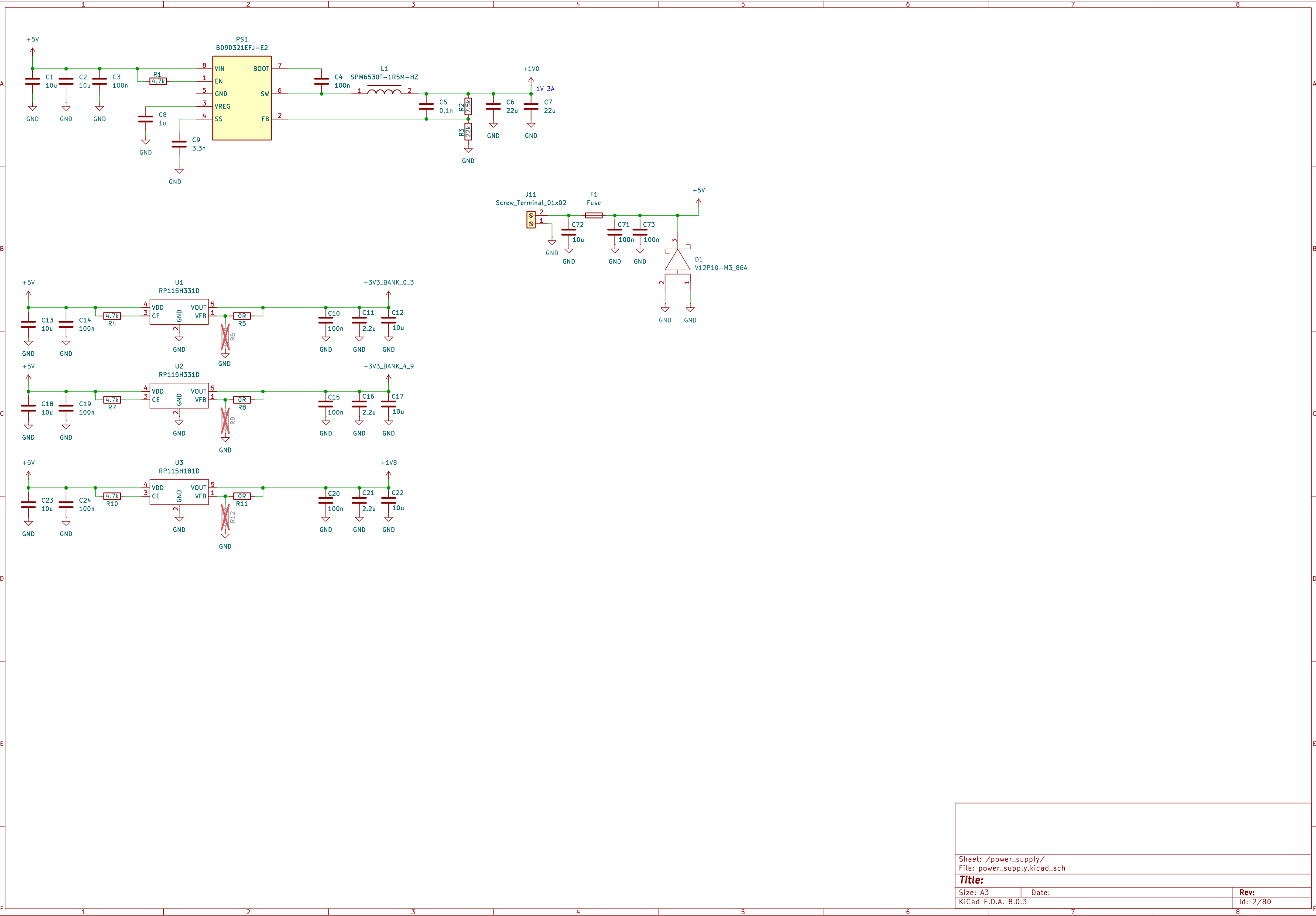


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A	<div>power_supply</div> <div>File: power_supply.kicad_sch</div>				A
B	<div>FPGA_machX05</div> <div>File: FPGA_machX05.kicad_sch</div>				B
C					C
D	<div></div> <div>Sheet: / File: home_auto.kicad_sch</div> <div>Title:</div> <div>Size: A4Date:KiCad E.D.A. 8.0.3</div> <div>Rev:Id: 1/80</div>				D
1	2	3	4	5	6



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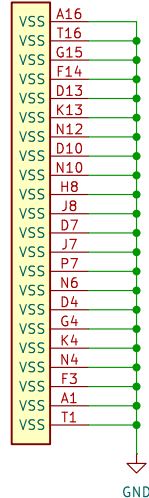
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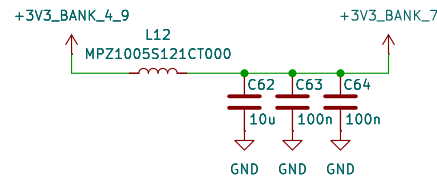
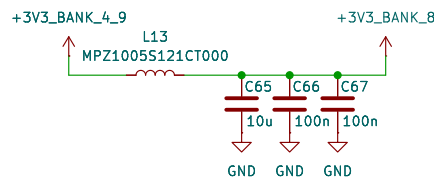
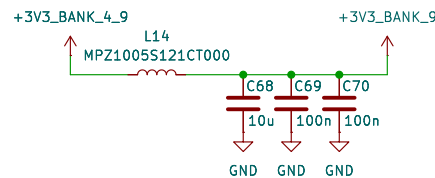
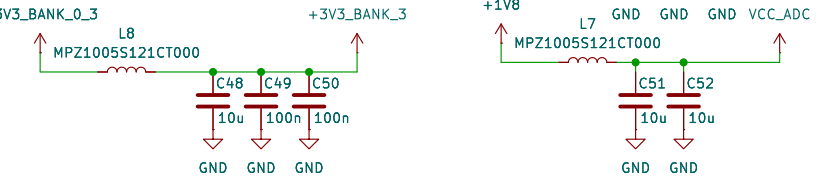
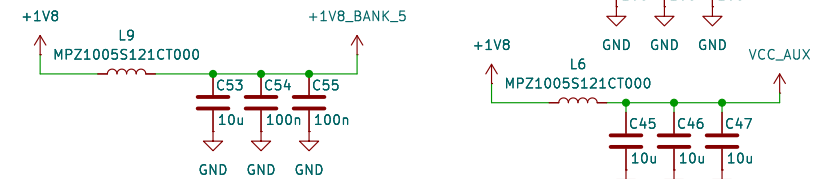
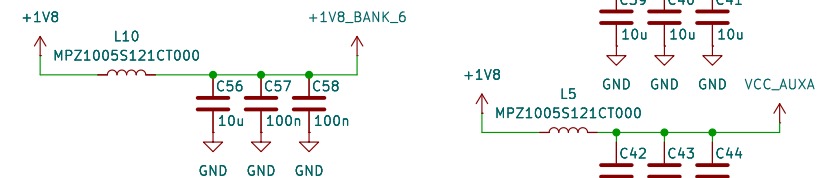
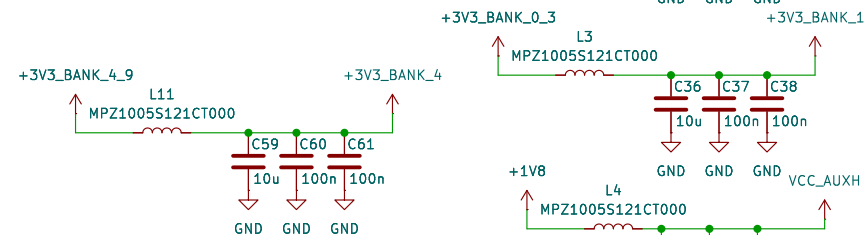
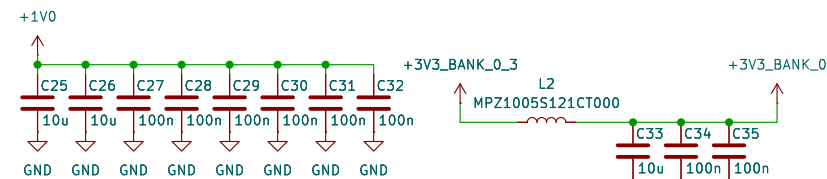
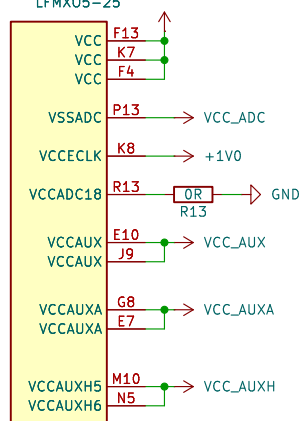
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B						
C						
D						<div></div> <div>Sheet: /FPGA_machX05/ File: FPGA_machX05.kicad_sch</div> <div>Title:</div> <div>Size: A4Date:KiCad E.D.A. 8.0.3</div> <div>Rev:Id: 3/80</div>
	1	2	3	4	5	6

U4L
LFMX05-25



U4N
LFMX05-25



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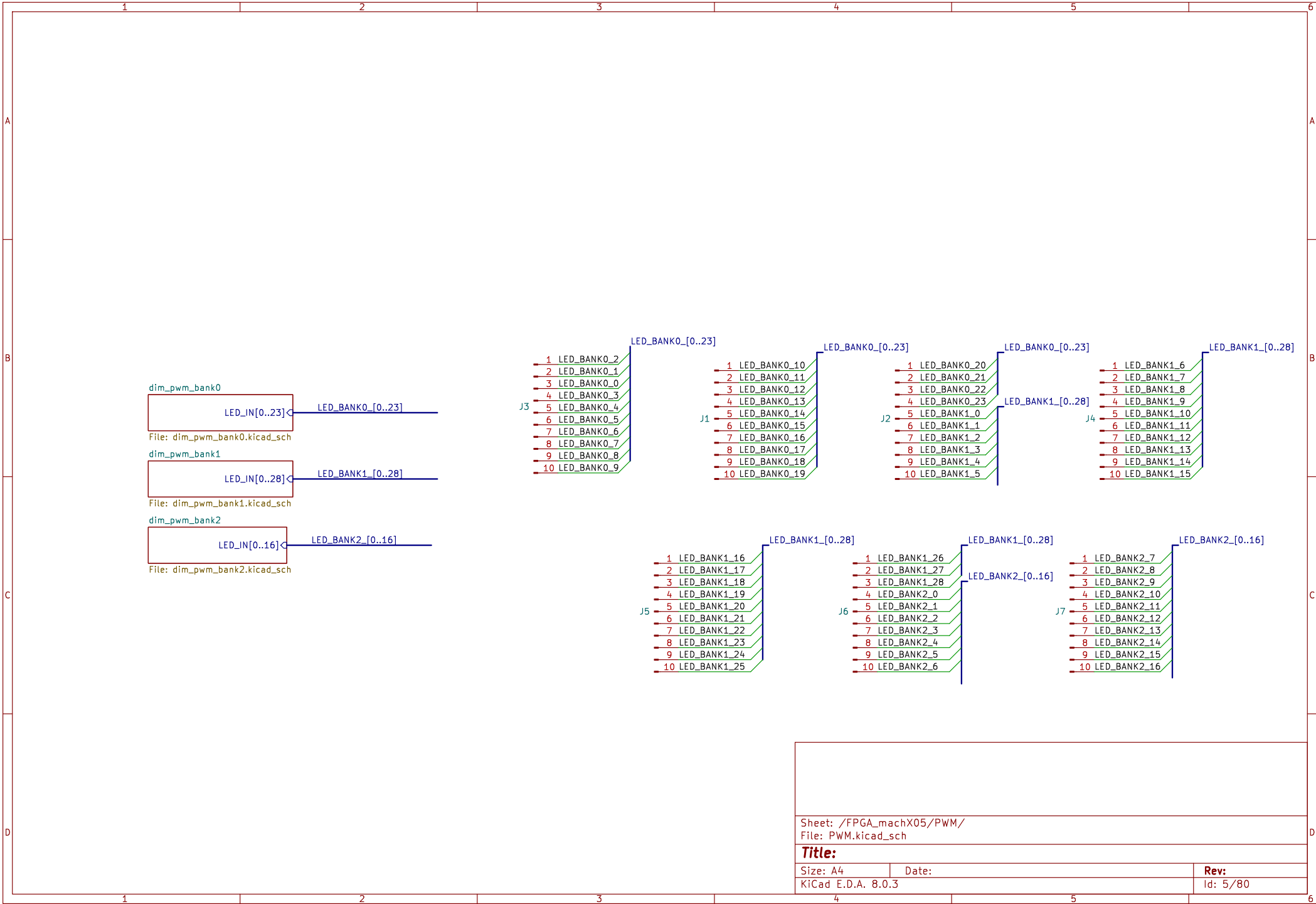
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Sheet: /FPGA_machX05/PWM/
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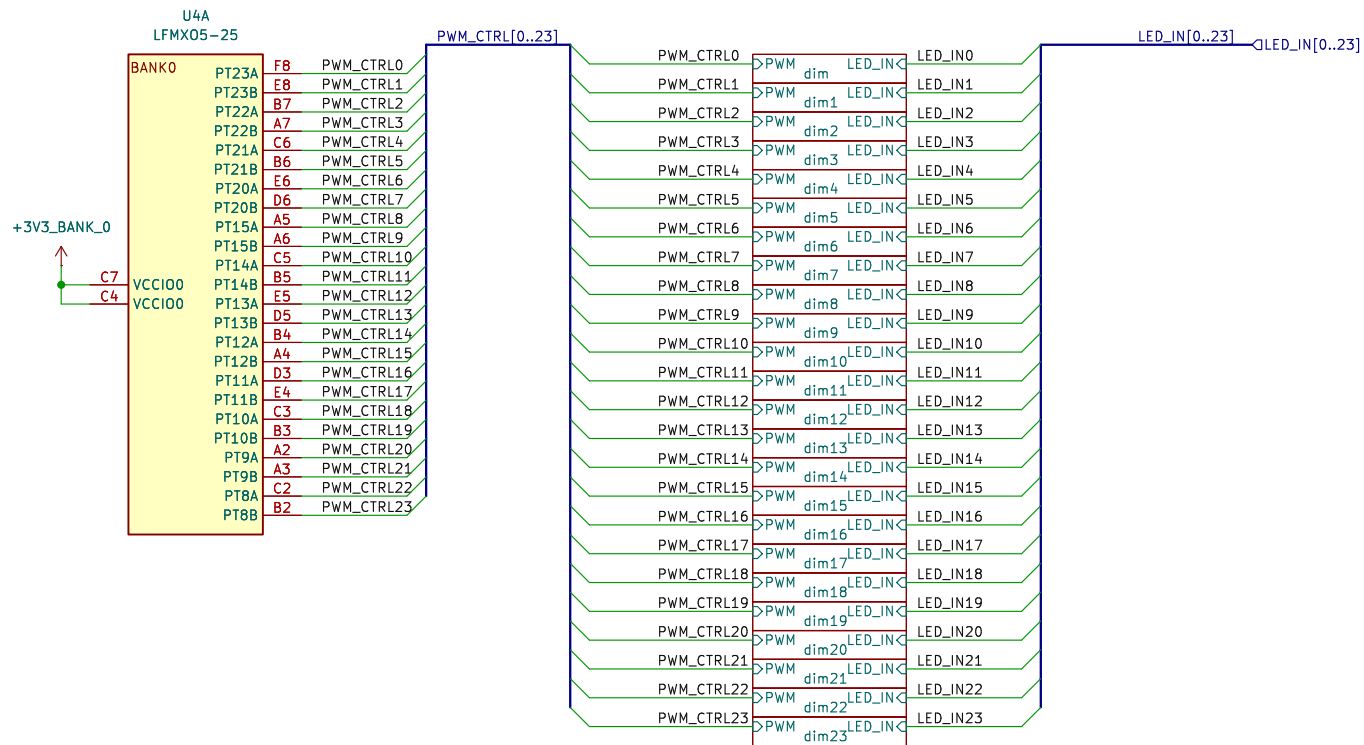
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Sheet: /FPGA_machX05/PWM/dim_pwm_bank0/
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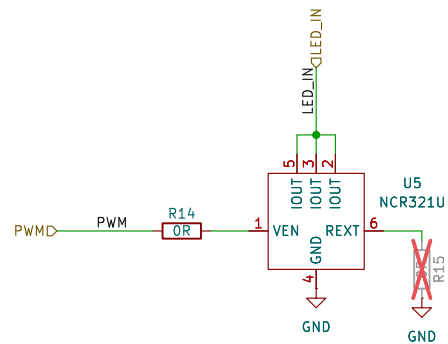
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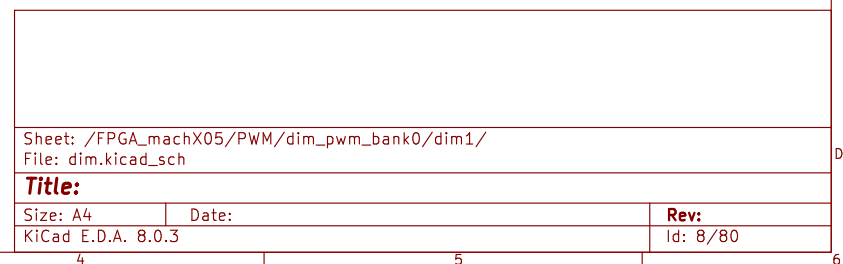
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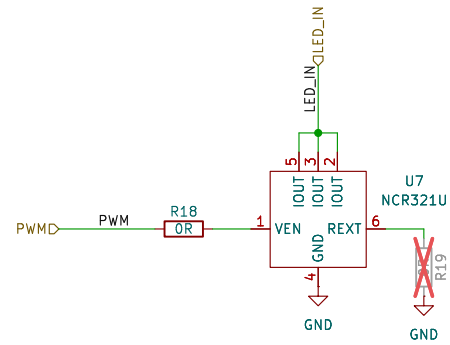
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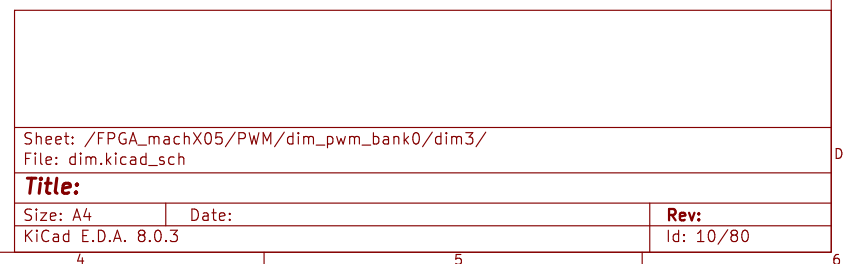
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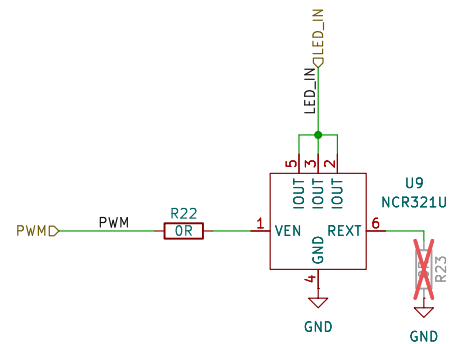
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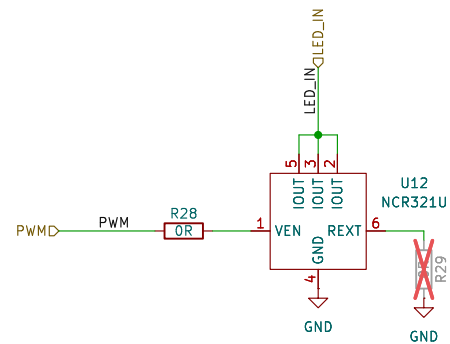
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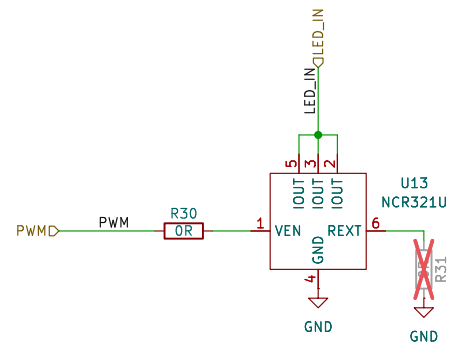
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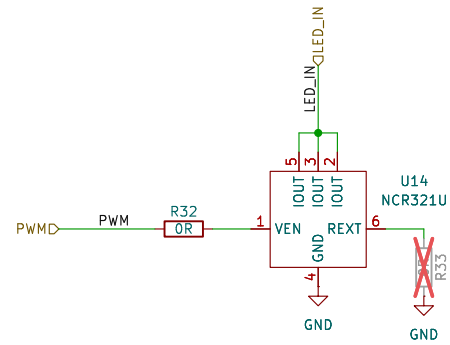
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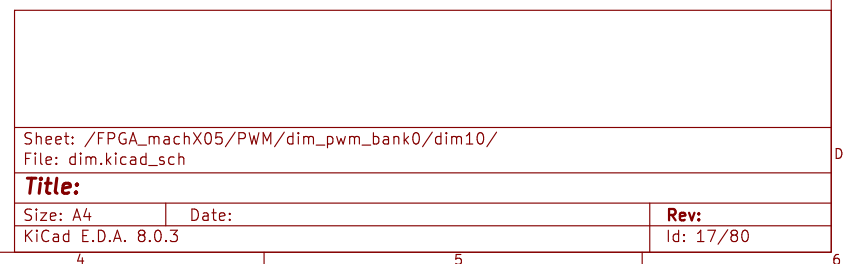
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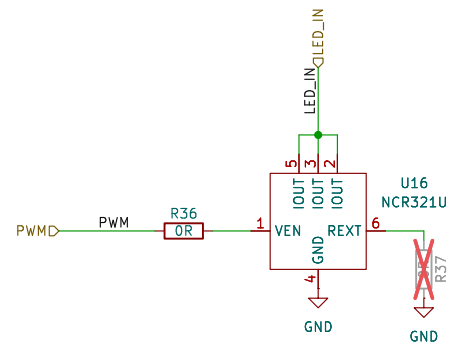
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Sheet: /FPGA_machX05/PWM/dim_pwm_bank0/dim11/
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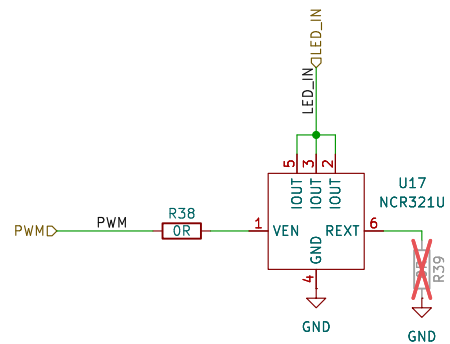
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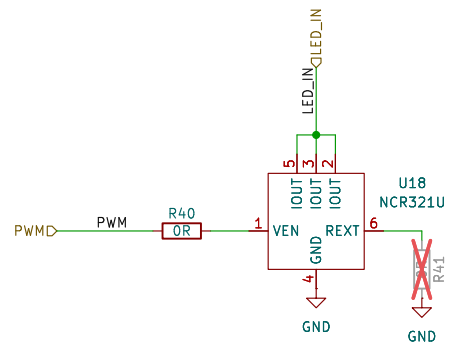
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Sheet: /FPGA_machX05/PWM/dim_pwm_bank0/dim13/
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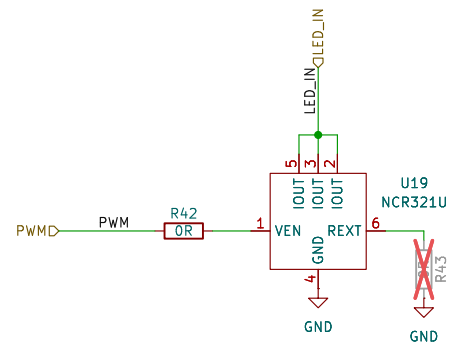
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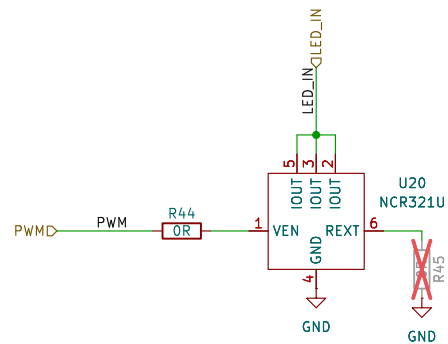
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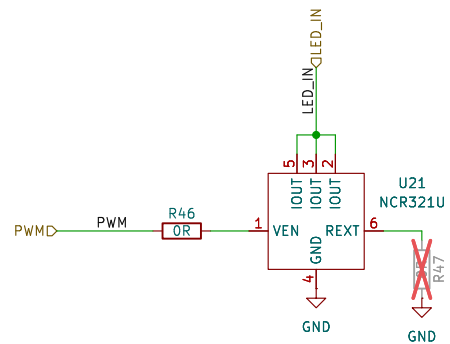
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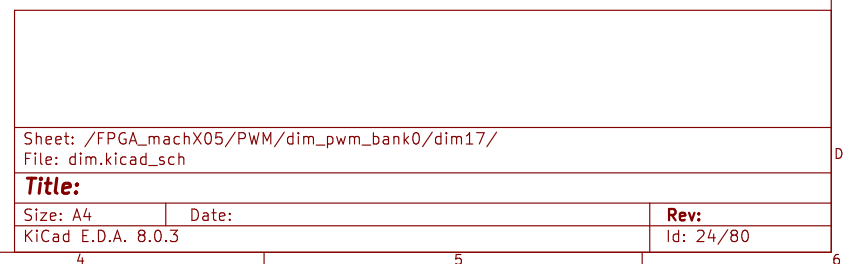
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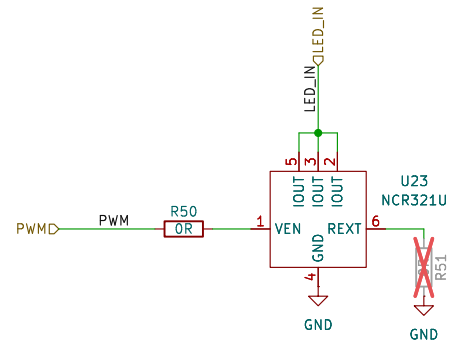
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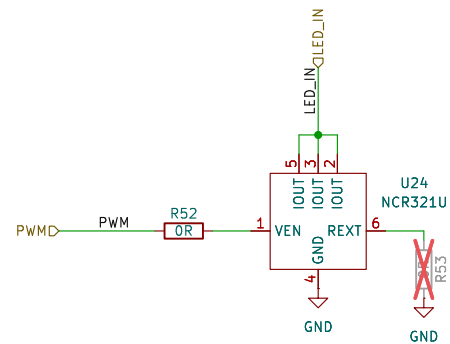
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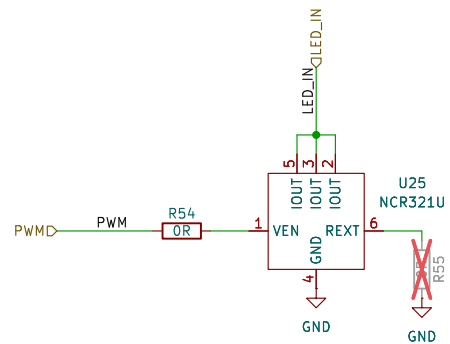
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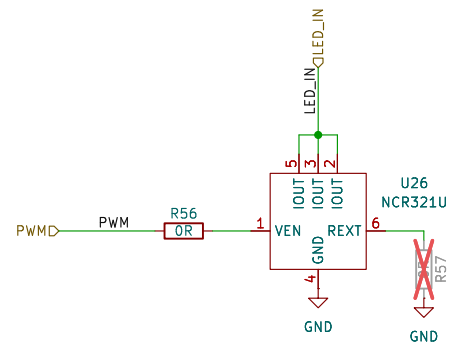
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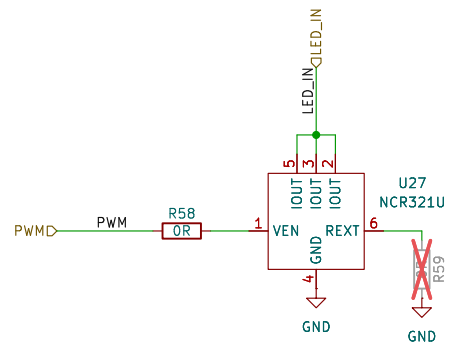
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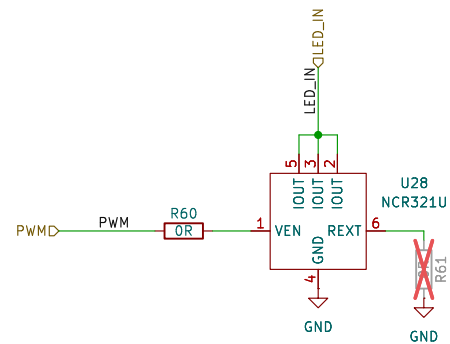
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Sheet: /FPGA_machX05/PWM/dim_pwm_bank0/dim23/
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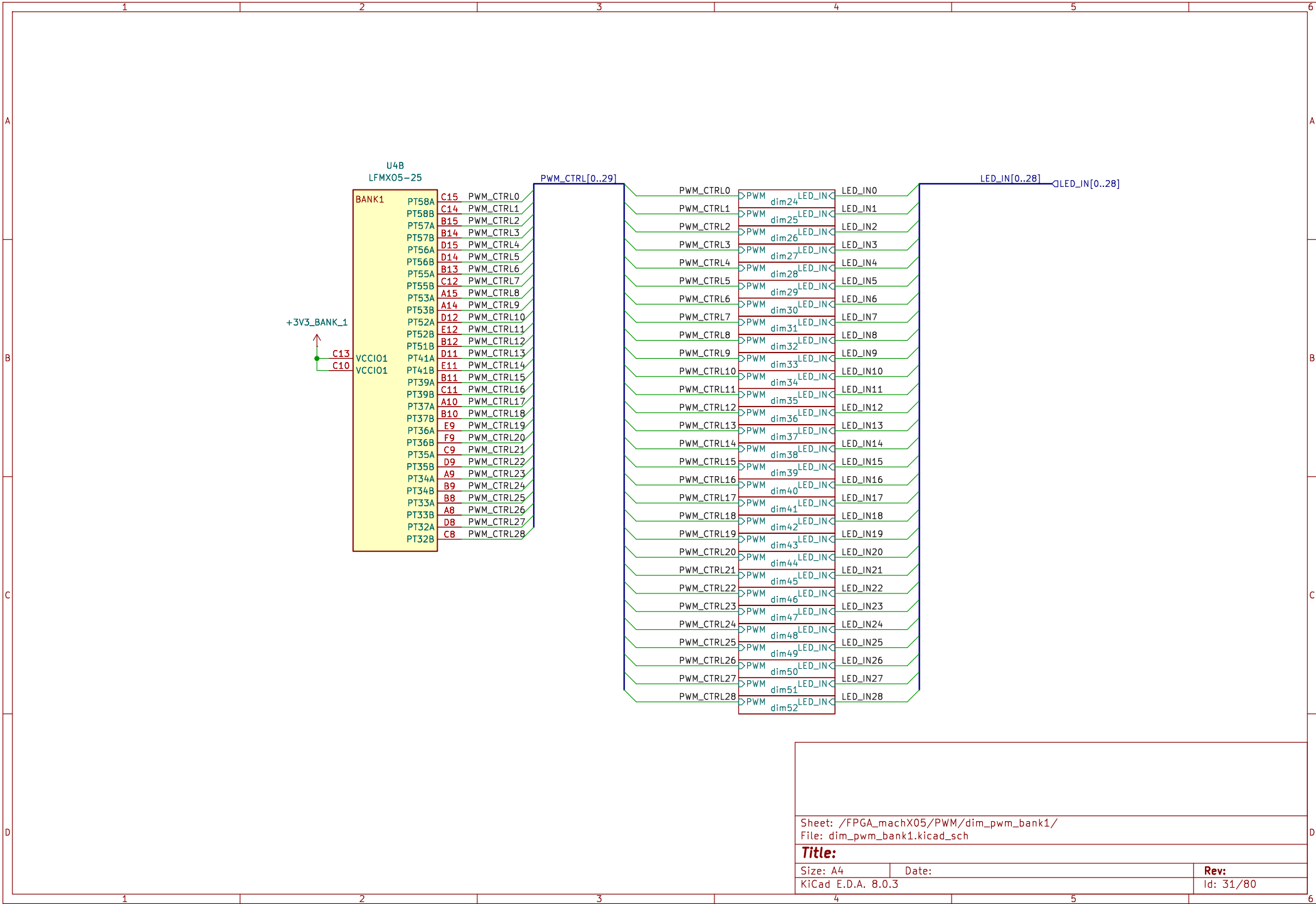
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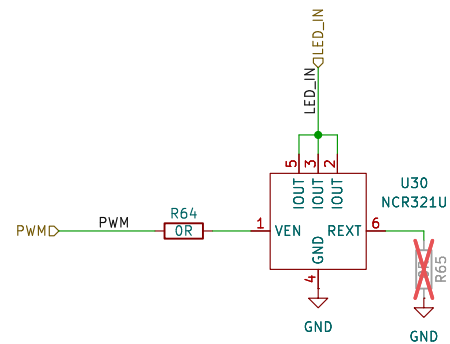
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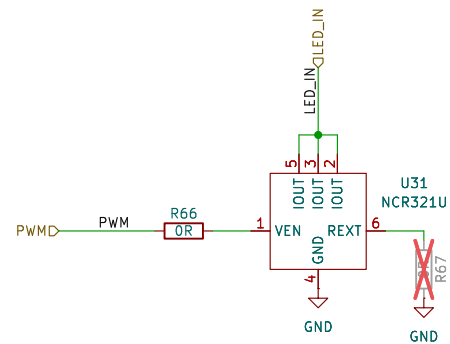
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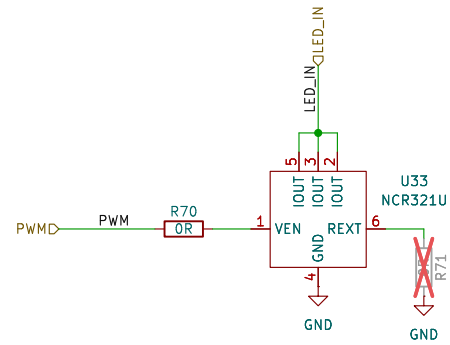
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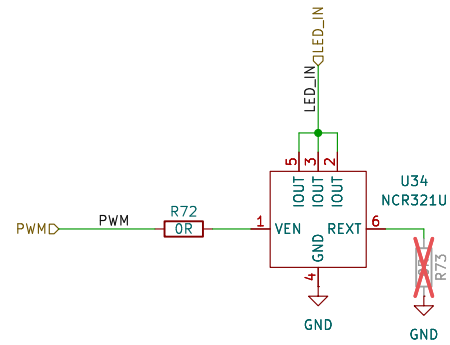
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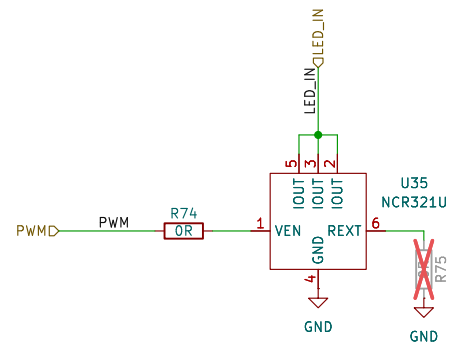
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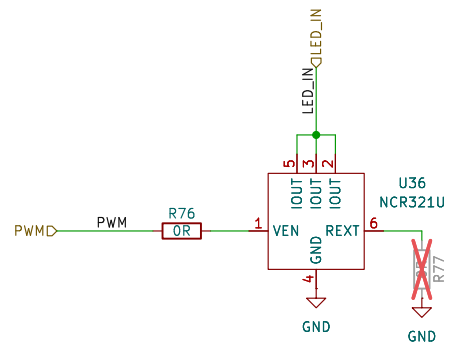
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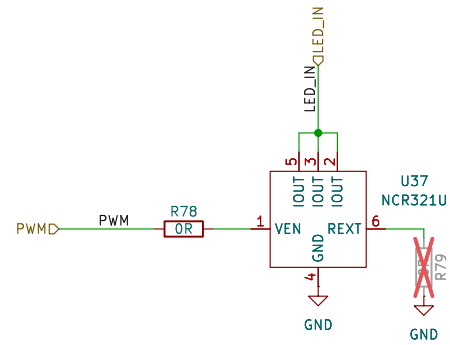
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Sheet: /FPGA_machX05/PWM/dim_pwm_bank1/dim32/
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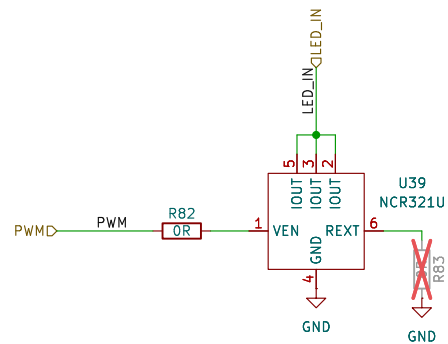
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Sheet: /FPGA_machX05/PWM/dim_pwm_bank1/dim34/
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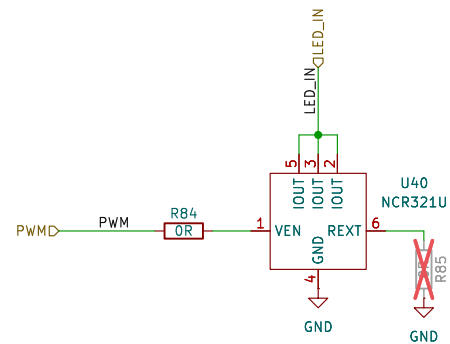
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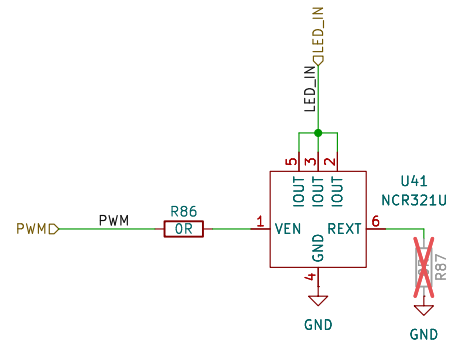
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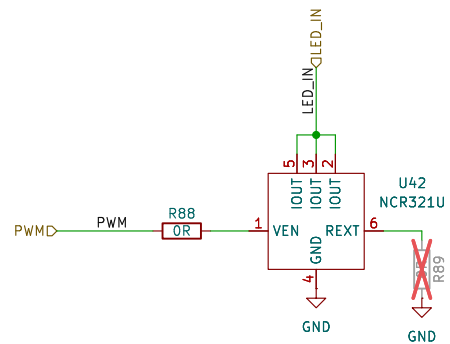
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Sheet: /FPGA_machX05/PWM/dim_pwm_bank1/dim37/
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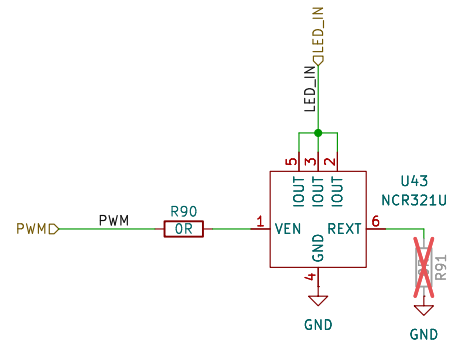
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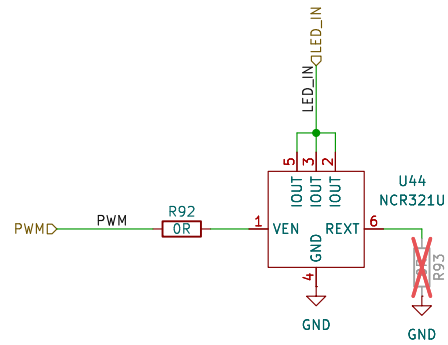
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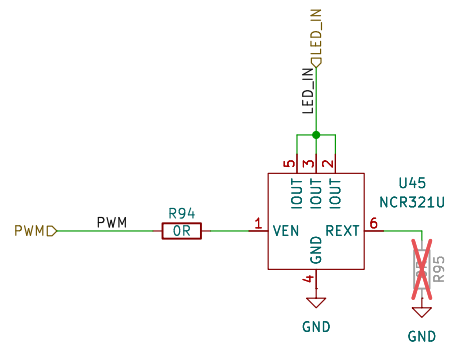
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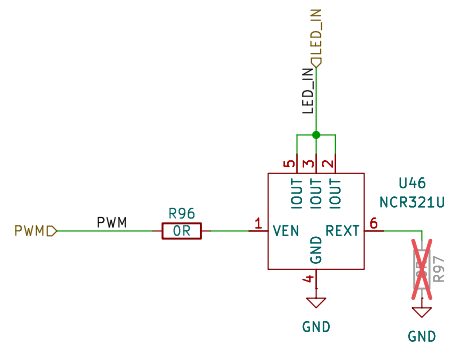
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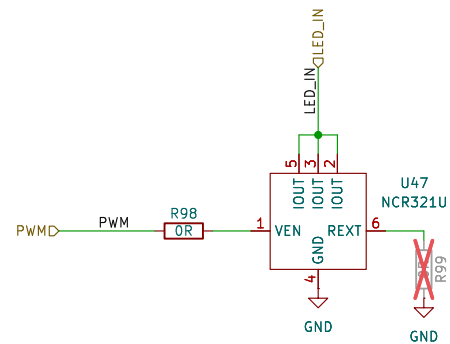
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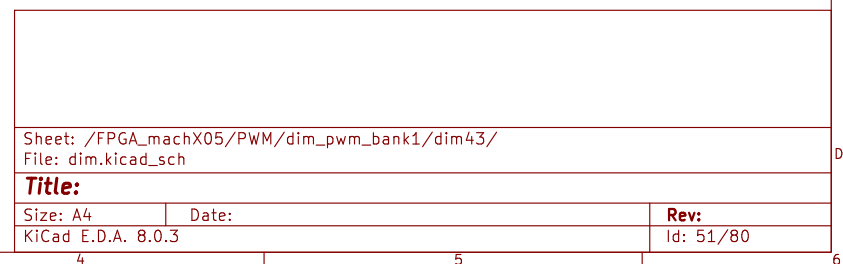
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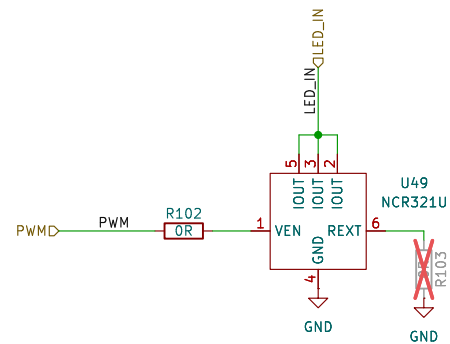
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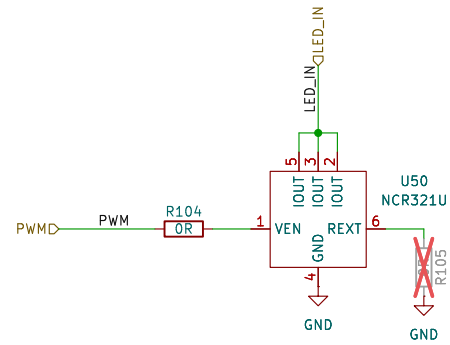
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File: dim.kicad_sch

Title:

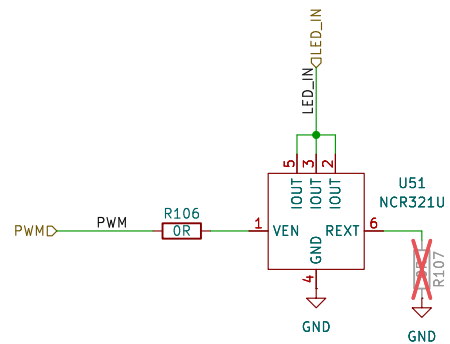
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank1/dim46/
File: dim.kicad_sch

Title:

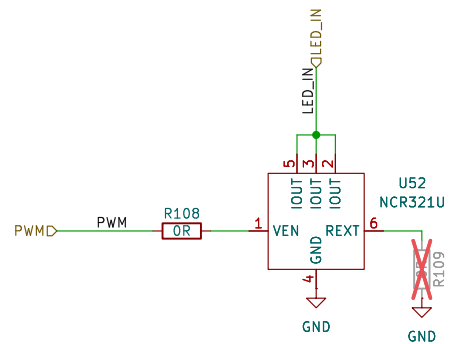
Size: A4

Date:

Rev:

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Id: 54/80



Sheet: /FPGA_machX05/PWM/dim_pwm_bank1/dim47/
File: dim.kicad_sch

Title:

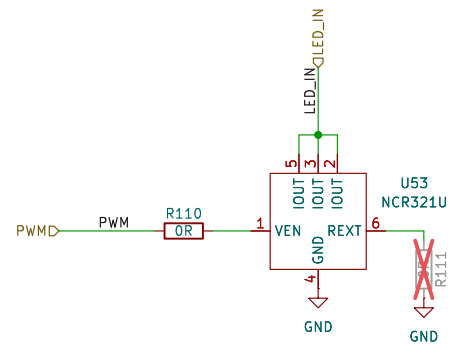
Size: A4

Date:

Rev:

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Id: 55/80



Sheet: /FPGA_machX05/PWM/dim_pwm_bank1/dim48/
File: dim.kicad_sch

Title:

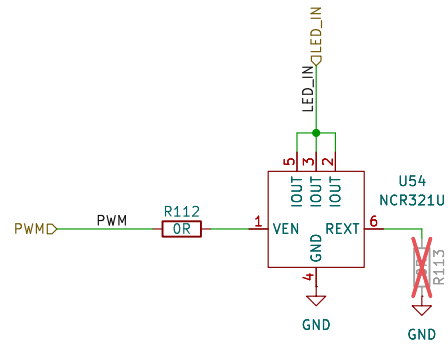
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank1/dim49/
File: dim.kicad_sch

Title:

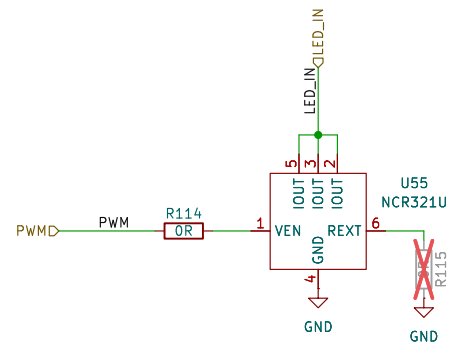
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank1/dim50/
File: dim.kicad_sch

Title:

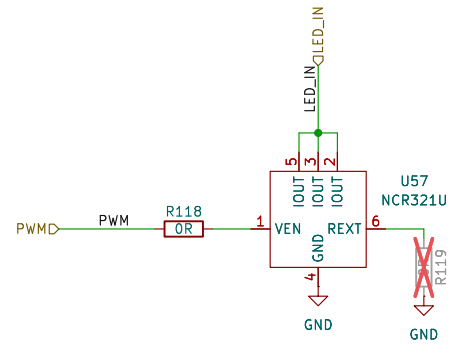
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank1/dim52/
File: dim.kicad_sch

Title:

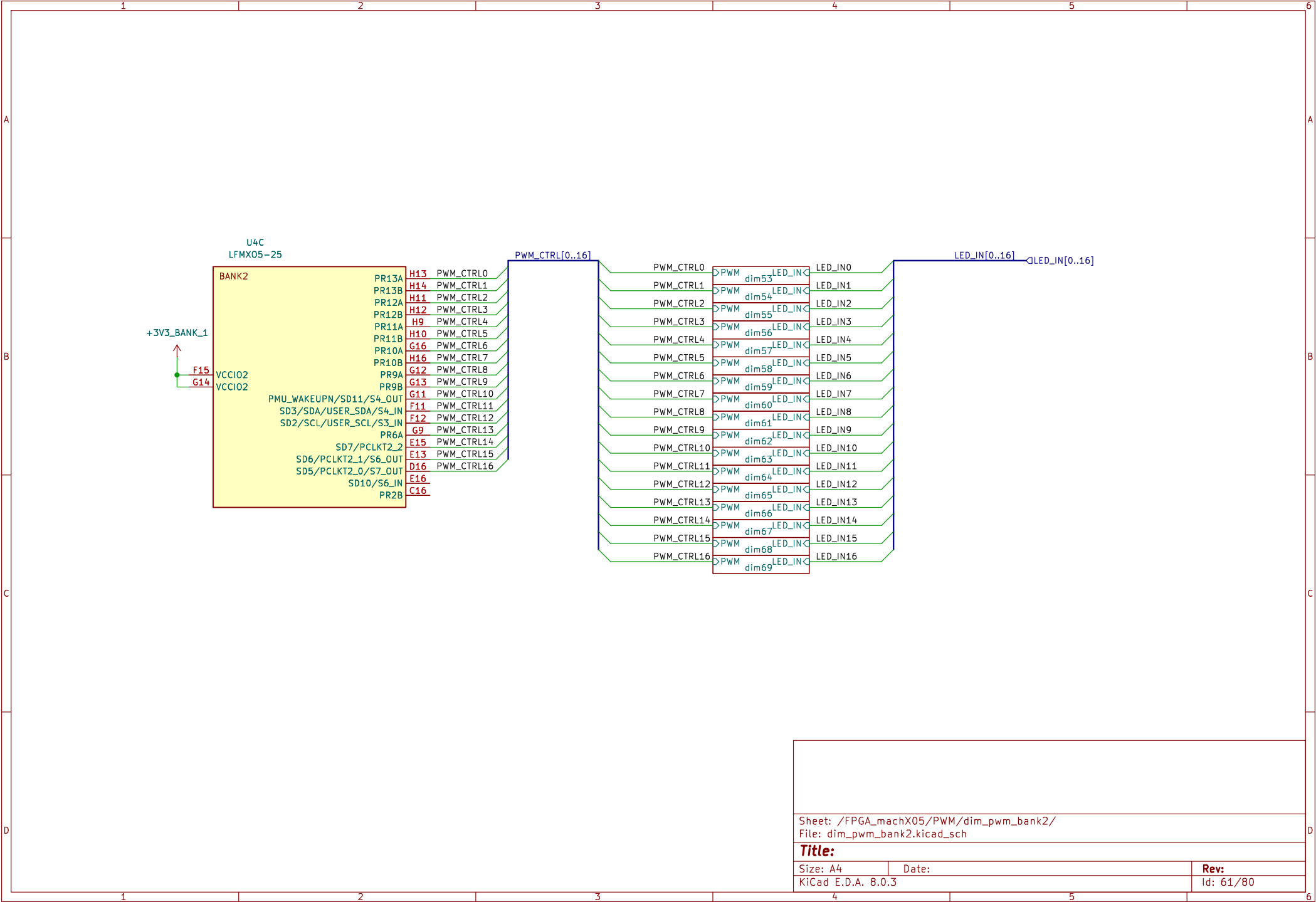
Size: A4

Date:

Rev:

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Id: 60/80



Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/
File: dim_pwm_bank2.kicad_sch

Title:

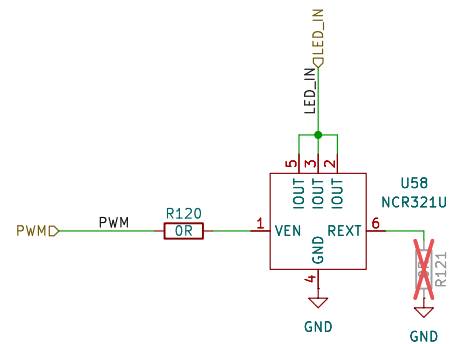
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim53/
File: dim.kicad_sch

Title:

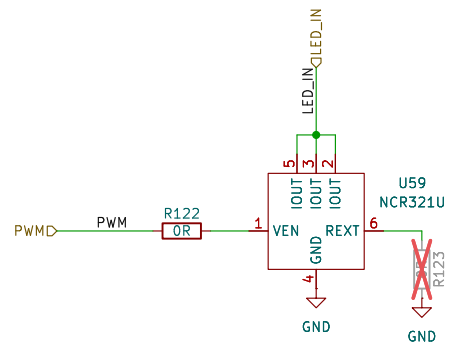
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim54/
File: dim.kicad_sch

Title:

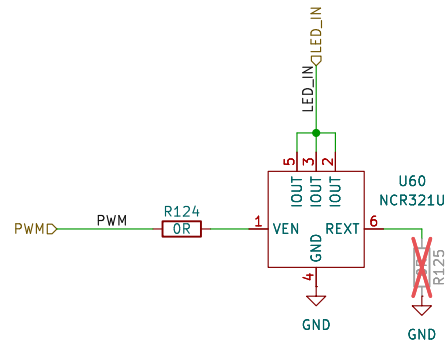
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim55/
File: dim.kicad_sch

Title:

Size: A4

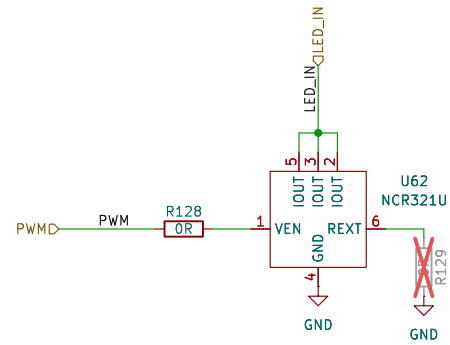
Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim57/
File: dim.kicad_sch

Title:

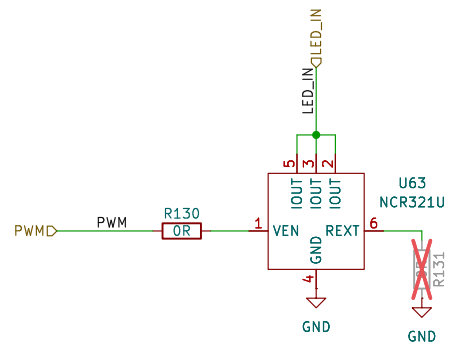
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim58/
File: dim.kicad_sch

Title:

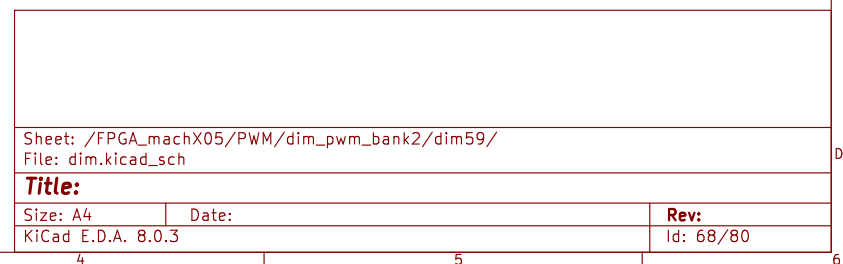
Size: A4

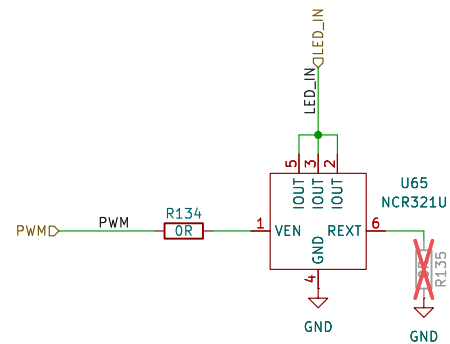
Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim60/
File: dim.kicad_sch

Title:

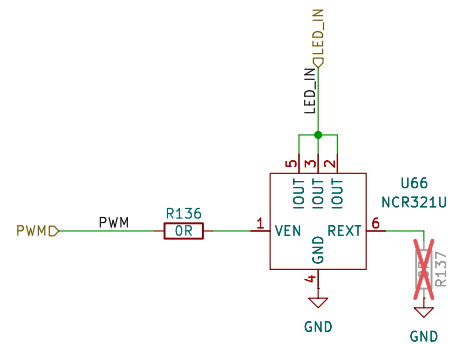
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim61/
File: dim.kicad_sch

Title:

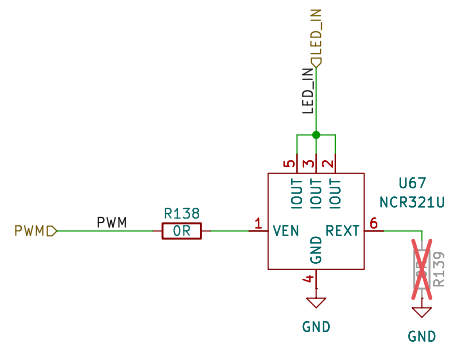
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim62/
File: dim.kicad_sch

Title:

Size: A4

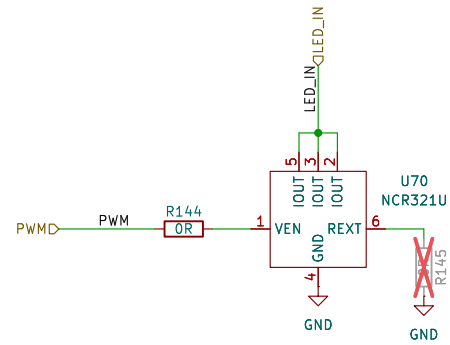
Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim65/
File: dim.kicad_sch

Title:

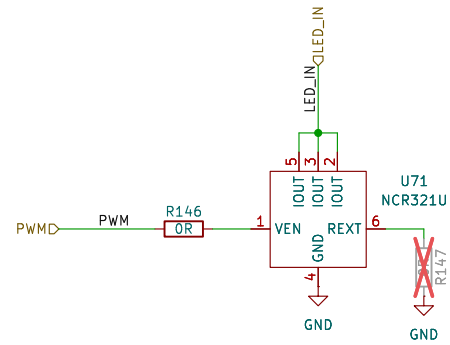
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim66/
File: dim.kicad_sch

Title:

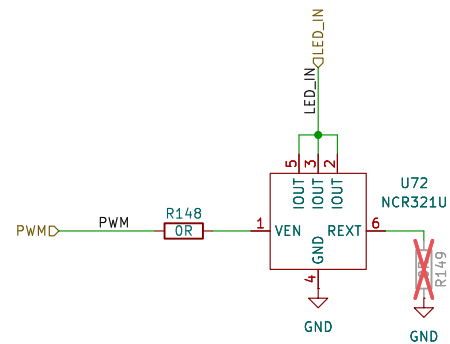
Size: A4

Date:

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Sheet: /FPGA_machX05/PWM/dim_pwm_bank2/dim67/
File: dim.kicad_sch

Title:

Size: A4

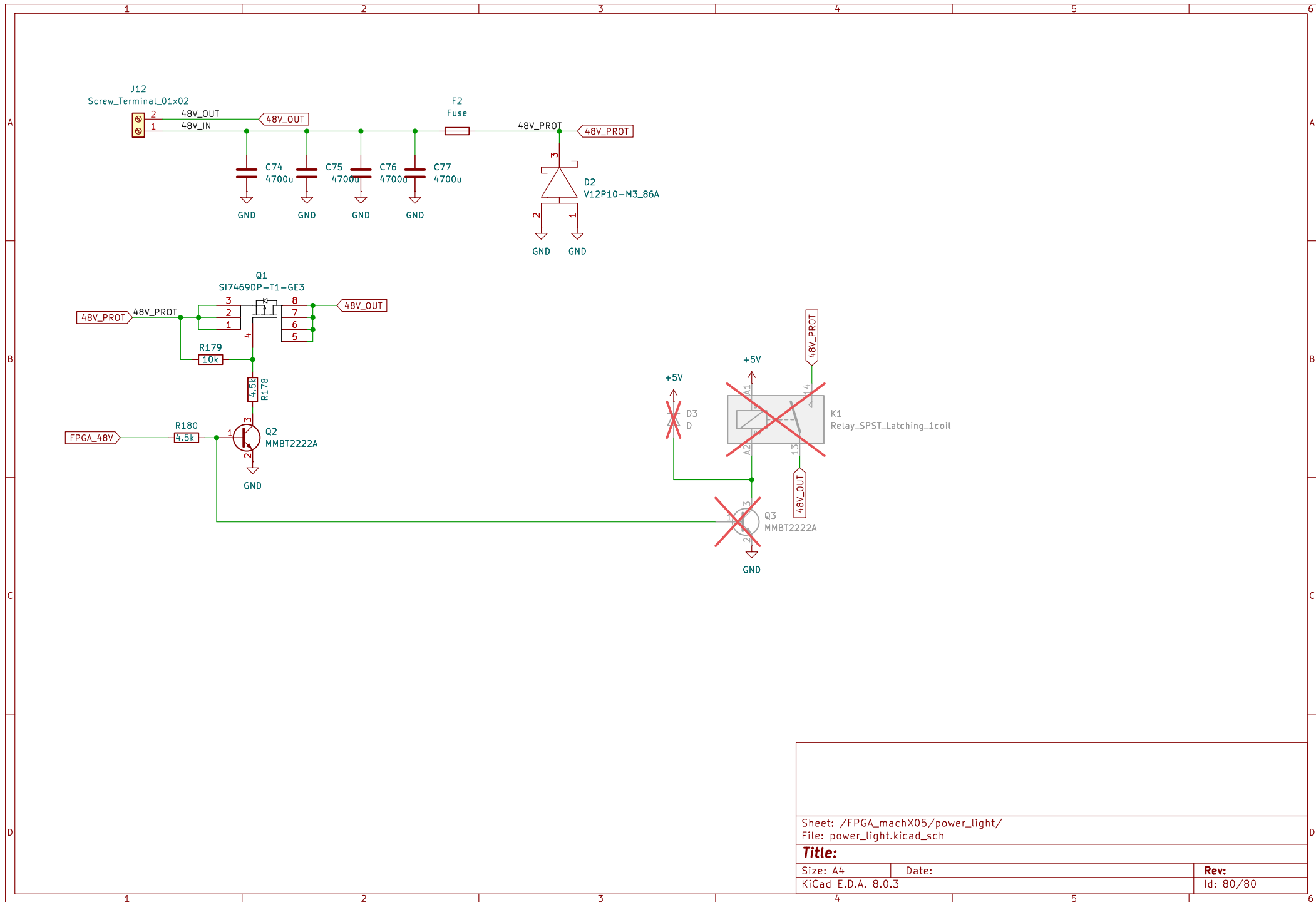
Date:

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Sheet: /FPGA_machX05/power_light/
File: power_light.kicad_sch

Title:

Size: A4

Date:

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