| Command Name | HEX | ASCII |
|---|---|---|
| FW Version | 0A 56 0D | <LF>V<CR> |
| Reader ID | 0A 53 0D | <LF>S<CR> |
| Query EPC | 0A 51 0D | <LF>Q<CR> |
| Multi EPC | 0A 55 0D | <LF>U<CR> |
| Read Power | 0A 4E 30 2C 30 30 0D | <LF>N0 ,0 0<CR> |
| Write Power | 0A 4E 31 2C 31 34 0D | <LF>N1 ,1 4<CR> |
| Read TID bank address=0 word=6 | 0A 52 32 2C 30 2C 36 0D | <LF>R2 ,0,6<CR> |
| Read EPC bank PC word | 0A 52 31 2C 31 2C 31 0D | <LF>R1 ,1,1<CR> |
| Read EPC bank address=0 word=8 | 0A 52 31 2C 31 2C 38 0D | <LF>R1 ,0,8<CR> |
| Read USER bank address=0 word=32 | 0A 52 33 2C 30 2C 32 30 0D | <LF>R3 ,0,20<CR> |
| Read Reserved bank kill and access pwd | 0A 52 30 2C 30 2C 32 0D | <LF>R0 ,0,2<CR> |
| Write EPC bank PC word | 0A 57 31 2C 31 2C 31 2C 33 30 30 30 0D | <LF>W1 ,1,1,3000<CR> |
| Write EPC bank address=2 word=6 | 0A 57 31 2C 32 2C 36 2C 30 30 30 30 31 31 31 31 32 32 32 32 33 33 33 33 34 34 34 34 35 35 35 35 0D | <LF>W1 ,2,6,0000111122223333 44445555<CR> |
| Write USER bank address=0 word=1 | 0A 57 33 2C 30 2C 31 2C 30 30 30 30 0D | <LF>W3 ,0,1,0000<CR> |
| Write USER bank address=0 word=8 | 0A 57 33 2C 30 2C 38 2C 30 30 30 30 31 31 31 31 32 32 32 32 33 33 33 33 34 34 34 34 35 35 35 35 36 36 36 36 37 37 37 37 0D | <LF>W3 ,0,8,0000111122223333 4444555566667777<CR> |
| Write Reserved bank kill | 0A 57 30 2C 30 2C 32 2C 30 31 30 32 30 33 30 34 0D | <LF>W0 ,0,2,01020304<CR> |
| Write access pwd | 0A 57 30 2C 32 2C 32 2C 31 32 33 34 35 36 37 38 0D | <LF>W0 ,2,2,12345678<CR> |
| Write Reserved bank kill and access pwd | 0A 57 30 2C 30 2C 34 2C 30 31 30 32 30 33 30 34 41 31 41 32 41 33 41 34 0D | <LF>W0 ,0,4,01020304A1A2A3A 4<CR> |
| Access password | 0A 50 41 31 41 32 41 33 | <LF>P A1A2A3A4<CR> |

| | | |
|---|---|---|
| | 41 34 0D | |
| Kill | 0A 4B 30 31 30 32 30 33 30 34 2C 30 0D | <LF>K01020304 ,0<CR> |
| Lock mask=020 action020(EPC write lock) | 0A 4C 30 32 30 2C 30 32 30 0D | <LF>L020,020<CR> |
| Lock mask=020 action000(EPC write unlock) | 0A 4C 30 32 30 2C 30 30 30 0D | <LF>L020,000<CR> |
| US mode 902-928 | 0A 4E 35 2C 30 31 0D | <LF>N5,01<CR> |
| TW mode 922-928 | 0A 4E 35 2C 30 32 0D | <LF>N5,02<CR> |
| CN1 mode 920-925 | 0A 4E 35 2C 30 33 0D | <LF>N5,03<CR> |
| CN2 mode 840-845 | 0A 4E 35 2C 30 34 0D | <LF>N5,04<CR> |
| CE mode 865-868 | 0A 4E 35 2C 30 35 0D | <LF>N5,05<CR> |
| JP mode 916-921 | 0A 4E 35 2C 30 36 0D | <LF>N5,06<CR> |
| KR mode 917-921 | 0A 4E 35 2C 30 37 0D | <LF>N5,07<CR> |
| VIN mode 918-923 | 0A 4E 35 2C 30 38 0D | <LF>N5,08<CR> |
| EU2 mode 916-920 | 0A 4E 35 2C 30 39 0D | <LF>N5,09<CR> |
| IN mode 865-867 | 0A 4E 35 2C 30 41 0D | <LF>N5,0A<CR> |

Impinj 、Alien 芯片存储区划分

| Model | User Memory | EPC Memory | Serialized TID | True3D™ Technology | QT™ Technology |
|---|---|---|---|---|---|
| Monza 3 | 0 | 96 | 0 | × | × |
| Monza 4D | 32 | 128 | 48 | √ | × |
| Monza 4E | 128 | 496 | 48 | √ | × |
| Monza 4U | 512 | 128 | 48 | √ | × |
| Monza 4QT | 512 | 128 | 48 | √ | √ |
| Monza 5 | 0 | 128 | 48 | × | × |
| H3 | 512 | 96 | 64 | × | × |
| H4 | 128 | 128 | 64 | × | × |

**Alien H3 Tag IC**

**Memory Map**

| Bank | Address | Description | Memory | Bits |
|---|---|---|---|---|
| User | 00h – 1FFh | User | NVM | 512 |
| TID | 70h – BFh | Device Configuration | ROM-NVM | 80 |
| | 60h – 6Fh | Mask Unique Identifier | ROM | 16 |
| | 20h – 5Fh | Unique Tag ID Unalterable | NVM | 64 |
| | 00h – 1Fh | TID EPC/TMD/TMDID/TMN | ROM | 32 |
| EPC | 20h – 7Fh | EPC # | NVM | 96 |
| | 10h – 1Fh | EPC-PC | NVM | 16 |
| | 00h – 0Fh | EPC-CRC | RAM | 16 |
| Reserved | 20h – 3Fh | RES-Access Pwd, EPC optional | NVM | 32 |
| | 00h – 1Fh | RES-Kill Pwd | NVM | 32 |