

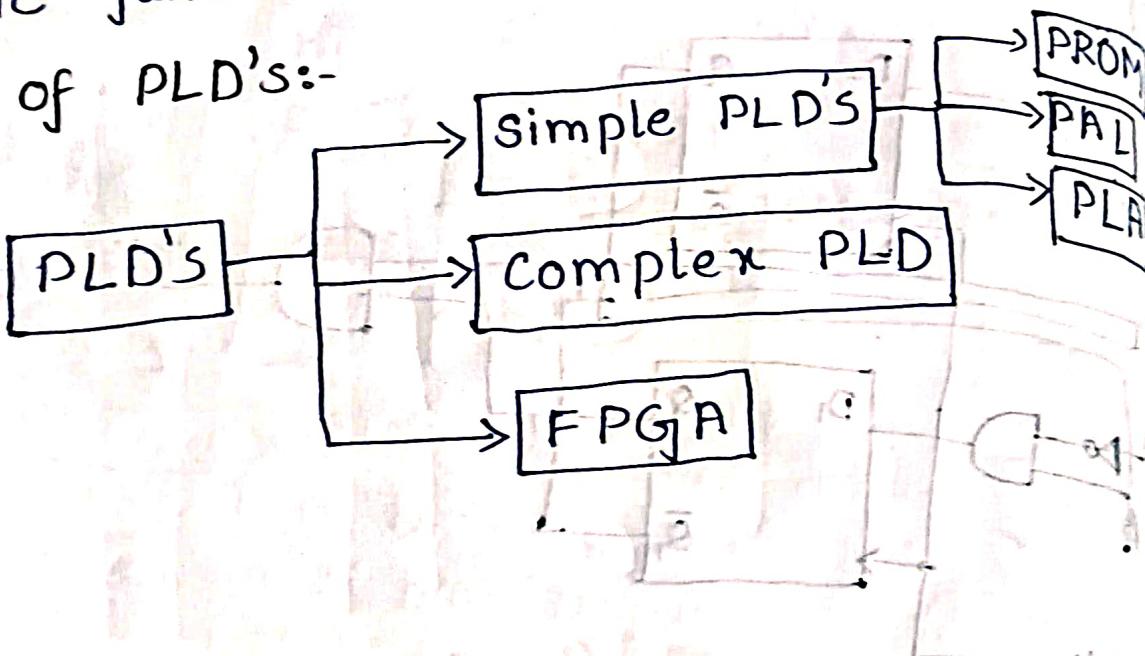
CU-4 Programmable logic devices.

- * PLD's are used to elaborate the various designs to be implemented by using single device.
- * These can be erased electrically and reprogrammed with a new design.

Programmable logic devices:-

A PLD is a type of digital integrated circuit that can be programmed by the user to implement various logic functions.

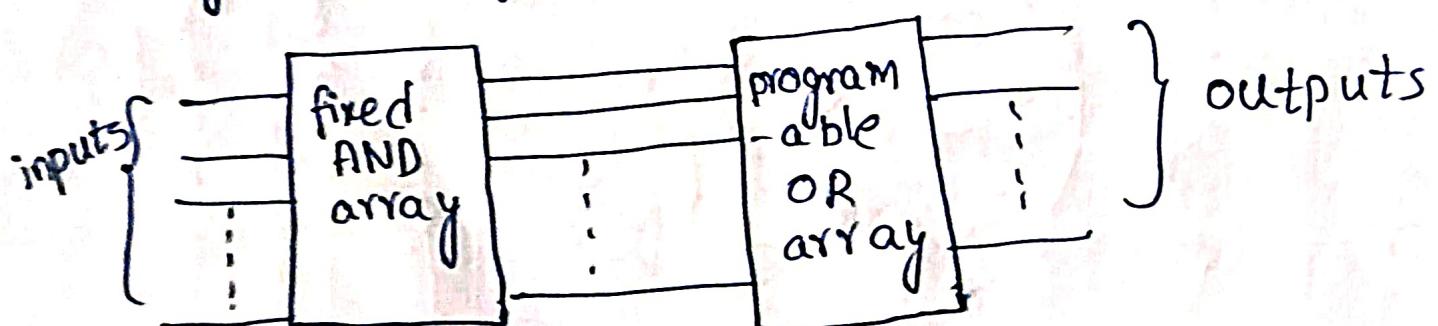
Classification of PLD's:-



PROM:-

Programmable read only memory.

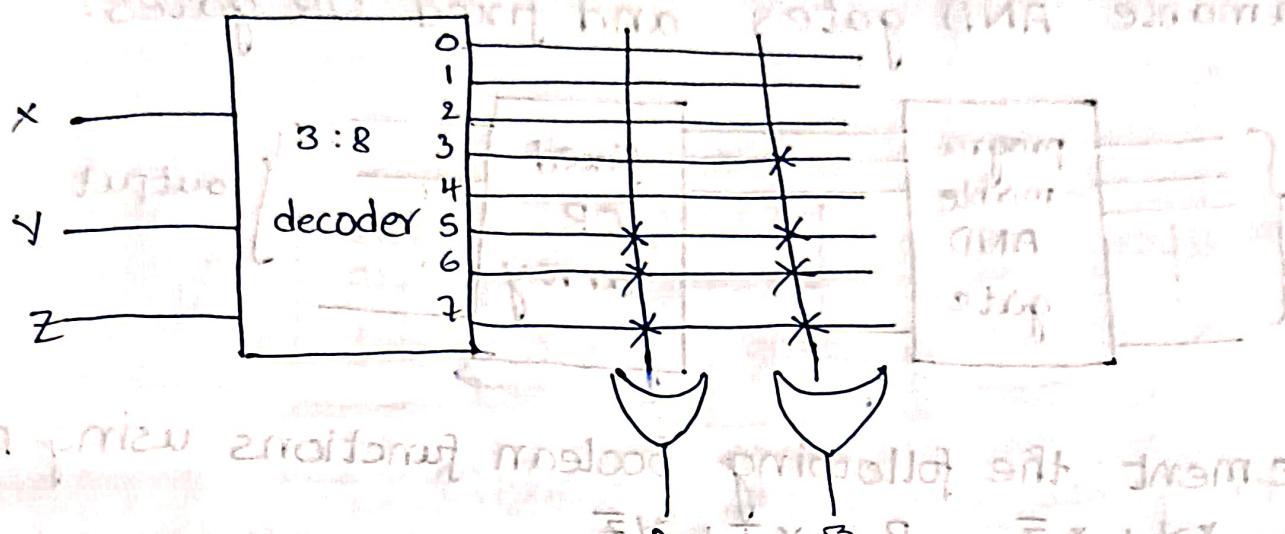
PROM is a programmable logic device that has fixed AND array and programmable OR array.



Examples:- Implement the following functions using PROM

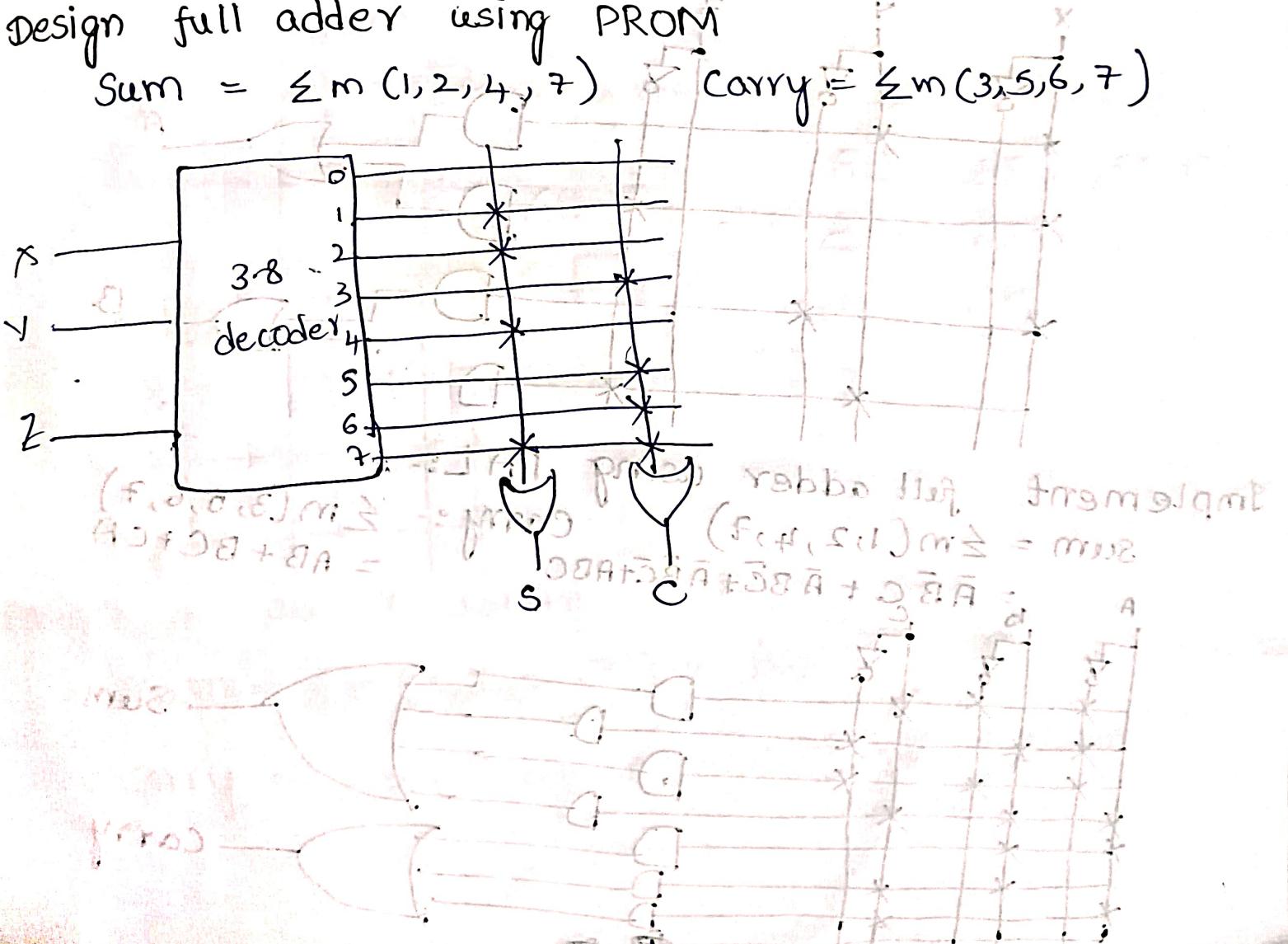
$$(i) A(x, y, z) = \sum m(5, 6, 7)$$

$$(ii) B(x, y, z) = \sum m(0, 3, 5, 6, 7)$$



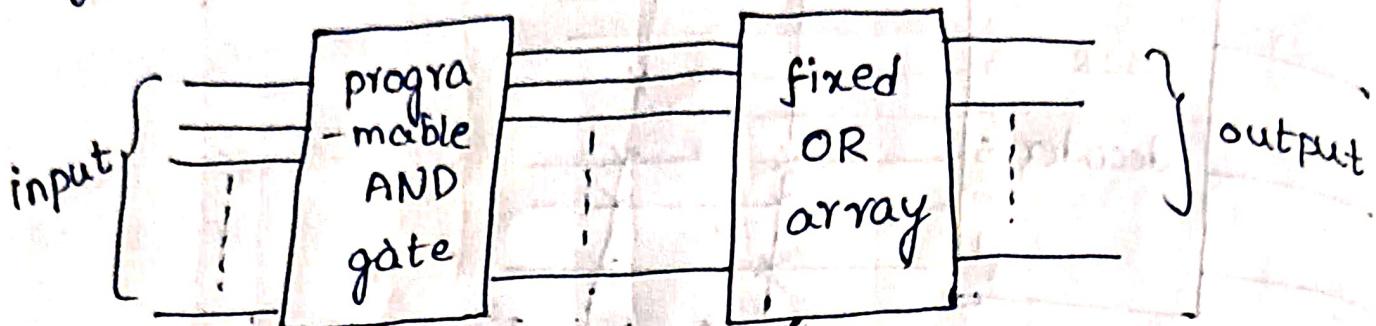
Design full adder using PROM

$$\text{Sum} = \sum m(1, 2, 4, 7) \quad \text{Carry} = \sum m(3, 5, 6, 7)$$



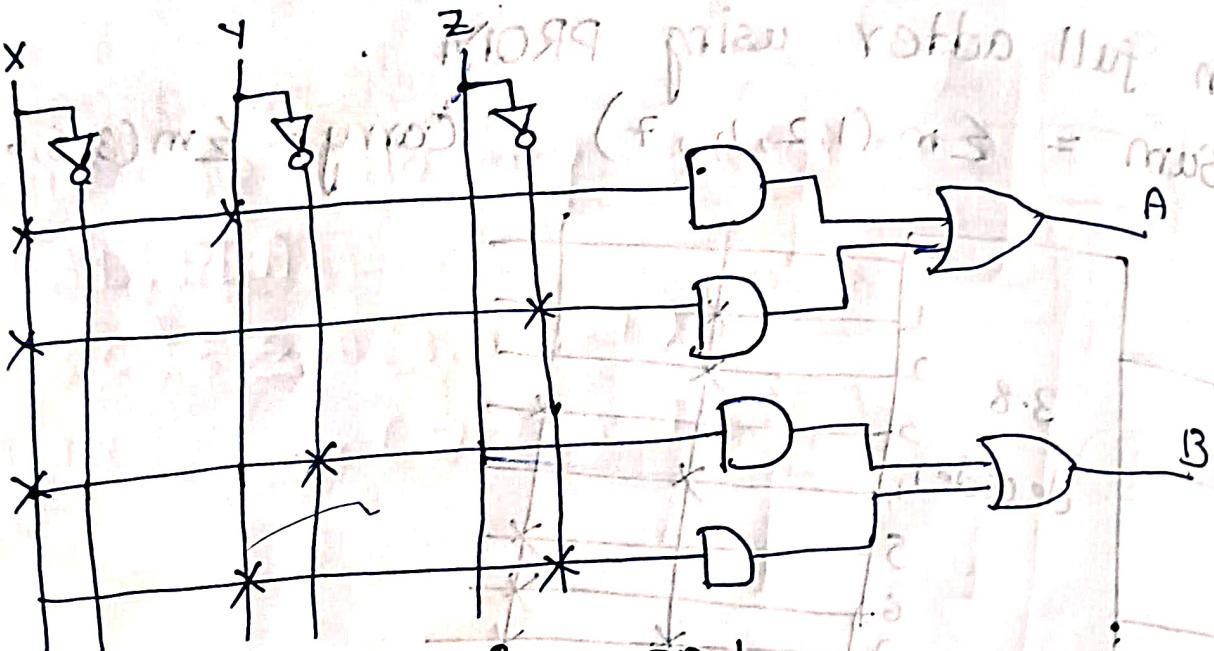
PAL:- (Programmable array logic):-

PAL is a programmable logic device that has programmable AND gates and fixed OR gates.



Implement the following boolean functions using PAL

$$A = XY + X\bar{Z} \quad B = X\bar{Y} + Y\bar{Z}$$

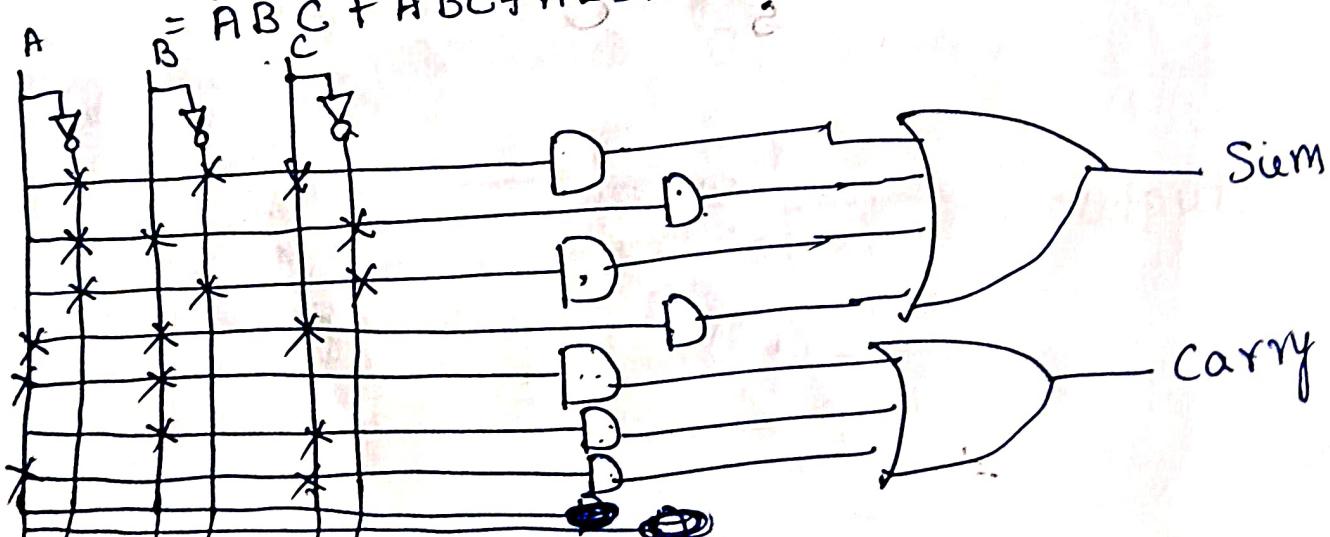


Implement full adder using PAL:-

$$\text{sum} = \sum m(1, 2, 4, 7)$$

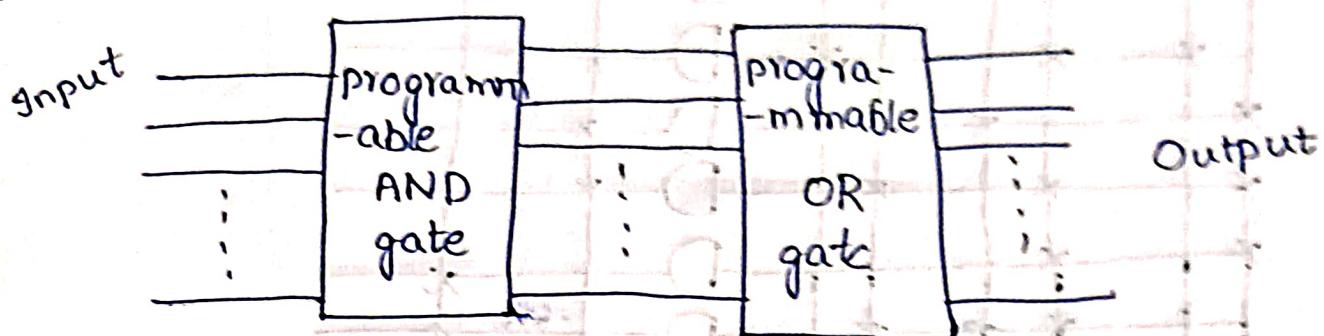
$$= \bar{A}\bar{B}C + \bar{A}B\bar{C} + \bar{A}\bar{B}\bar{C} + ABC$$

$$\begin{aligned} \text{carry} &= \sum m(3, 5, 6, 7) \\ &= AB + BC + CA \end{aligned}$$



PLA (Programmable logic array):-

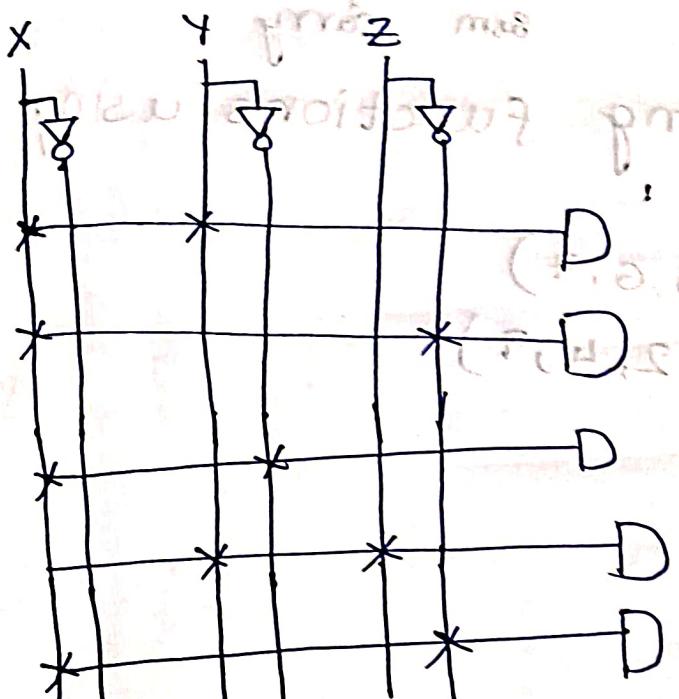
PLA is a programmable logic device that has both programmable AND gates and programmable OR gates.



Implement the following functions using PLA

$$A = XY + X\bar{Z}$$

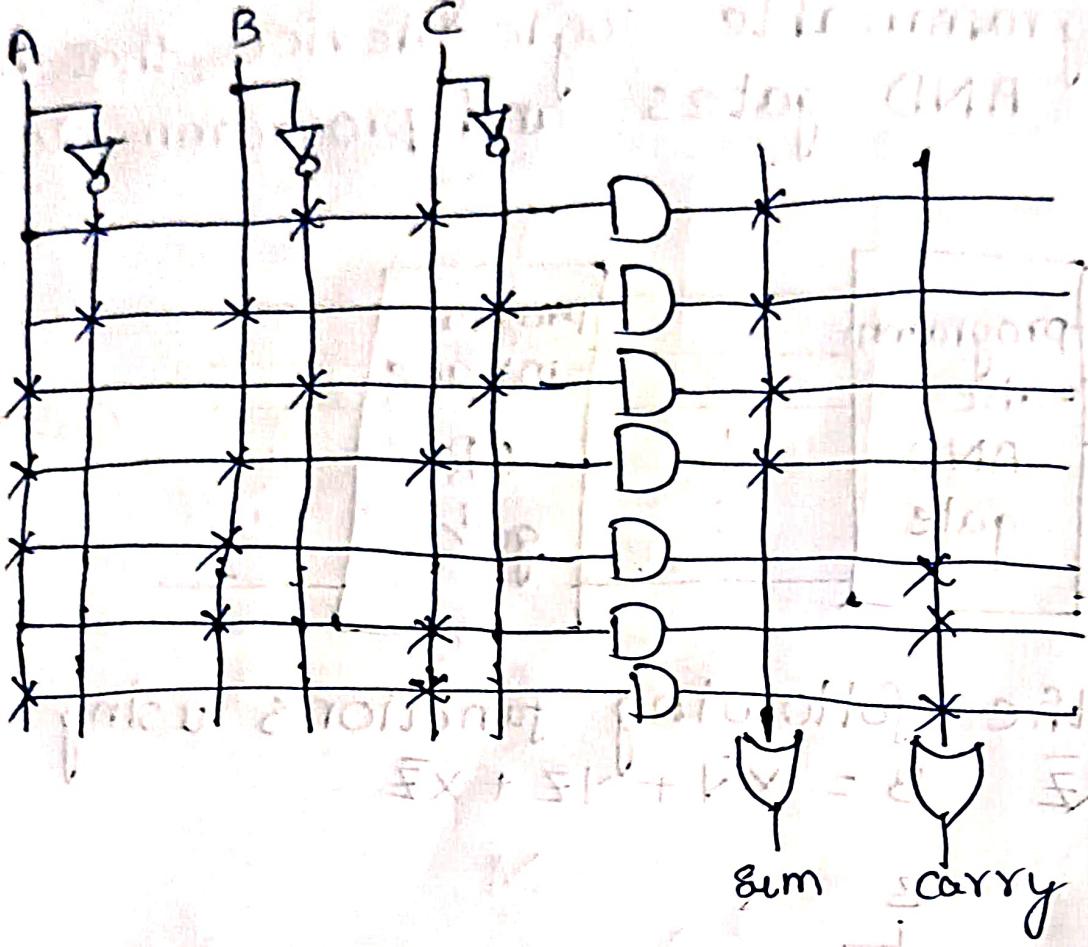
$$B = X\bar{Y} + YZ + X\bar{Z}$$



Design full adder using PLA:-

$$\text{Sum} = \sum m(1, 2, 4, 7) = \bar{A}\bar{B}C + \bar{A}B\bar{C} + A\bar{B}\bar{C} + ABC$$

$$\text{Carry} = \sum m(3, 5, 6, 7) = AB + BC + AC$$



Implement the following functions using PROM, PAL, PLA.

$$(i) F_1(A, B, C) = \sum m(3, 5, 6, 7)$$

$$(ii) F_2(A, B, C) = \sum m(0, 2, 4, 7)$$

