KEVIN LYNCH

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126 Montgomery Street 3A ◇ Highland Park, NJ 08904

EDUCATION

Master of Science in Computer Science *January 2010*
Drexel University Philadelphia, PA

Bachelor of Science in Computer Science *Summa Cum Laude* *September 2007*
Drexel University Philadelphia, PA
Minors in Mathematics and Philosophy

TECHNICAL STRENGTHS

Languages	Python, Ruby, Shell, C/C++, Java, JavaScript, SQL, XSLT, Lisp, Stratego
Frameworks	Django, Ruby on Rails, Android, GWT, Java EE, Spring
Software	PostgreSQL, MySQL, Apache, Tomcat, JBoss, Amazon Web Services, KVM, ESXi, VirtualBox, Git, SVN, Emacs, Eclipse, Jenkins, Sonar, Maven, Puppet, Chef, Nagios, Cisco IOS
Patents	"Detection, Diagnosis, And Mitigation Of Software Faults" (PCT/US2011/022846)

PROFESSIONAL EXPERIENCE

Drakontas LLC June 2012 - present
Lead Software Engineer *Glenside, PA*

- Leads the design and development of a web and mobile based command and control system for the security sector
- Introduced engineering and operations best practices to automate the deployment, testing, and recovery processes
- Streamlined the release process to integrate seamlessly with resellers' business and production environments
- Consults with customers in the specification of new requirements, and lead the design and implementation of the services necessary to satisfy the requirements

Drexel University March 2008 - June 2012
Graduate Research Assistant *Philadelphia, PA*

- Designed an autonomic framework using computational geometry to detect unhealthy states using runtime metrics
- Designed robust general and application specific mitigations for faults and other Quality of Service violations
- Explored the effects of mitigations on faults and QoS violations with no prior knowledge of monitored system
- Developed a system to efficiently collect large amounts of runtime metrics, perform fault detection and classification, and apply appropriate mitigations in real-time

Lockheed Martin Advanced Technology Laboratories September 2008 - December 2010
Research Intern *Cherry Hill, NJ*

- Designed a dynamically configurable and schedulable testbed for analysis of heterogeneous and real-time systems
- Developed tools for automatic source-to-source transformations of code segments to run on heterogeneous architectures
- Developed a framework to support dynamic loading and hotswapping of components for heterogeneous architectures

ACIN Program for Warfigher Support September 2005 - September 2007
Research Engineer *Camden, NJ*

- Researched methods for detecting reverse engineering tools running directly on a system or in a virtualized environment
- Applied program analyis techniques to improve the correctness and robustness of a communications software system
- Evaluated the reliability, robustness, and efficiency of delay-tolerant networks on large-scale virtualized radio networks

Intel Corporation September 2004 - March 2005
Validation Engineer *Hudson, MA*

- Performed validation work on the system interface of multi-core Itanium2 and Xeon processors
- Analyzed and validated parts of the processor HDL code, simulating it's functionality in C++
- Tested and analyzed coverage for the router box of the system interface