

UEC612 Digital System Design – 2018 Odd Semester @TIET

LAB 1

Aim: Implement the given Boolean expression using basic logic gates and generate its truth table, Convert the implementation to NAND-NAND implementation and verify the truth table.

$$F = (A \oplus B) + \overline{C}D$$

LAB 2

Aim: A 7-bit Hamming coded data [1000111] is received at the end in the communication system. a) Design a circuit to check whether the given code is correct or not. b) If not, then correct it & display the correct data on 7 segment display.