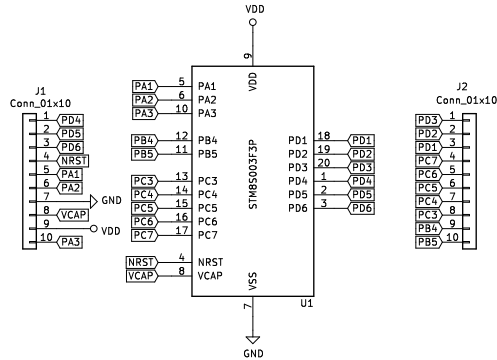
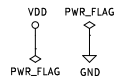


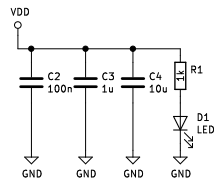
STM8S003F3P GPIO



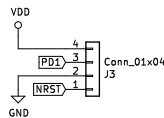
Power flags



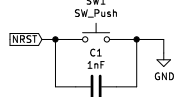
Power supply



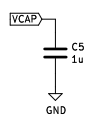
Programmer Input



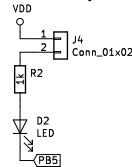
NRST pin



VCAP pin



PB5 test pin



Kamil Mężyński

Sheet: /
File: STM8_board.sch

Title: STM8S003F3P development board

Size: A4 Date: 2019-11-08
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Rev: 1.3
Id: 1/1