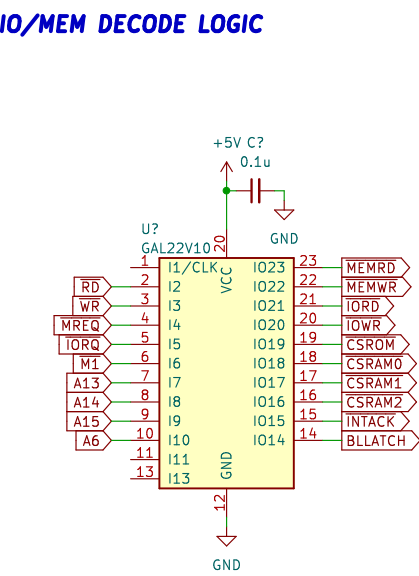
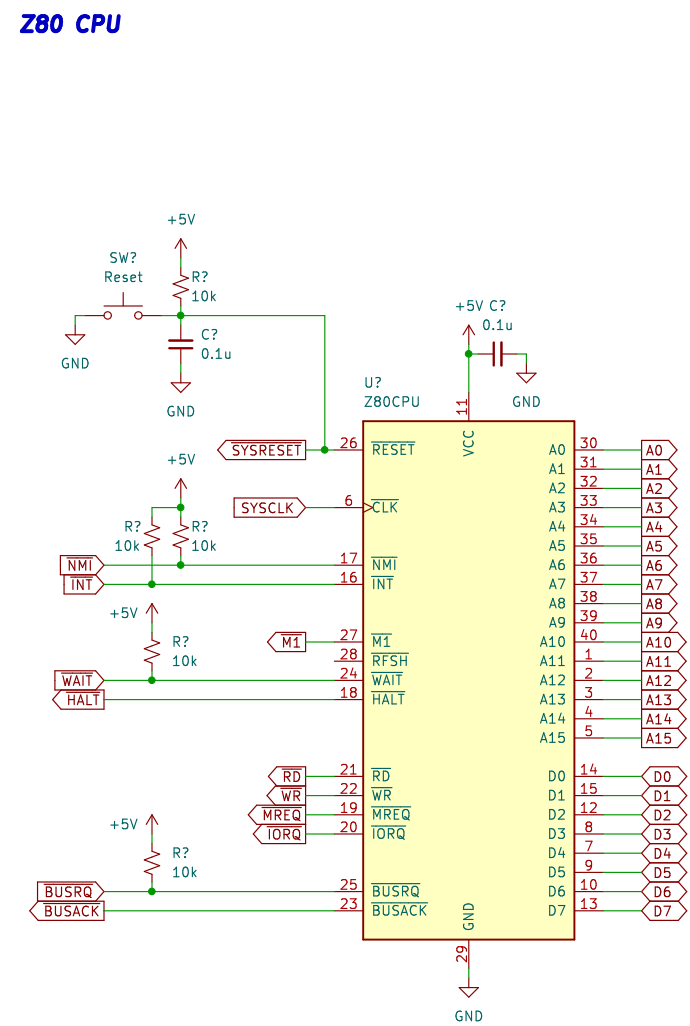


COOF-80 MICROCOMPUTER



IO/MEM Decoder PLD Equations

```
/* INPUT */
PIN 2 = !RD;
PIN 3 = !WR;
PIN 4 = !MEMRQ;
PIN 5 = !IORQ;
PIN 6 = !M1;
PIN 7 = A13;
PIN 8 = A14;
PIN 9 = A15;
PIN 10 = A6;

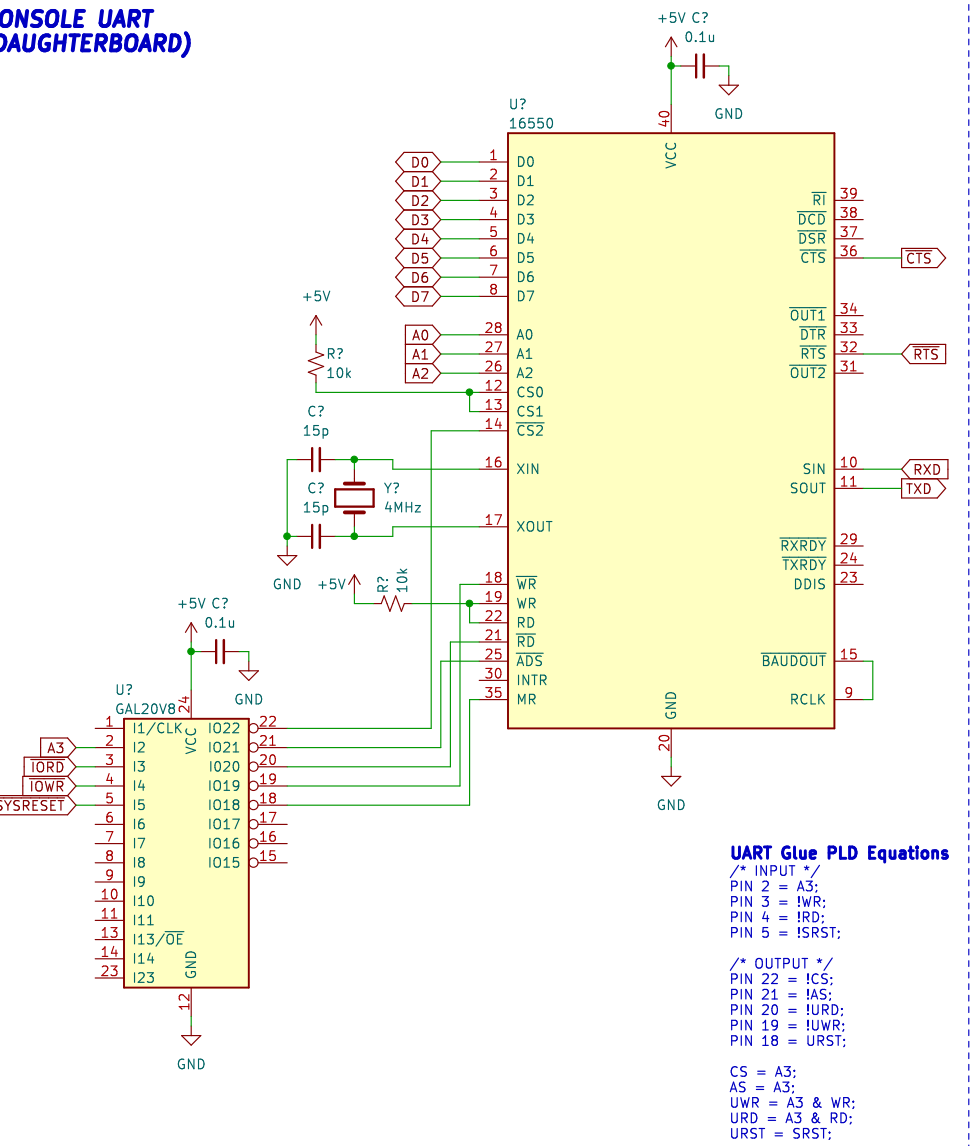
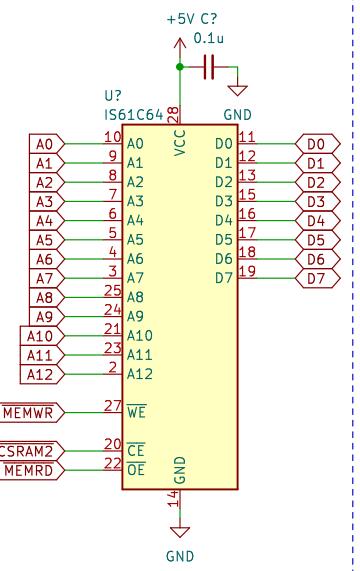
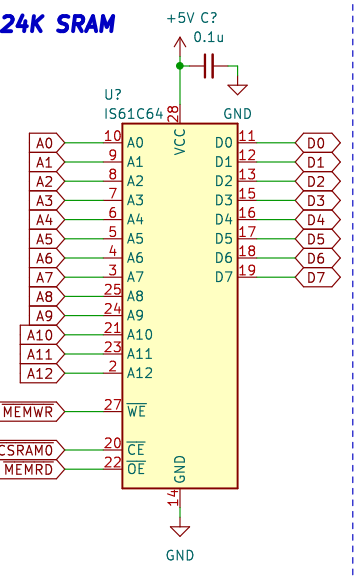
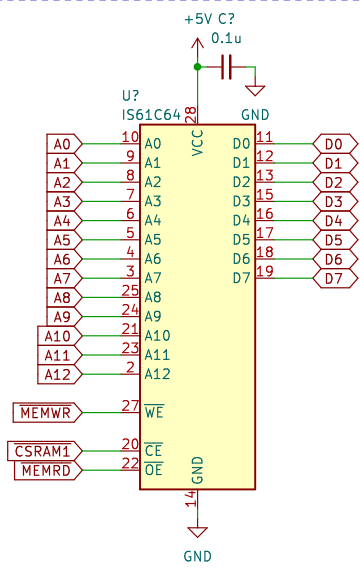
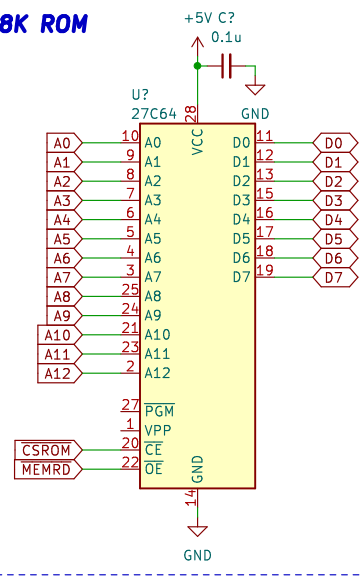
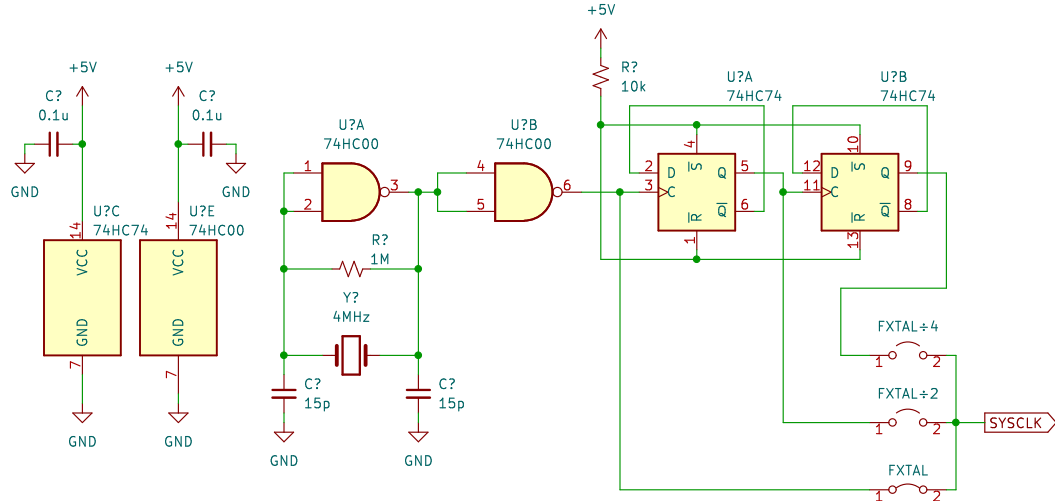
/* OUTPUT */
PIN 23 = !MEMRD;
PIN 22 = !MEMWR;
PIN 21 = !IORD;
PIN 20 = !IOWR;
PIN 19 = !CSROM;
PIN 18 = !CSRAM0;
PIN 17 = !CSRAM1;
PIN 16 = !CSRAM2;
PIN 15 = !INTACK;
PIN 14 = !BLLATCH;

CSROM = MEMRQ & !A13 & !A14 & !A15;
CSRAM0 = MEMRQ & A13 & !A14 & !A15;
CSRAM1 = MEMRQ & !A13 & A14 & !A15;
CSRAM2 = MEMRQ & A13 & A14 & !A15;

IORD = IORQ & !MEMRQ & RD & !WR;
IOWR = IORQ & !MEMRQ & !RD & WR;
MEMRD = !IORQ & MEMRQ & RD & !WR;
MEMWR = !IORQ & MEMRQ & !RD & WR;

INTACK = IORQ & M1;
BLLATCH = IORQ & !MEMRQ & !RD & WR & A6;
```

SYSTEM CLOCK

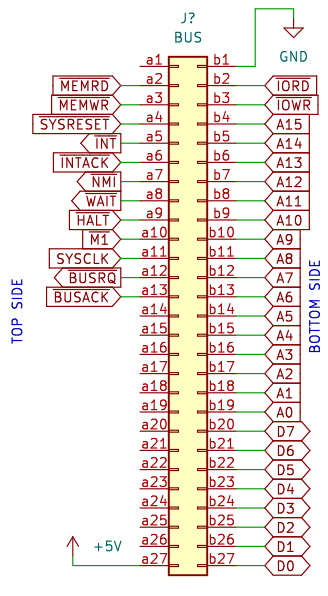


UART Glue PLD Equations

```
/* INPUT */
PIN 2 = A3;
PIN 3 = !WR;
PIN 4 = !RD;
PIN 5 = !SRST;

/* OUTPUT */
PIN 22 = !CS;
PIN 21 = !AS;
PIN 20 = !URD;
PIN 19 = !UWR;
PIN 18 = !URST;
```

EXPANSION INTERFACE



BLINKENLIGHTS

