```
module max_min(a, b, maximum, minimum);

input unsigned [7:0] a; // input number a
input unsigned [7:0] b; // input number b
output unsigned [7:0] maximum; // greater number output
output unsigned [7:0] minimum; // smaller number output

assign minimum = (a<b) ? a:b; // if a<b, a is the smaller number, otherwise it's b
assign maximum = (a>b) ? a:b; // if a>b, a is the bigger number, otherwise it's b
endmodule
```

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