

U\_01  
Power1.SchDoc



U\_01  
03.SchDoc



U\_01  
05.SchDoc



U\_01  
07.SchDoc



U\_01  
Power2.SchDoc



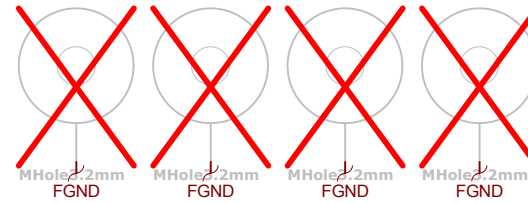
U\_01  
04.SchDoc



U\_01  
06.SchDoc



U\_01  
Power3.SchDoc



LOGO1

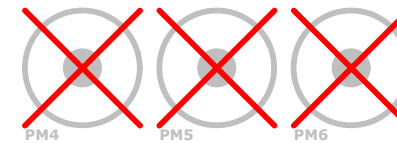
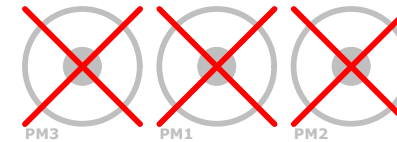


LOGO TOP small

WEEE



WEEE TOP



Title: **Overview**

A4

Nummer: **TE630**

Rev. **00**

Datum: **2011-08-23**

Zeichner: **Trenz Electronic GmbH / TT**

Blatt 1 von 9

Filename: **00.SchDoc**

1

2

3

4

A

A

B

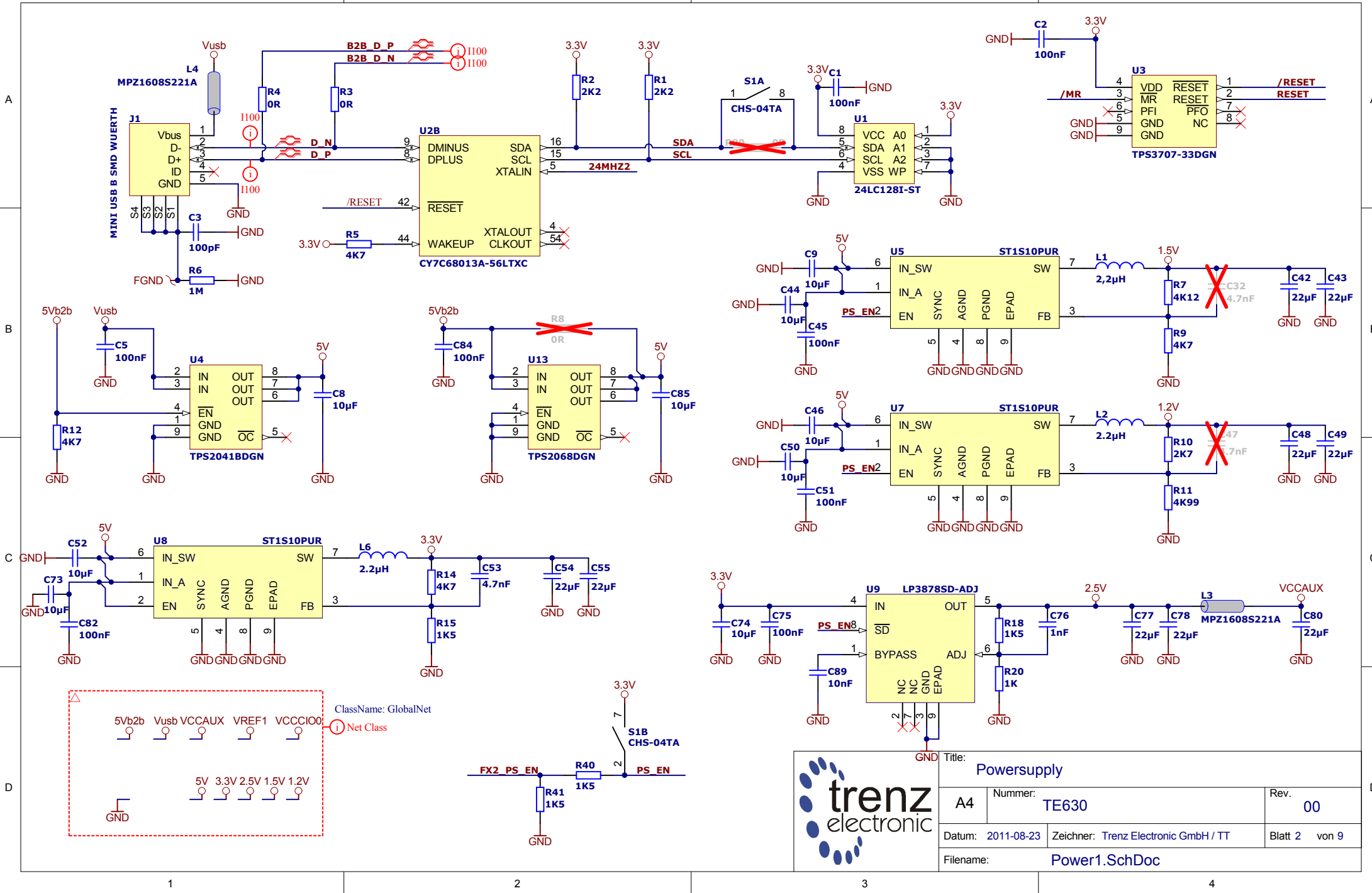
B

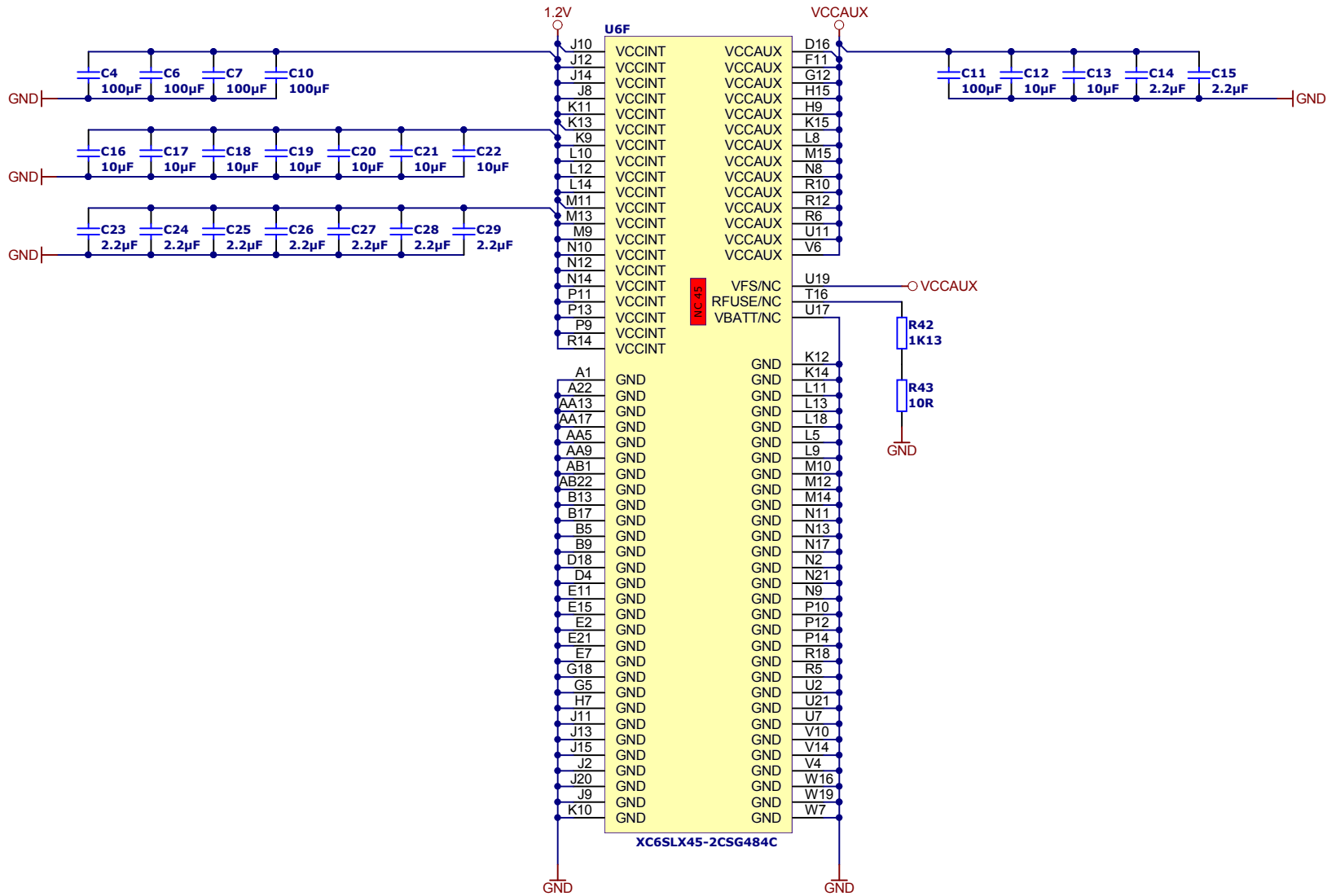
C


C

D

D





		Title: Chip Powersupply	
		A4	Rev. 00
Datum: 2011-08-23		Zeichner: Trenz Electronic GmbH / TT	Blatt 3 von 9
Filename: Power2.SchDoc			

1

2

3

4

A

A

B

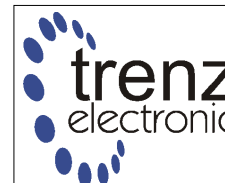
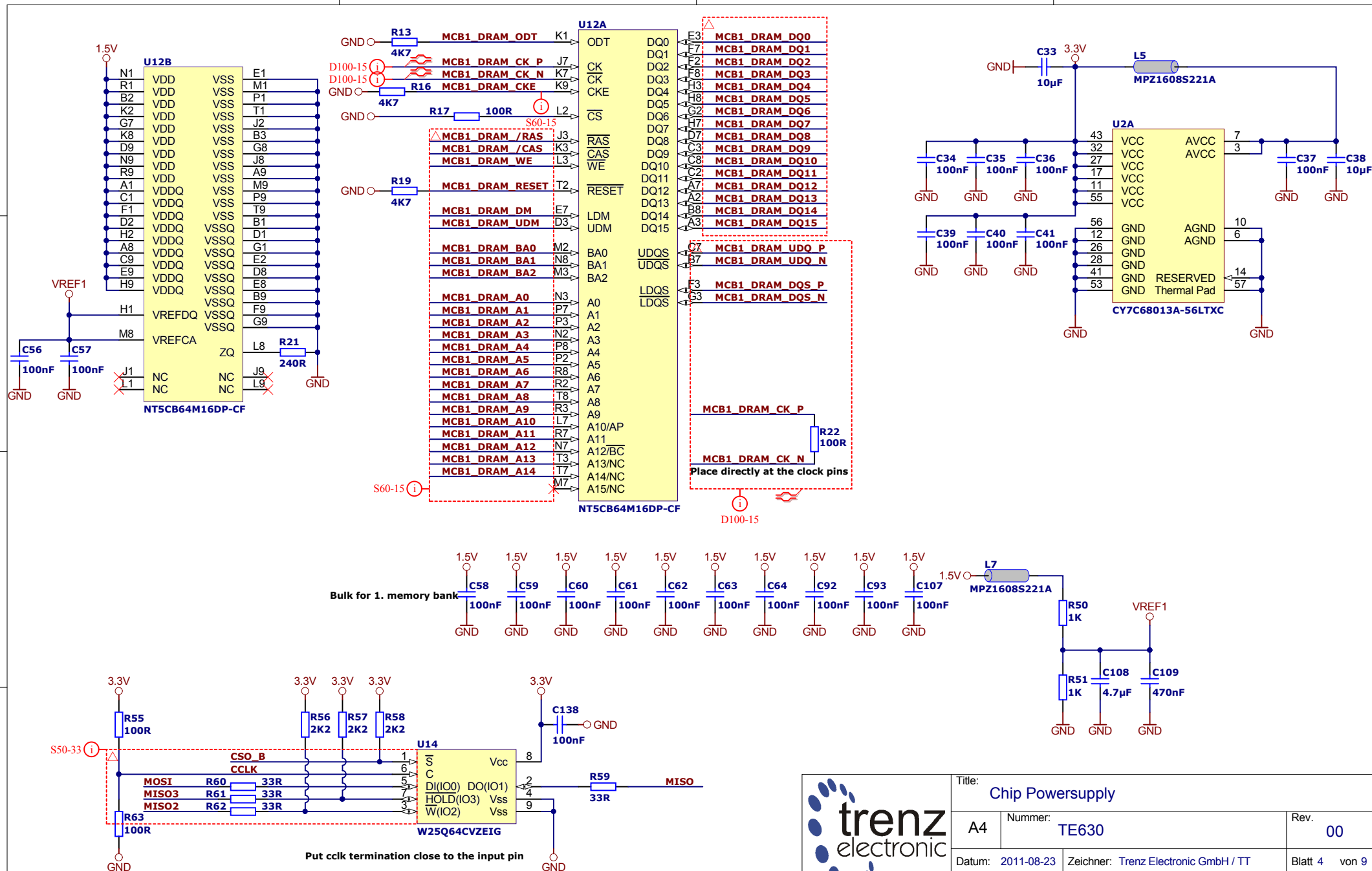
B

C

C

D

D



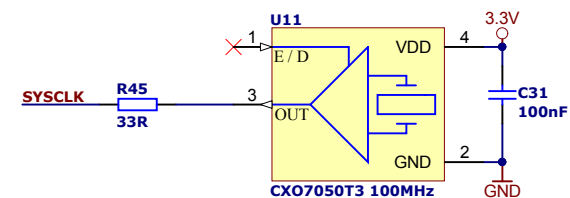
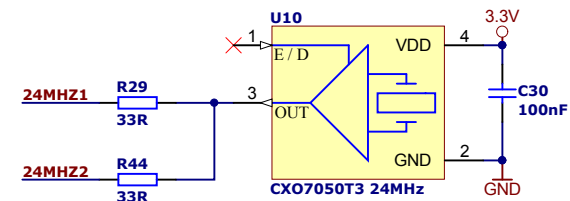
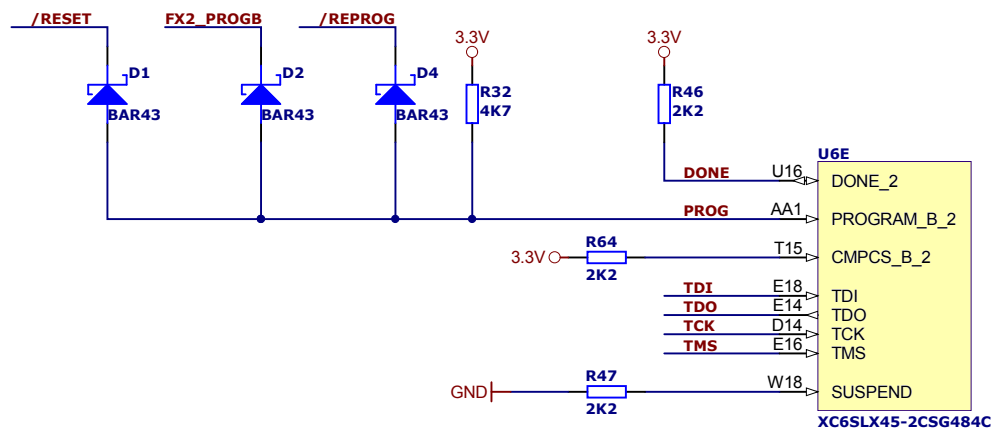
Title: <b>Chip Powersupply</b>			
A4	Number: <b>TE630</b>		Rev. <b>00</b>
Datum: <b>2011-08-23</b>	Zeichner: <b>Trenz Electronic GmbH / TT</b>		Blatt <b>4</b> von <b>9</b>
Filename: <b>Power3.SchDoc</b>			

1

2

3

4

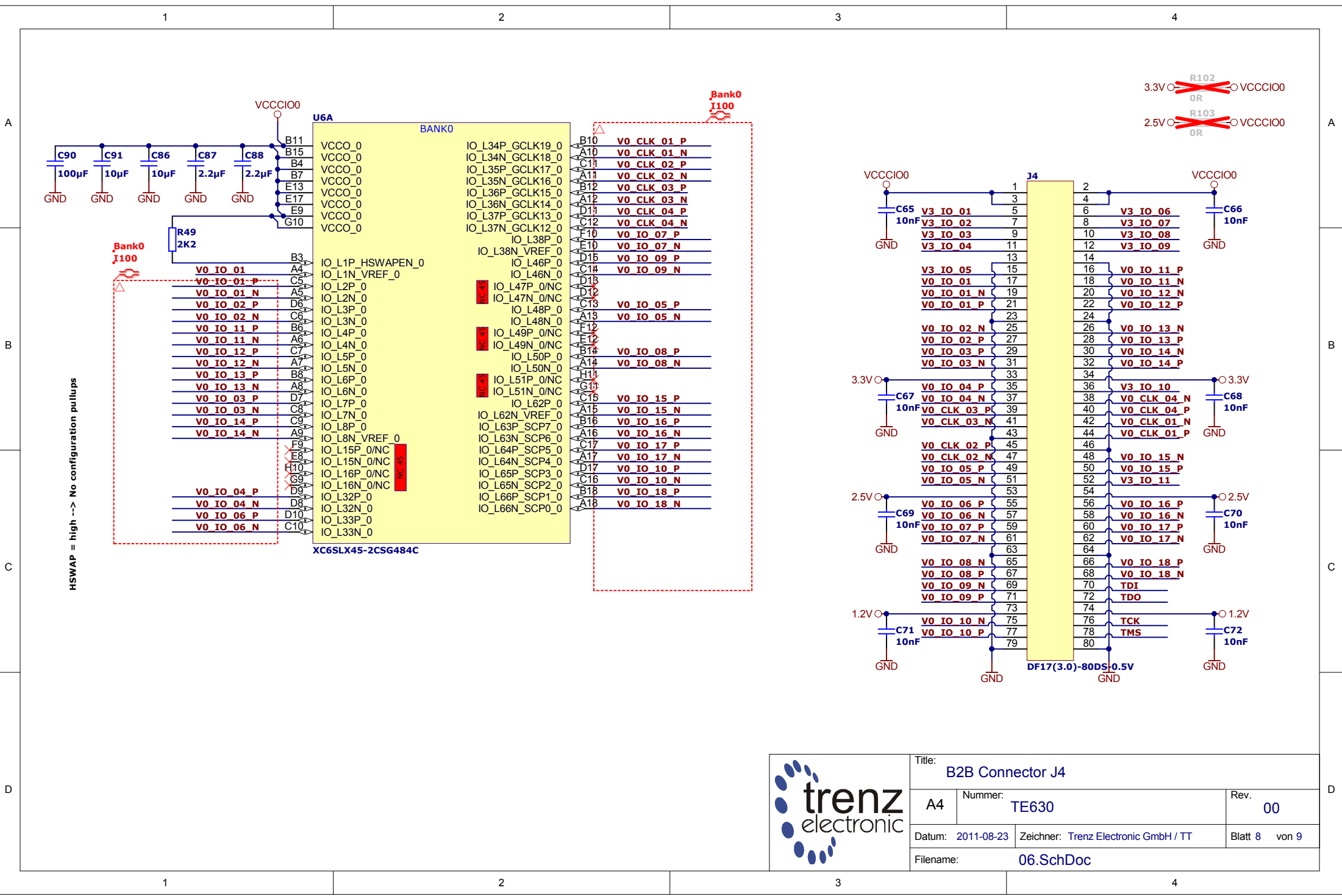



	Title: Configuration and Clock		
	A4	Nummer: TE630	Rev. 00
	Datum: 2011-08-23	Zeichner: Trenz Electronic GmbH / TT	Blatt 5 von 9
	Filename: 03.SchDoc		









		Title: B2B Connector J4	
		A4	Rev. 00
Datum: 2011-08-23		Number: TE630	Blatt 8 von 9
Zeichner: Trenz Electronic GmbH / TT		Filename: 06.SchDoc	



