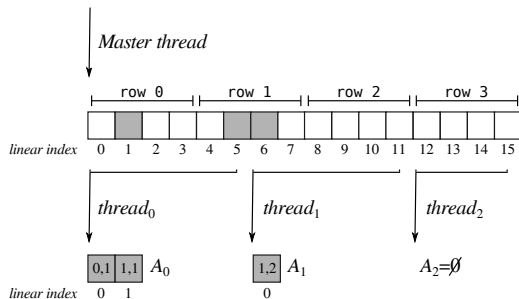
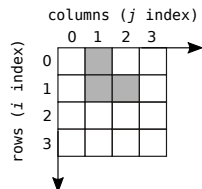


step  $t$

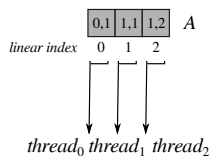
Active cells configuration



scattered array  $F$

Parallel Stream Compaction

Master thread

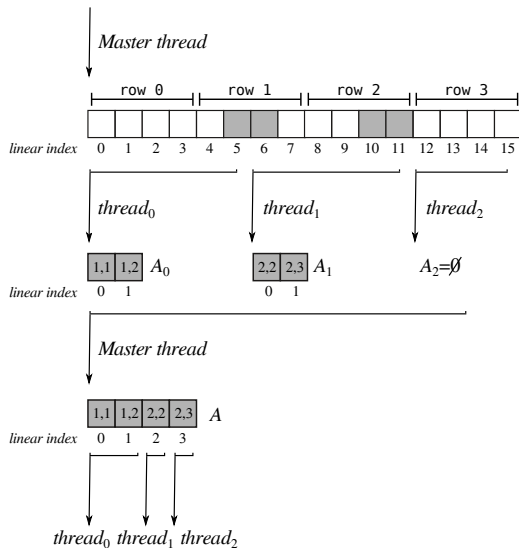
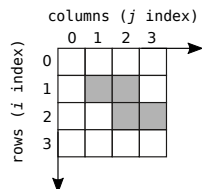


compacted array  $A$

state transition

step  $t+1$

Active cells configuration



scattered array  $F$

Parallel Stream Compaction

compacted array  $A$