CS302:

Assignment9 Report

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1 Answer of Question 1

When a process accesses a memory page that is not present in physical memory, a page fault occurs. The page fault exception will be triggered then trapped into kernel to let OS to handle. OS will first go to the **page fault handler** to determine what to do. The handler then check if there are **free** page frame in the physical memory to be allocated. If not, then issue a request to fetch a page from the disk and to **replace** a page in the physical memory according to some page replacement policy. When the disk I/O completes, the OS will then update the page table to mark the page as present, update the PFN of PTE, and retry the instruction returned from the exception handler. After that a TLB miss will happen then the TLB will be updated.

2 Answer of Question 2

The implementation of the functions are shown below.

(a) Init function and Swappable Function

```
static int
clock, sup_out_victin(struct am_struct *um, struct Page **ptr page, int in_tick)
{
/ TROD
/ TROD
/ Iss entry t *head = (list_entry_t *)um->sn_priv;
assert(int int is = 0);
list_entry_t *entry = head;
for page = BULL;
for page = BULL;
for page = le2page(entry, pra_page_link);
pit_t *ptep = vet_pries = vet_page = vet_p
```

(b) Swap Out Victim Function

Fig. 1: Implementation

The results are shown below.

```
write Virt Page a in clock_check_swap
Store/AMD page fault
page falut a 0x000050000: K/W
swap out: 1 0, store page in vaddr 0x1000 to disk swap entry 2
write Virt Page b in clock_check_swap
store/AMD page fault
page falut at 0x00001000: K/W
swap.out: 1 0, store page in vaddr 0x3000 to disk swap entry 4
swap.out: 1 0, store page in vaddr 0x3000 to disk swap entry 4
swap.out: 1 0, store page in vaddr 0x3000 to disk swap entry 4
write Virt Page b in clock_check_swap
store/AMD page fault
page falut at 0x00003000: K/W
swap.out: 1 0, store page in vaddr 0x4000 to disk swap entry 5
swap.in: load disk swap entry 4 with swap page in vadr 0x3000
write Virt Page c in clock_check_swap
store/AMD page fault
page falut at 0x00003000: K/W
swap.out: 1 0, store page in vaddr 0x4000 to disk swap entry 5
swap.in: load disk swap entry 6
swap.in: load disk swap entry 5 with swap.page in vadr 0x4000
write Virt Page e in clock_check_swap
store/AMD page fault
page falut at 0x00005000: K/W
swap.out: 1 0, store page in vaddr 0x5000 to disk swap entry 6
swap.in: load disk swap entry 5 with swap.page in vadr 0x4000
write Virt Page e in clock_check_swap
store/AMD page fault
page falut at 0x000005000: K/W
swap.out: 1 0, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 1 0, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 1 0, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 1 0, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 1 0, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 1 0, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 1 0, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 10, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 10, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 10, store page in vaddr 0x2000 to disk swap entry 3
swap.out: 10, store page in vaddr 0
```

Fig. 2: Final Results