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| **Lab 5** | |
| 學號: 123456789 | 姓名: 姓名 |

// This is the template of the report. Remove all the blue comments/parts before submission.

// 繳交前記得移除藍色註解/說明。

// You may extend the sections or add additional one.

// 你可以延伸以下內容，或加入額外章節。

1. **Lab Implementation**

You may elaborate on the followings.

* + - 1. Block diagram of the design with explanation
      2. Partial code screenshot with the explanation: you don’t need to paste the entire code into the report. Just explain the kernel part.
      3. Event-based finite state machine (FSM) with the explanation:

Note1: Use events to describe the transition of FSM but not signals logic statement.

Note2: You don’t need to do this part if there is no FSM in this lab.

1. **Questions and Discussions**

Provide your answer to the Questions and Discussions in the lab assignment.

1. **Problem Encountered**

Describe the problems you encountered, solutions you developed, and the discussion. Explaining them with code segments or diagrams is recommended.

1. **Suggestions**

Any suggestions for this course are more welcome.

(If not, you may also post a joke. A funny one, please. It is not mandatory and has nothing to do with the grading. But it would undoubtedly amuse us. ☺)