



Application Note AN1200.73:

SX126x/LR11xx

SIGFOX® Modulation

Table of Contents

1	Introduction.....	8
1.1	Purpose of This Document	8
1.2	Scope of This Document	8
1.3	Disclaimer	8
2	Sigfox Library	9
2.1	API Functions.....	9
2.2	Sigfox Library Usage.....	9
3	Sigfox Code Example	10
4	Sending BPSK Packets With SX126x	11
5	Sending BPSK Packets With LR11xx.....	12
6	Sigfox Compliance	13
6.1	General Compliance Notes.....	13
6.1.1	PRS-RFP-004, Static Frequency Tolerance	13
6.1.2	PRS-RFP-010, Sigfox Modulation.....	14
6.1.3	PRS-RFP-011, Phase Measurement.....	14
6.1.4	PRS-RFP-016, Power Spectral Density For Class 0 and 1.....	14
6.1.5	PRS-RFP-017, Power Spectral Density For Class 0 and 1.....	14
6.1.6	PRS-RFP-018	14
6.1.7	PRS-RFP-070.....	14
6.2	LR11xxMB1DJS Measurement Results, TCXO, Radio Configuration RC1	15
6.2.1	Device Configuration	15
6.2.2	FFT	16
6.2.3	Demodulation	16
6.2.4	Power Spectral Density for Class 0 & 1	17
6.2.5	Full FFT.....	17
6.2.6	Data Rate.....	18
6.2.7	Data Rate Cumulated Error	18
6.2.8	Extra Symbol Before Transmission.....	19
6.2.9	Extra Symbol After Transmission	19
6.2.10	Dynamic Drift	20
6.2.11	Dynamic Drift per Second.....	20
6.2.12	Phase	21
6.2.13	Envelope.....	21
6.2.14	Verdict	22
6.3	LR11xxMB1DJS Measurement Results, TCXO, Radio Configuration RC2	25
6.3.1	Device Configuration	25
6.3.2	FFT	26

6.3.3	Demodulation	26
6.3.4	Power Spectral Density for Class 0 & 1	27
6.3.5	Full FFT.....	27
6.3.6	Data Rate.....	28
6.3.7	Data Rate Cumulated Error	28
6.3.8	Extra Symbol Before Transmission.....	29
6.3.9	Extra Symbol After Transmission	29
6.3.10	Dynamic Drift	30
6.3.11	Dynamic Drift per Second.....	30
6.3.12	Phase	31
6.3.13	Envelope.....	31
6.3.14	Verdict	32
6.4	SX1261MB1BAS Measurement Results, XTAL, Radio Configuration RC1.....	35
6.4.1	Device Configuration	35
6.4.2	FFT	36
6.4.3	Demodulation	36
6.4.4	Power Spectral Density for Class 0 & 1	37
6.4.5	Full FFT.....	37
6.4.6	Data Rate.....	38
6.4.7	Data Rate Cumulated Error	38
6.4.8	Extra Symbol Before Transmission.....	39
6.4.9	Extra Symbol After Transmission	39
6.4.10	Dynamic Drift	40
6.4.11	Dynamic Drift per Second.....	40
6.4.12	Phase	41
6.4.13	Envelope.....	41
6.4.14	Verdict	42
6.5	SX1261MB1BAS Measurement Results, XTAL, Radio Configuration RC2.....	45
6.5.1	Device Configuration	45
6.5.2	FFT	46
6.5.3	Demodulation	46
6.5.4	Power Spectral Density for Class 0 & 1	47
6.5.5	Full FFT.....	47
6.5.6	Data Rate.....	48
6.5.7	Data Rate Cumulated Error	48
6.5.8	Extra Symbol Before Transmission.....	49
6.5.9	Extra Symbol After Transmission	49
6.5.10	Dynamic Drift	50
6.5.11	Dynamic Drift per Second.....	50

6.5.12	Phase	51
6.5.13	Envelope.....	51
6.5.14	Verdict	52
6.6	SX1262DVK1DAS Measurement Results, TCXO, Radio Configuration RC1	55
6.6.1	Device Configuration	55
6.6.2	FFT	56
6.6.3	Demodulation	56
6.6.4	Power Spectral Density for Class 0 & 1	57
6.6.5	Full FFT.....	57
6.6.6	Data Rate.....	58
6.6.7	Data Rate Cumulated Error	58
6.6.8	Extra Symbol Before Transmission.....	59
6.6.9	Extra Symbol After Transmission	59
6.6.10	Dynamic Drift	60
6.6.11	Dynamic Drift per Second.....	60
6.6.12	Phase	61
6.6.13	Envelope.....	61
6.6.14	Verdict	62
6.7	SX1262DVK1DAS Measurement Results, TCXO, Radio Configuration RC2	65
6.7.1	Device Configuration	65
6.7.2	FFT	66
6.7.3	Demodulation	66
6.7.4	Power Spectral Density for Class 0 & 1	67
6.7.5	Full FFT.....	67
6.7.6	Data Rate.....	68
6.7.7	Data Rate Cumulated Error	68
6.7.8	Extra Symbol Before Transmission.....	69
6.7.9	Extra Symbol After Transmission	69
6.7.10	Dynamic Drift	70
6.7.11	Dynamic Drift per Second.....	70
6.7.12	Phase	71
6.7.13	Envelope.....	71
6.7.14	Verdict	72
7	Conclusion	75
8	Glossary	76
9	Revision History.....	77

List of Figures

Figure 1: LR11xxMB1DJS RC1 Device Configuration	15
Figure 2: LR11xxMB1DJS RC1 FFT	16
Figure 3: LR11xxMB1DJS RC1 Demodulation	16
Figure 4: LR11xxMB1DJS RC1 Power Spectral Density for Class 0 & 1	17
Figure 5: LR11xxMB1DJS RC1 Full FFT	17
Figure 6: LR11xxMB1DJS RC1 Data Rate	18
Figure 7: LR11xxMB1DJS RC1 Data Rate Cumulated Error	18
Figure 8: LR11xxMB1DJS RC1 Extra Symbol Before Transmission	19
Figure 9: LR11xxMB1DJS RC1 Extra Symbol After Transmission	19
Figure 10: LR11xxMB1DJS RC1 Dynamic Drift	20
Figure 11: LR11xxMB1DJS RC1 Dynamic Drift per Second	20
Figure 12: LR11xxMB1DJS RC1 Phase	21
Figure 13: LR11xxMB1DJS RC1 Envelope	21
Figure 14: LR11xxMB1DJS RC1 Verdict page 1	22
Figure 15: LR11xxMB1DJS RC1 Verdict page 2	22
Figure 16: LR11xxMB1DJS RC1 Verdict page 3	23
Figure 17: LR11xxMB1DJS RC1 Verdict page 4	23
Figure 18: LR11xxMB1DJS RC1 Verdict page 5	24
Figure 19: LR11xxMB1DJS RC2 Device Configuration	25
Figure 20: LR11xxMB1DJS RC2 FFT	26
Figure 21: LR11xxMB1DJS RC2 Demodulation	26
Figure 22: LR11xxMB1DJS RC2 Power Spectral Density for Class 0 & 1	27
Figure 23: LR11xxMB1DJS RC2 Full FFT	27
Figure 24: LR11xxMB1DJS RC2 Data Rate	28
Figure 25: LR11xxMB1DJS RC2 Data Rate Cumulated Error	28
Figure 26: LR11xxMB1DJS RC2 Extra Symbol Before Transmission	29
Figure 27: LR11xxMB1DJS RC2 Extra Symbol After Transmission	29
Figure 28: LR11xxMB1DJS RC2 Dynamic Drift	30
Figure 29: LR11xxMB1DJS RC2 Dynamic Drift per Second	30
Figure 30: LR11xxMB1DJS RC2 Phase	31
Figure 31: LR11xxMB1DJS RC2 Envelope	31
Figure 32: LR11xxMB1DJS RC2 Verdict page 1	32
Figure 33: LR11xxMB1DJS RC2 Verdict page 2	32
Figure 34: LR11xxMB1DJS RC2 Verdict page 3	33
Figure 35: LR11xxMB1DJS RC2 Verdict page 4	33
Figure 36: LR11xxMB1DJS RC2 Verdict page 5	34
Figure 37: SX1261MB1BAS RC1 Device Configuration	35
Figure 38: SX1261MB1BAS RC1 FFT	36
Figure 39: SX1261MB1BAS RC1 Demodulation	36
Figure 40: SX1261MB1BAS RC1 Power Spectral Density for Class 0 & 1	37
Figure 41: SX1261MB1BAS RC1 Full FFT	37
Figure 42: SX1261MB1BAS RC1 Data Rate	38
Figure 43: SX1261MB1BAS RC1 Data Rate Cumulated Error	38
Figure 44: SX1261MB1BAS RC1 Extra Symbol Before Transmission	39
Figure 45: SX1261MB1BAS RC1 Extra Symbol After Transmission	39
Figure 46: SX1261MB1BAS RC1 Dynamic Drift	40
Figure 47: SX1261MB1BAS RC1 Dynamic Drift per Second	40
Figure 48: SX1261MB1BAS RC1 Phase	41
Figure 49: SX1261MB1BAS RC1 Envelope	41
Figure 50: SX1261MB1BAS RC1 Verdict Page 1	42
Figure 51: SX1261MB1BAS RC1 Verdict Page 2	42
Figure 52: SX1261MB1BAS RC1 Verdict Page 3	43
Figure 53: SX1261MB1BAS RC1 Verdict Page 4	43

Sigfox Modulation

5 of 78

Figure 54: SX1261MB1BAS RC1 Verdict page 5	44
Figure 55: SX1261MB1BAS RC2 Device Configuration	45
Figure 56: SX1261MB1BAS RC2 FFT	46
Figure 57: SX1261MB1BAS RC2 Demodulation	46
Figure 58: SX1261MB1BAS RC2 Power Spectral Density for Class 0 & 1	47
Figure 59: SX1261MB1BAS RC2 Full FFT	47
Figure 60: SX1261MB1BAS RC2 Data Rate	48
Figure 61: SX1261MB1BAS RC2 Data Rate Cumulated Error	48
Figure 62: SX1261MB1BAS RC2 Extra Symbol Before Transmission	49
Figure 63: SX1261MB1BAS RC2 Extra Symbol After Transmission	49
Figure 64: SX1261MB1BAS RC2 Dynamic Drift	50
Figure 65: SX1261MB1BAS RC2 Dynamic Drift per Second	50
Figure 66: SX1261MB1BAS RC2 Phase	51
Figure 67: SX1261MB1BAS RC2 Envelope	51
Figure 68: SX1261MB1BAS RC2 Verdict Page 1	52
Figure 69: SX1261MB1BAS RC2 Verdict Page 2	52
Figure 70: SX1261MB1BAS RC2 Verdict Page 3	53
Figure 71: SX1261MB1BAS RC2 Verdict Page 4	53
Figure 72: SX1261MB1BAS RC2 Verdict page 5	54
Figure 73: SX1262DVK1DAS RC1 Device Configuration	55
Figure 74: SX1262DVK1DAS RC1 FFT	56
Figure 75: SX1262DVK1DAS RC1 Demodulation	56
Figure 76: SX1262DVK1DAS RC1 Power Spectral Density for Class 0 & 1	57
Figure 77: SX1262DVK1DAS RC1 Full FFT	57
Figure 78: SX1262DVK1DAS RC1 Data Rate	58
Figure 79: SX1262DVK1DAS RC1 Data Rate Cumulated Error	58
Figure 80: SX1262DVK1DAS RC1 Extra Symbol Before Transmission	59
Figure 81: SX1262DVK1DAS RC1 Extra Symbol After Transmission	59
Figure 82: SX1262DVK1DAS RC1 Dynamic Drift	60
Figure 83: SX1262DVK1DAS RC1 Dynamic Drift per Second	60
Figure 84: SX1262DVK1DAS RC1 Phase	61
Figure 85: SX1262DVK1DAS RC1 Envelope	61
Figure 86: SX1262DVK1DAS RC1 Verdict Page 1	62
Figure 87: SX1262DVK1DAS RC1 Verdict Page 2	62
Figure 88: SX1262DVK1DAS RC1 Verdict Page 3	63
Figure 89: SX1262DVK1DAS RC1 Verdict Page 4	63
Figure 90: SX1262DVK1DAS RC1 Verdict page 5	64
Figure 91: SX1262DVK1DAS RC2 Device Configuration	65
Figure 92: SX1262DVK1DAS RC2 FFT	66
Figure 93: SX1262DVK1DAS RC2 Demodulation	66
Figure 94: SX1262DVK1DAS RC2 Power Spectral Density for Class 0 & 1	67
Figure 95: SX1262DVK1DAS RC2 Full FFT	67
Figure 96: SX1262DVK1DAS RC2 Data Rate	68
Figure 97: SX1262DVK1DAS RC2 Data Rate Cumulated Error	68
Figure 98: SX1262DVK1DAS RC2 Extra Symbol Before Transmission	69
Figure 99: SX1262DVK1DAS RC2 Extra Symbol After Transmission	69
Figure 100: SX1262DVK1DAS RC2 Dynamic Drift	70
Figure 101: SX1262DVK1DAS RC2 Dynamic Drift per Second	70
Figure 102: SX1262DVK1DAS RC2 Phase	71
Figure 103: SX1262DVK1DAS RC2 Envelope	71
Figure 104: SX1262DVK1DAS RC2 Verdict Page 1	72
Figure 105: SX1262DVK1DAS RC2 Verdict Page 2	72
Figure 106: SX1262DVK1DAS RC2 Verdict Page 3	73
Figure 107: SX1262DVK1DAS RC2 Verdict Page 4	73
Figure 108: SX1262DVK1DAS RC2 Verdict Page 5	74

List of Tables

Table 1: API Functions	9
-------------------------------------	---

1 Introduction

The SX1261/2 and LR11xx transceivers have capabilities that facilitate data transmission using BPSK modulation.

1.1 Purpose of This Document

This document explains how to transmit data using the BPSK (Binary Phase-Shift Keying) modulator implemented in the SX1261/62 and the LR11xx transceivers. BPSK and Sigfox® (Differential Binary Phase-Shift Keying) modulations are simple digital modulation schemes where the phase positions (0 or π radians) are used to encode a binary value (0 or 1). For the BPSK modulation, the phase itself represents a binary value (0 or 1) while in the case of the Sigfox modulation described here, a binary value of zero is represented by a change in phase, and a binary value of one is represented by an unchanging phase.

BPSK modulation is implemented in the hardware, and Sigfox is implemented via some software preprocessing and the BPSK hardware modulator.

1.2 Scope of This Document

It is recommended to read this document in conjunction with the following resources:

- Transceiver drivers embedding Sigfox support:
 - SX126x: https://github.com/Lora-net/sx126x_driver/releases/tag/v2.2.0
 - LR11xx: <https://github.com/Lora-net/SWDR001/releases/tag/v2.3.0>

1.3 Disclaimer

Any code or code snippet provided in this application note is provided as an example on an “as is” basis. The user must ensure that the settings are correct to meet their required protocol/certification tests. Semtech is not liable for any performance issues or field returns related to BPSK modulation support.

2 Sigfox Library

The Sigfox library provides utilities to convert a BPSK payload to Sigfox. This makes it possible to recover the payload when there is a phase error that cannot be determined by the receiver.

The Sigfox library is available here:

https://github.com/Lora-net/SWSD003/tree/master/libs/smtc_dbpsk_driver/src

2.1 API Functions

The Sigfox library code provides the following principal API functions:

Table 1: API Functions

Function	Description
<code>smtc_dbpsk_encode_buffer()</code>	Perform differential encoding for DBPSK modulation.
<code>smtc_dbpsk_get_pld_len_in_bytes()</code>	Given the length of a BPSK frame, in bytes, calculate the space necessary to hold the frame after differential encoding, in bytes.
<code>smtc_dbpsk_get_pld_len_in_bits()</code>	Given the length of a BPSK frame, in bits, calculate the space necessary to hold the frame after differential encoding, in bits.

2.2 Sigfox Library Usage

1. Add the following directories to your C or C++ compiler include path:

lib\sigfox_dbpsk_driver\src

2. Include the following in your application source code files:

```
#include "smtc_dbpsk.h"
```

3. Compile and link your software with:

smtc_dbpsk.c

3 Sigfox Code Example

Sigfox example code is available:

- For LR11xx: <https://github.com/Lora-net/SWSD003/tree/master/lr11xx/apps/sigfox>
- For SX126x: <https://github.com/Lora-net/SWSD003/tree/master/sx126x/apps/sigfox>

4 Sending BPSK Packets With SX126x

Follow these steps to use the BPSK modulator on the SX126x:

1. Select the BPSK packet type by executing:

```
sx126x_set_pkt_type( context, SX126X_PKT_TYPE_BPSK );
```

2. Set the RF frequency, PA configuration, TX params, DiolrqParams, and TCXO configuration as you would for any other modulation. For further information, please see the transceiver data sheet.

3. Set the BPSK modulation params (bit rate and modulation shaping filter) by executing:

```
sx126x_set_bpsk_mod_params( context, &mod_params );
```

Note that only 100bps and 600bps are supported. The modulation shaping filter is a raised-cosine filter with a BT of 0.7.

4. Prepare the packet in a data array by concatenating the separate parts of the packet (preamble, sync word, device id, application payload, CRC, etc.).

If the packet is meant to be transmitted using Sigfox, then execute:

```
dbpsk_encode_buffer( data_in, bpsk_pld_len_in_bits, data_out );
```

to represent zeros as phase changes and to represent ones as unchanging phase. One additional bit of data is added to keep track of the phase, and one additional bit is added to the end of the data to make it possible to slightly extend the last bit. This function assumes that *data_out* is large enough to store *bpsk_pld_len_in_bits* + 2 bits of data.

5. Set the BPSK packet params (payload length and ramp tuning parameters) by executing:

```
sx126x_set_bpsk_pkt_params( context, &pkt_params_bpsk );
```

For the ramp tuning parameters, use one of these:

- *SIGFOX_DBPSK_RAMP_UP_TIME_100_BPS / SIGFOX_DBPSK_RAMP_DOWN_TIME_100_BPS*
- *SIGFOX_DBPSK_RAMP_UP_TIME_600_BPS / SIGFOX_DBPSK_RAMP_DOWN_TIME_600_BPS*

Note that the physical packet length is specified both in bytes (indicating total storage space), and in bits, since the latter is not necessarily a multiple of 8. If the physical payload was processed with *dbpsk_encode_buffer*, above, then use the following two API functions to get these packet lengths:

- *dbpsk_get_pld_len_in_bytes(bpsk_pld_len_in_bits)*
- *dbpsk_get_pld_len_in_bits(bpsk_pld_len_in_bits)*

6. To write the physical payload, execute:

```
sx126x_write_buffer( context, 0, buffer, length );
```

7. To start transmission, execute:

```
sx126x_set_tx( context, timeout );
```

5 Sending BPSK Packets With LR11xx

Follow these steps to use the BPSK modulator on the LR11xx:

1. Select the BPSK packet type by executing:

```
lr11xx_radio_set_pkt_type( context, LR11XX_RADIO_PKT_TYPE_BPSK );
```

2. Set the RF frequency, PA configuration, TX params, DiolrqParams, and TCXO configuration as you would for any other modulation. For further information, please see the transceiver user manual.

3. Set the BPSK modulation params (bit rate and modulation shaping filter) by executing:

```
lr11xx_radio_set_bpsk_mod_params( context, &mod_params );
```

Note that only 100bps and 600bps are supported. The modulation shaping filter is a raised-cosine filter with a BT of 0.7.

4. Prepare the packet in a data array by concatenating the separate parts of the packet (preamble, sync word, device id, application payload, CRC, etc.).

If the packet is meant to be transmitted using Sigfox, then execute:

```
dbpsk_encode_buffer( data_in, bpsk_pld_len_in_bits, data_out );
```

to represent zeros as phase changes and to represent ones as unchanging phase. One additional bit of data is added to keep track of the phase, and one additional bit is added to the end of the data to make it possible to slightly extend the last bit. This function assumes that *data_out* is large enough to store *bpsk_pld_len_in_bits* + 2 bits of data.

5. Set the BPSK packet params (payload length and ramp tuning parameters) by executing:

```
lr11xx_radio_set_bpsk_pkt_params( context, &pkt_params_bpsk );
```

For the ramp tuning parameters, use either:

- SIGFOX_DBPSK_RAMP_UP_TIME_100_BPS / SIGFOX_DBPSK_RAMP_DOWN_TIME_100_BPS
- SIGFOX_DBPSK_RAMP_UP_TIME_600_BPS / SIGFOX_DBPSK_RAMP_DOWN_TIME_600_BPS

Note that the physical packet length is specified both in bytes (indicating total storage space), and in bits, since the latter is not necessarily a multiple of 8. If the physical payload was processed with *dbpsk_encode_buffer*, above, then use the following two API functions to get these packet lengths:

- dbpsk_get_pld_len_in_bytes(*bpsk_pld_len_in_bits*)
- dbpsk_get_pld_len_in_bits(*bpsk_pld_len_in_bits*)

6. To write the physical payload, execute:

```
lr11xx_regmem_write_buffer8( context, buffer, length );
```

7. To start transmission, execute:

```
lr11xx_radio_set_tx( context, timeout );
```

6 Sigfox Compliance

The Sigfox Radio Signal Analyzer tool was used to perform all the following tests.

All tests were done at ambient temperature, with the transceiver powered by the Nucleo board.

The transceiver PA ramp time was set to 200us, with 40dB of attenuation placed at the input of the Sigfox Radio Signal Analyzer tool.

When a TCXO was present, it was powered at 1.8V by the transceiver, with a startup time of 150ms.

In the Sigfox Radio Signal Analyzer tool, all three oscillator accuracy parameters were set to 5ppm.

The payload that was sent for these tests was 'sample1', found in the Sigfox payload samples, below:

```
// data = =01
const uint8_t sample0[] = { 0xaa, 0xaa, 0xa0, 0x8d, 0x01, 0x05, 0x98, 0xba,
                           0xdc, 0xfe, 0x01, 0x9a, 0x09, 0xfe, 0x04 };

// data = 0123
const uint8_t sample1[] = { 0xaa, 0xaa, 0xa3, 0x5f, 0x81, 0x3c, 0x98, 0xba,
                           0xdc, 0xfe, 0x01, 0x23, 0xea, 0xd3, 0xe1, 0xdb,
                           0xd4, 0xf0 };

// data = 0123456789
const uint8_t sample2[] = { 0xaa, 0xaa, 0xa6, 0x11, 0xc1, 0x3d, 0x98, 0xba,
                           0xdc, 0xfe, 0x01, 0x23, 0x45, 0x67, 0x89, 0x11,
                           0xe3, 0x3d, 0x2e, 0x3e, 0xf9, 0x28 };

// data = 0123456789ABCDEF01
const uint8_t sample3[] = { 0xaa, 0xaa, 0xa9, 0x4c, 0xc1, 0x3e, 0x98, 0xba,
                           0xdc, 0xfe, 0x01, 0x23, 0x45, 0x67, 0x89, 0xab,
                           0xcd, 0xef, 0x01, 0xef, 0xeb, 0x05, 0x5f, 0xc6,
                           0x91, 0x08 };
```

6.1 General Compliance Notes

The following tests are known to either fail or be inconclusive.

All other executed tests pass, as can be seen in the Verdict sections of the measurement results in Sections 6.2 to 0.

6.1.1 PRS-RFP-004, Static Frequency Tolerance

4	[PRS-RFP-004]	Static Frequency Tolerance	Vnom	-20 ppm	20 ppm	0.331748 ppm	10.3317 ppm	INCONCLUSIVE	Declared Aging for 5 years: 5.00...
---	----------------------	----------------------------	------	---------	--------	--------------	-------------	---------------------	-------------------------------------

Specification description

UUT carrier frequency (absolute value) must be at +/- 20ppm for operational bands.

Temperature Frequency tolerance added to Aging frequency tolerance and Static Frequency Tolerance

(static imprecision of the UUT) must be less or equal to +/- 20ppm during all the product life.

The above must be verified by taking into consideration the temperature and aging properties of the TCXO.

6.1.2 PRS-RFP-010, Sigfox Modulation

7	[PRS-RFP-010]	DBPSK Modulation		Vnom				FAILED	Wrong payload received -
---	---------------	------------------	--	------	--	--	--	--------	--------------------------

This test fails because the test code does not know what payload is expected by the RSA tool. The Sigfox Test Mode software is proprietary.

6.1.3 PRS-RFP-011, Phase Measurement

10	[PRS-RFP-011]	Phase Measurement		Vnom	10 °	30 °	0.798708 °	3.43242 °	PASSED	
----	---------------	-------------------	--	------	------	------	------------	-----------	--------	--

With the current version of the RSA tool, the phase test sometimes fails (See Section 6.3.12). On the phase plot, one notices relatively significant phase change very early in the measurement. It is possible that the Sigfox dongle is not sufficiently reliable for these tests, as mentioned in Sigfox documentation.

6.1.4 PRS-RFP-016, Power Spectral Density For Class 0 and 1

25	[PRS-RFP-016]	Power Spectral Density For Class 0 And 1		Vnom					INCONCLUSIVE	
----	---------------	--	--	------	--	--	--	--	--------------	--

With the current version of the Sigfox Radio Signal Analyzer (RSA) and the Sigfox dongle used for these tests, the power spectral density test is inconclusive. It is possible that the Sigfox dongle is not sufficiently reliable for these tests, as mentioned in Sigfox documentation.

6.1.5 PRS-RFP-017, Power Spectral Density For Class 0 and 1

28	[PRS-RFP-017]	Power Spectral Density For Class 2 And 3		Vnom					INCONCLUSIVE	
----	---------------	--	--	------	--	--	--	--	--------------	--

This test is inconclusive because class 2 and class 3 are not tested.

6.1.6 PRS-RFP-018

31	[PRS-RFP-018]	Transitional Frequency Dynamic Drift		Vnom	-30 Hz/s	30 Hz/s		18.4304 Hz/s	PASSED	
----	---------------	--------------------------------------	--	------	----------	---------	--	--------------	--------	--

The current version of the RSA does not appear to accurately measure the drift for RC2 cases, and the plots are incomplete. The verdict may contain incorrect information for the RC2 cases.

6.1.7 PRS-RFP-070

63	[PRS-RFP-070]	Modulated Conducted Output Power		Vnom			-28.3067 dBm	-28.2999 dBm	INCONCLUSIVE	
----	---------------	----------------------------------	--	------	--	--	--------------	--------------	--------------	--

The Sigfox dongle is not appropriate for making precise power measurements. A 40dB attenuator and approximately 1dB of additional attenuation were used when taking these measurements.

6.2 LR11xxMB1DJS Measurement Results, TCXO, Radio Configuration RC1

See Section 6.1 for general information about the Sigfox Radio Signal Analyzer tool test results.

6.2.1 Device Configuration

Device Configuration

Library Configuration

Features

Hardware Configuration

Supported Frame Types Configuration

Any change of static configuration will result in a RESET of Information / Verdicts tables

Frame Type	Status
Frame Type No Payload	<input type="checkbox"/>
Frame Type Bit (False)	<input checked="" type="checkbox"/>
Frame Type Bit (True)	<input checked="" type="checkbox"/>
Keep Alive Frame Type	<input checked="" type="checkbox"/>
1 Byte Frame Type	<input checked="" type="checkbox"/>
2 Bytes Frame Type	<input checked="" type="checkbox"/>
3 Bytes Frame Type	<input checked="" type="checkbox"/>
4 Bytes Frame Type	<input checked="" type="checkbox"/>
5 Bytes Frame Type	<input checked="" type="checkbox"/>
6 Bytes Frame Type	<input checked="" type="checkbox"/>
7 Bytes Frame Type	<input checked="" type="checkbox"/>
8 Bytes Frame Type	<input checked="" type="checkbox"/>
9 Bytes Frame Type	<input checked="" type="checkbox"/>
10 Bytes Frame Type	<input checked="" type="checkbox"/>
11 Bytes Frame Type	<input checked="" type="checkbox"/>
12 Bytes Frame Type	<input checked="" type="checkbox"/>

Figure 1: LR11xxMB1DJS RC1 Device Configuration

6.2.2 FFT

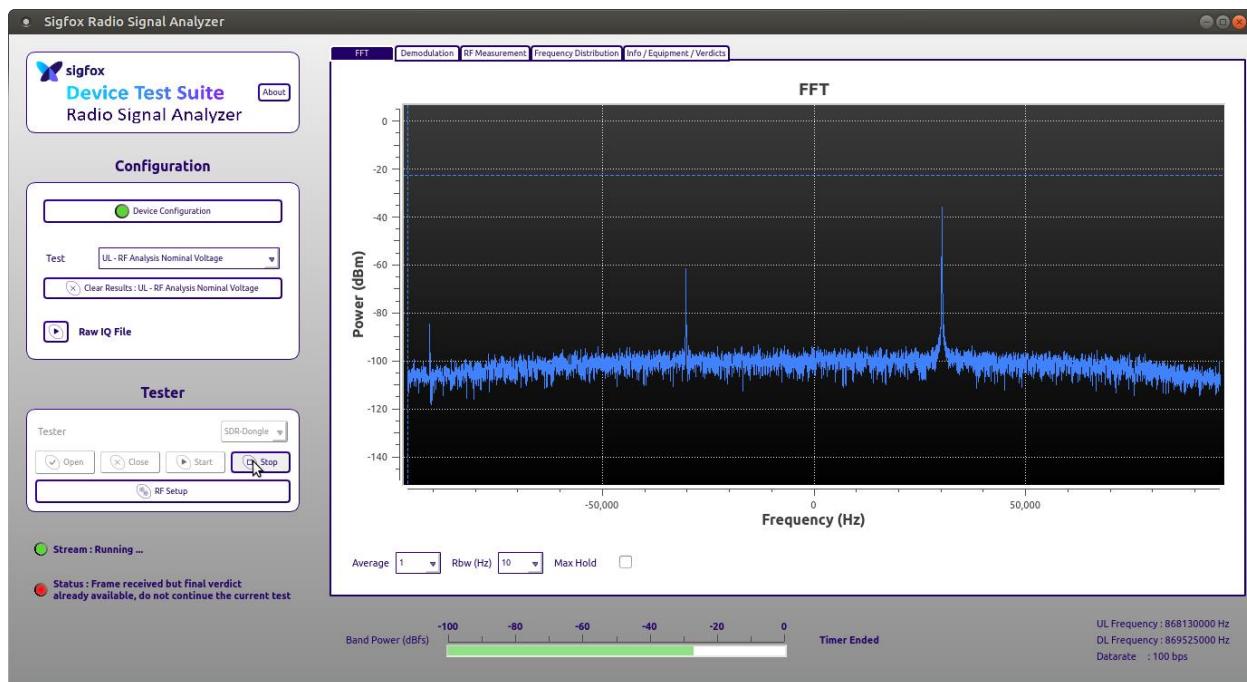


Figure 2: LR11xxMB1DJS RC1 FFT

6.2.3 Demodulation

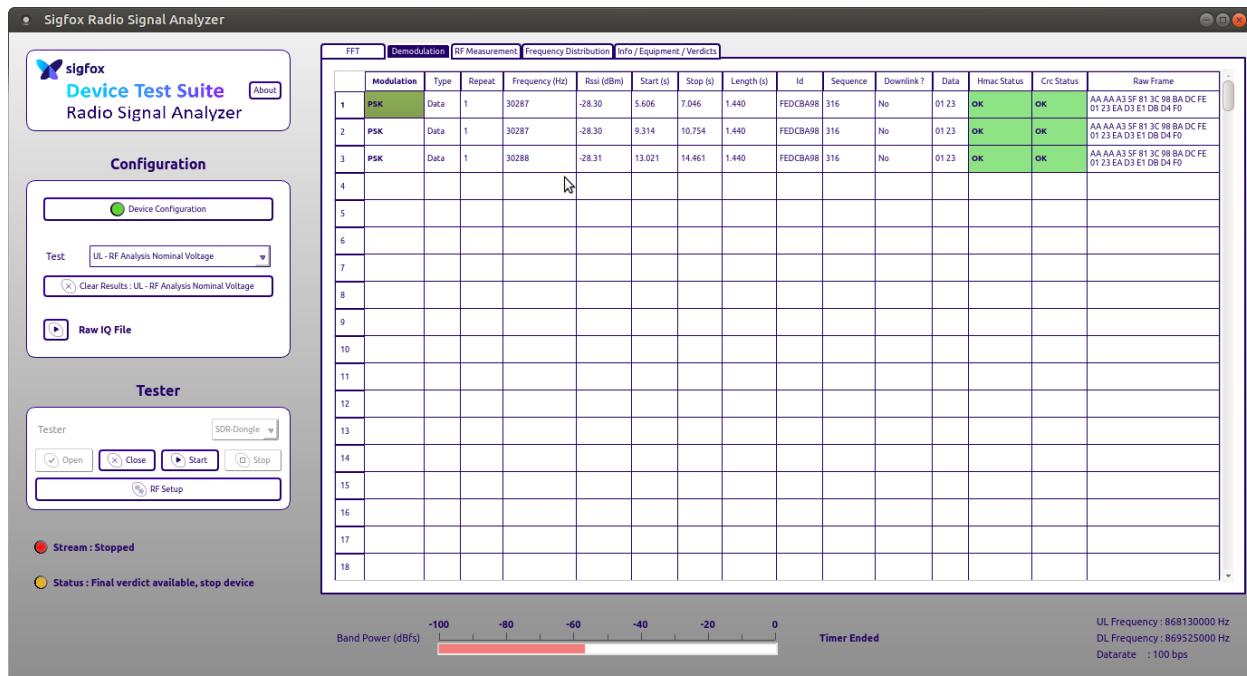


Figure 3: LR11xxMB1DJS RC1 Demodulation

6.2.4 Power Spectral Density for Class 0 & 1

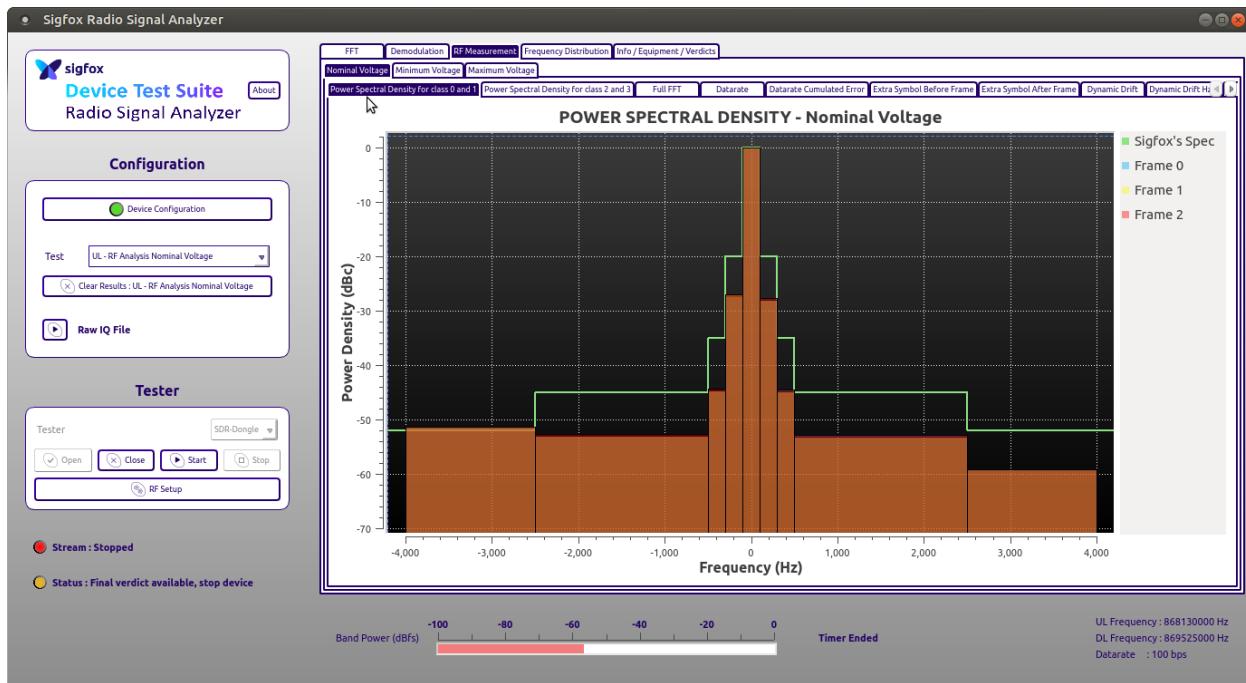


Figure 4: LR11xxMB1DJS RC1 Power Spectral Density for Class 0 & 1

6.2.5 Full FFT

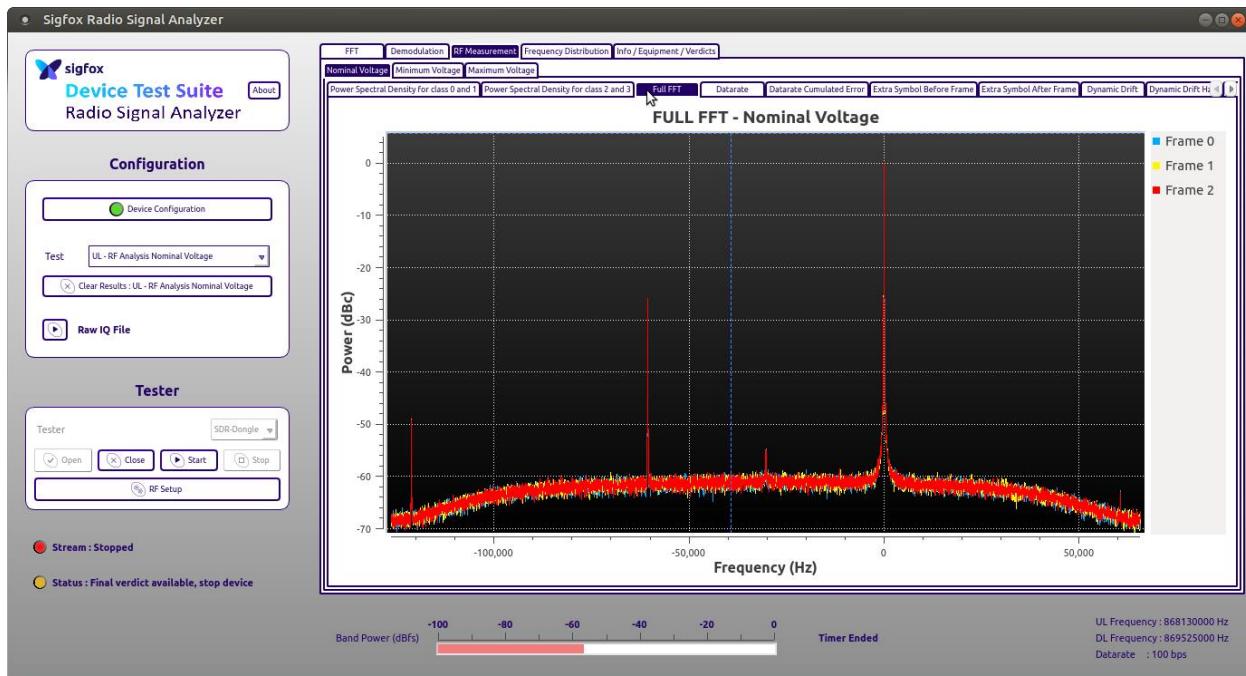


Figure 5: LR11xxMB1DJS RC1 Full FFT

6.2.6 Data Rate

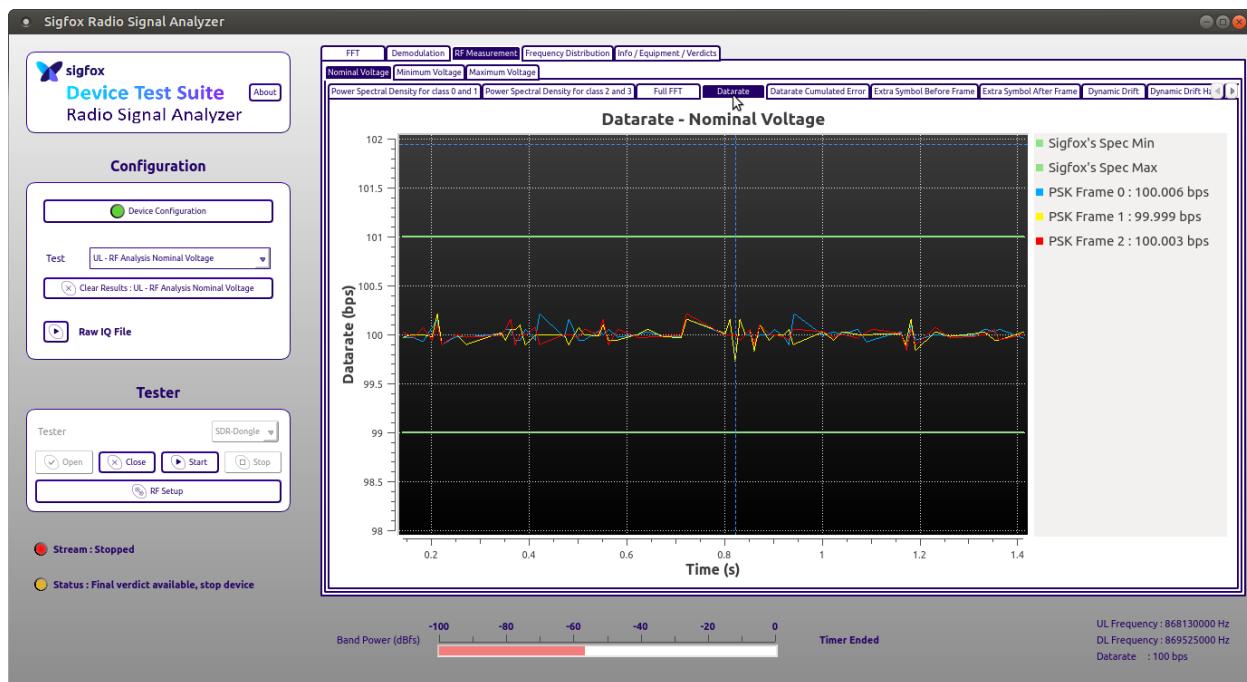


Figure 6: LR11xxMB1DJS RC1 Data Rate

6.2.7 Data Rate Cumulated Error

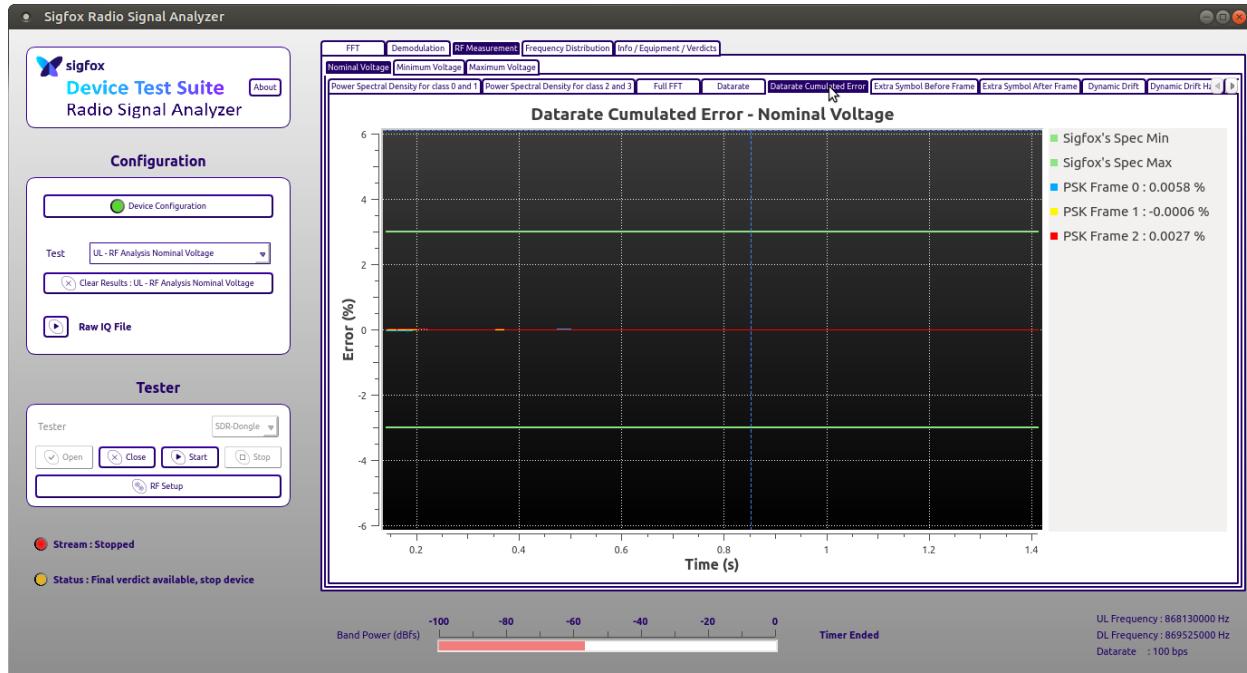


Figure 7: LR11xxMB1DJS RC1 Data Rate Cumulated Error

6.2.8 Extra Symbol Before Transmission

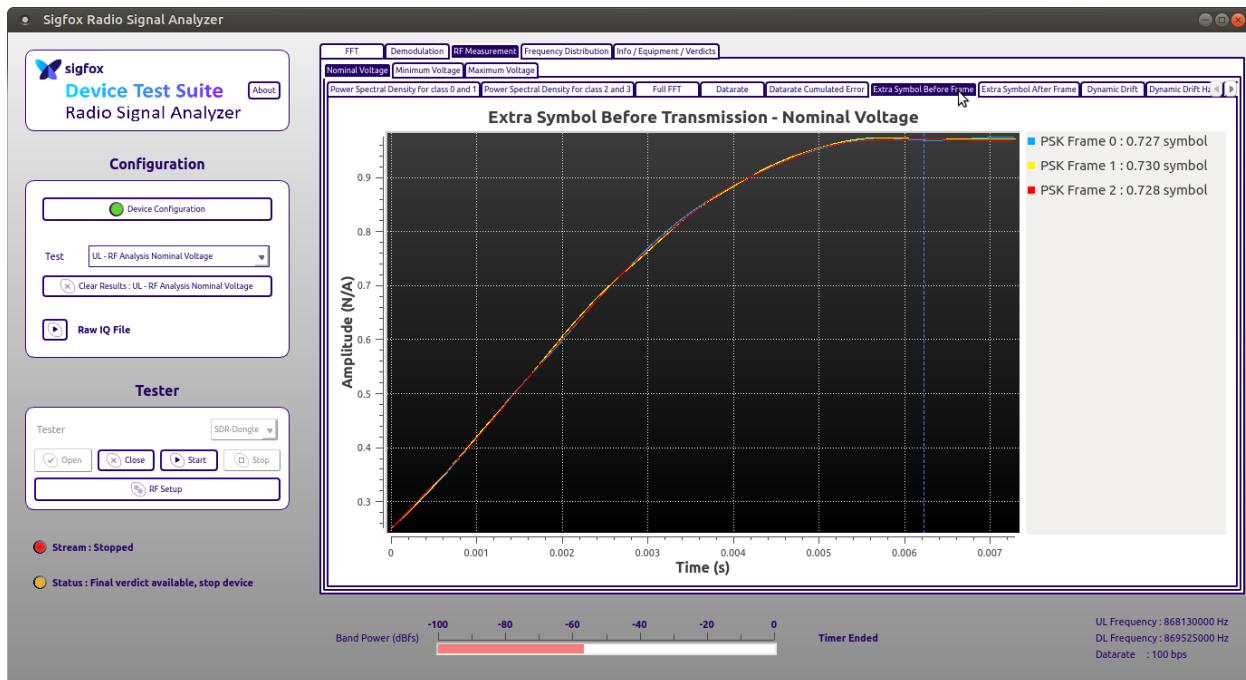


Figure 8: LR11xxMB1DJS RC1 Extra Symbol Before Transmission

6.2.9 Extra Symbol After Transmission

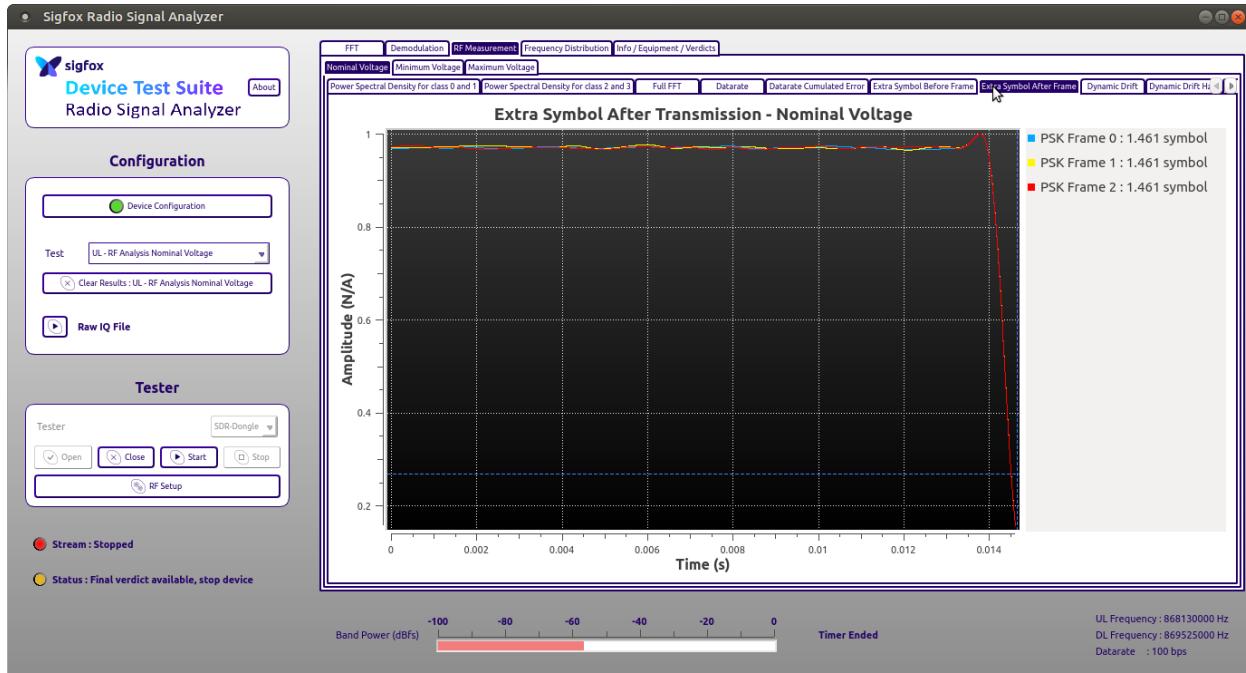


Figure 9: LR11xxMB1DJS RC1 Extra Symbol After Transmission

6.2.10 Dynamic Drift

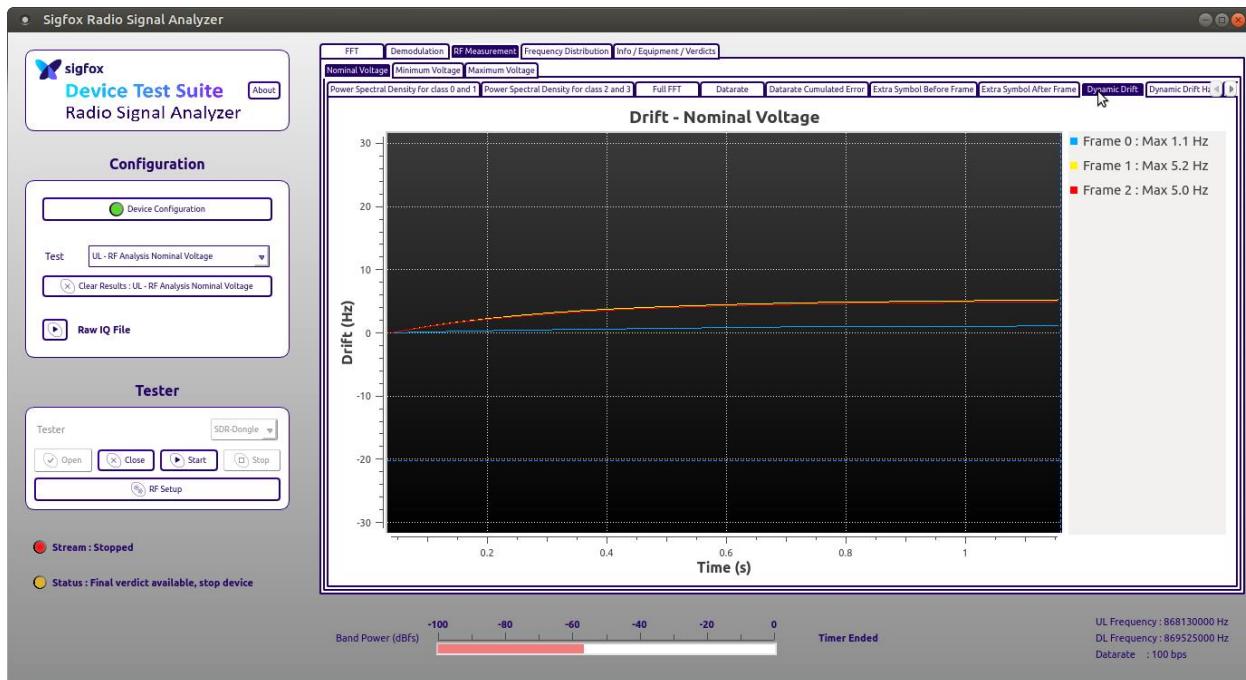


Figure 10: LR11xxMB1DJS RC1 Dynamic Drift

6.2.11 Dynamic Drift per Second

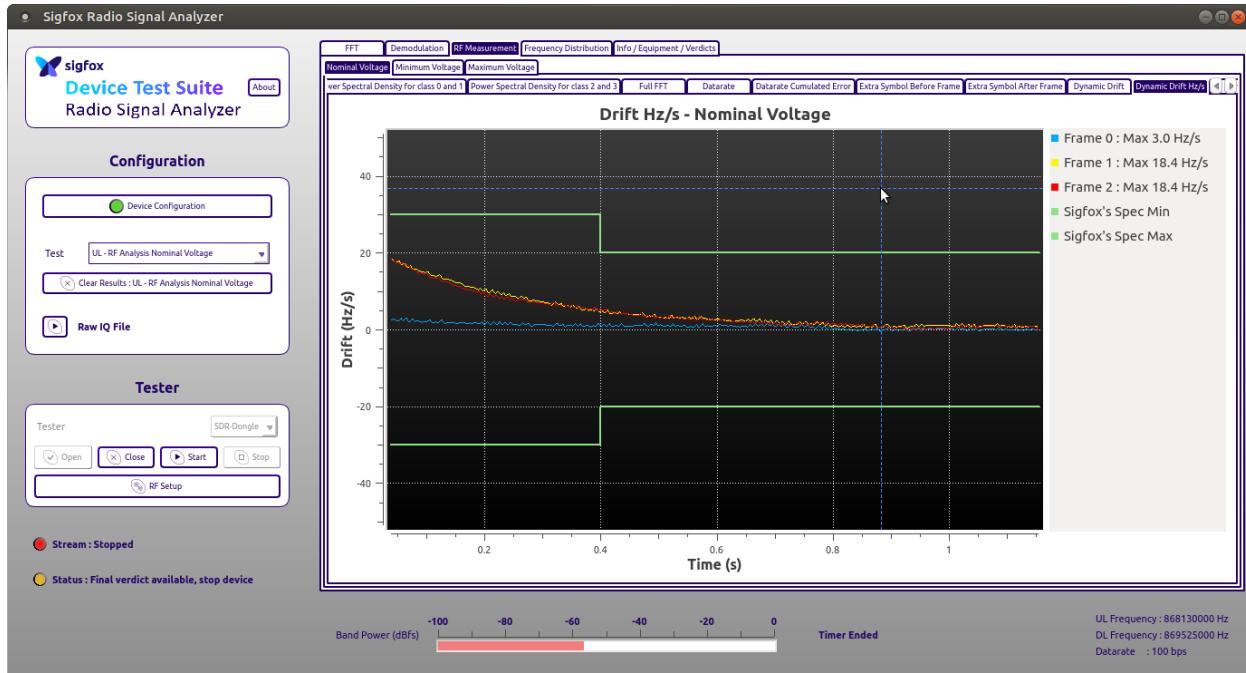


Figure 11: LR11xxMB1DJS RC1 Dynamic Drift per Second

6.2.12 Phase

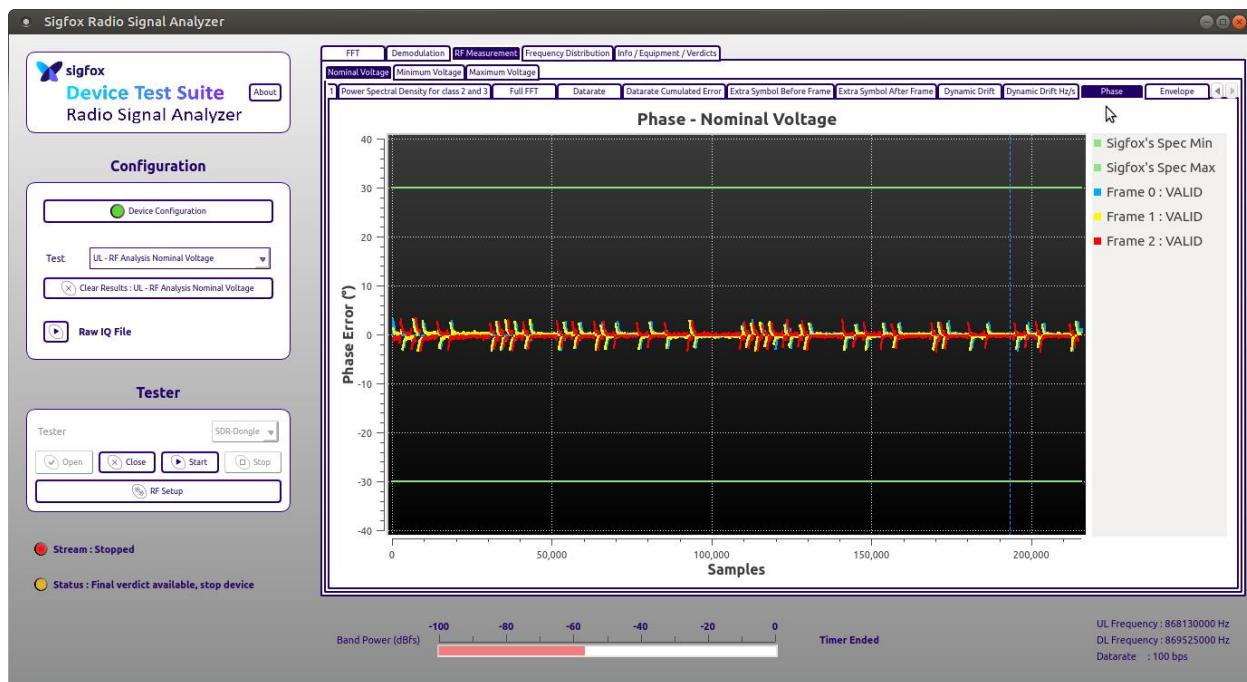


Figure 12: LR11xxMB1DJS RC1 Phase

6.2.13 Envelope

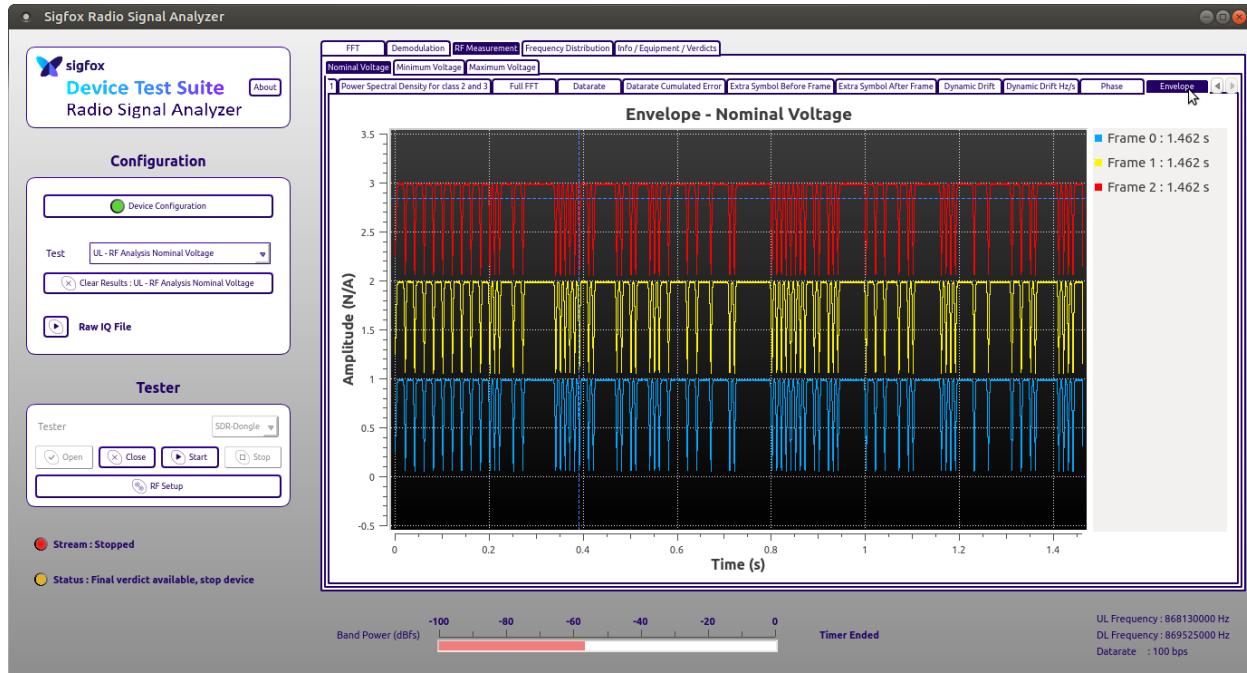


Figure 13: LR11xxMB1DJS RC1 Envelope

6.2.14 Verdict

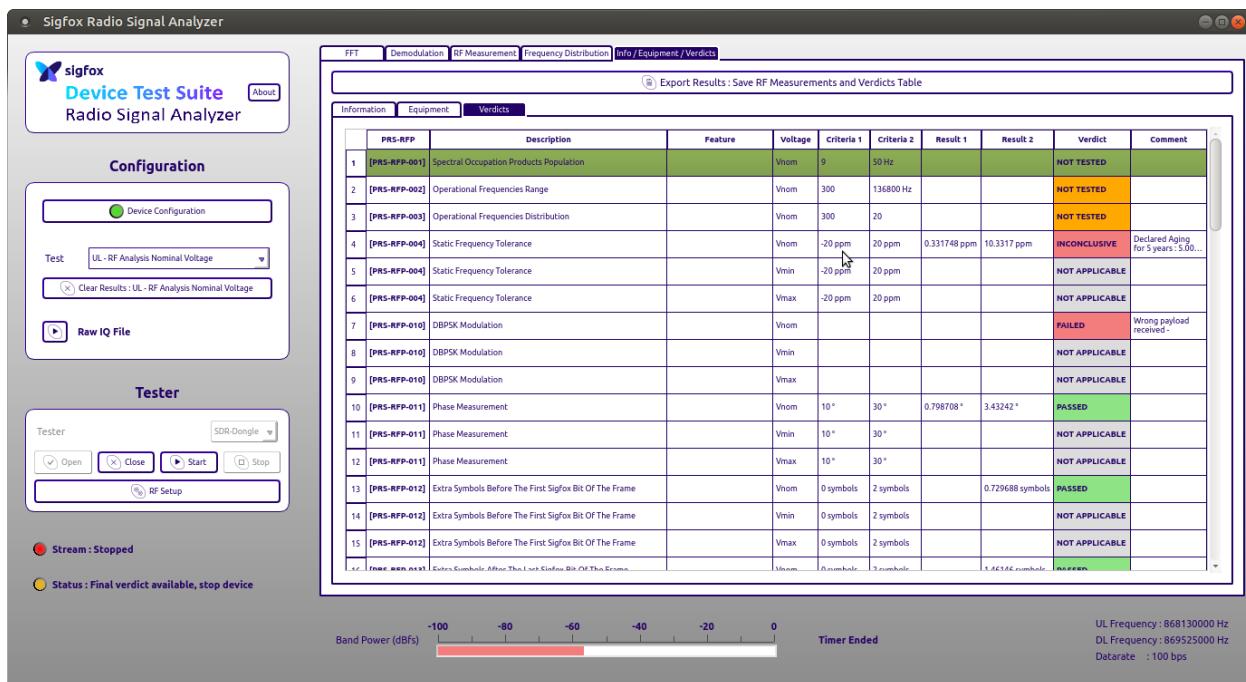


Figure 14: LR11xxMB1DJS RC1 Verdict page 1

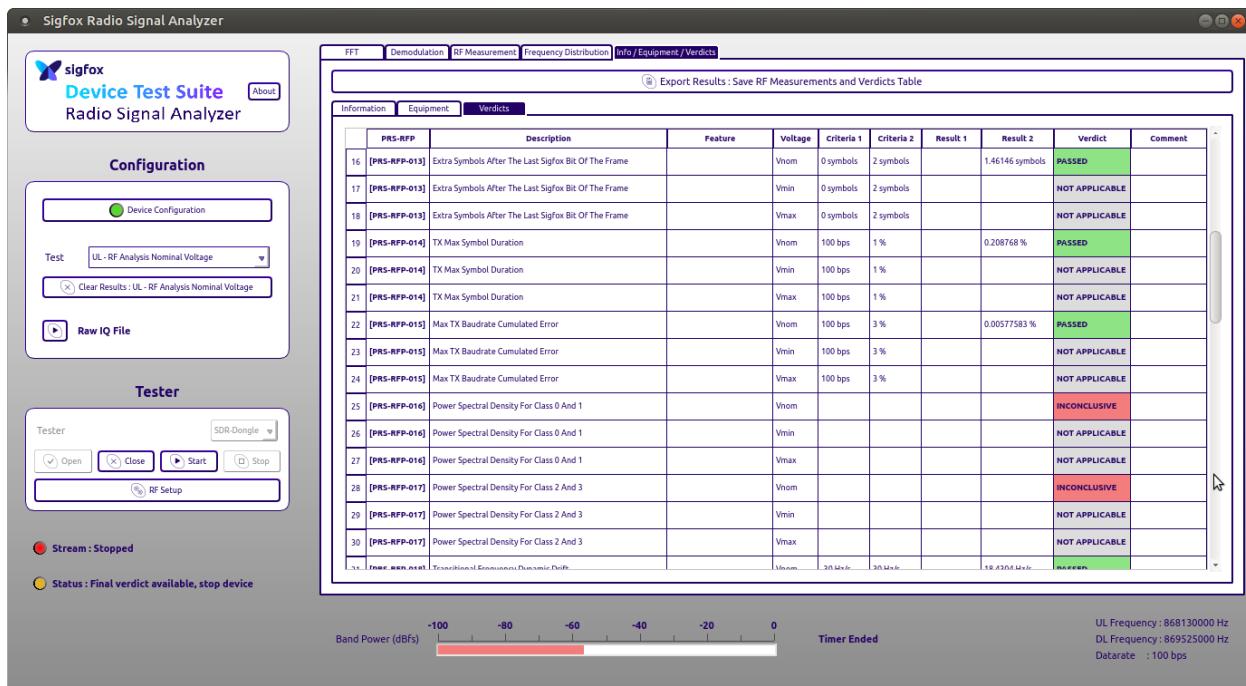


Figure 15: LR11xxMB1DJS RC1 Verdict page 2

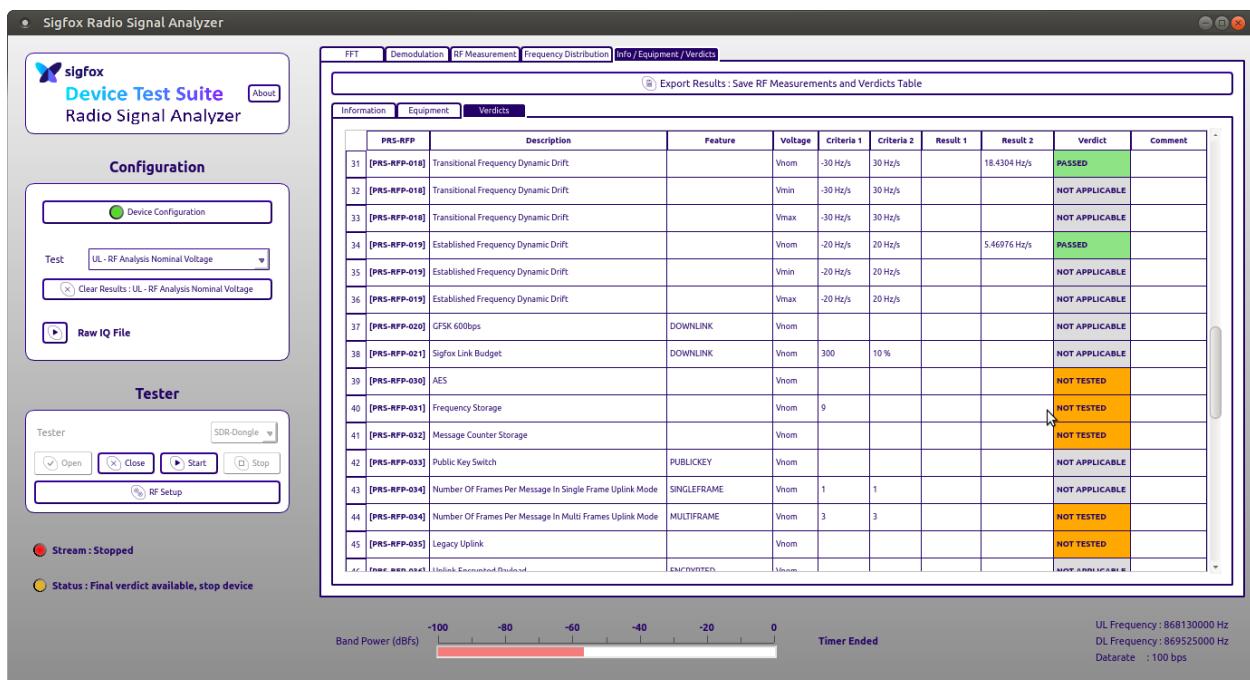


Figure 16: LR11xxMB1DJS RC1 Verdict page 3

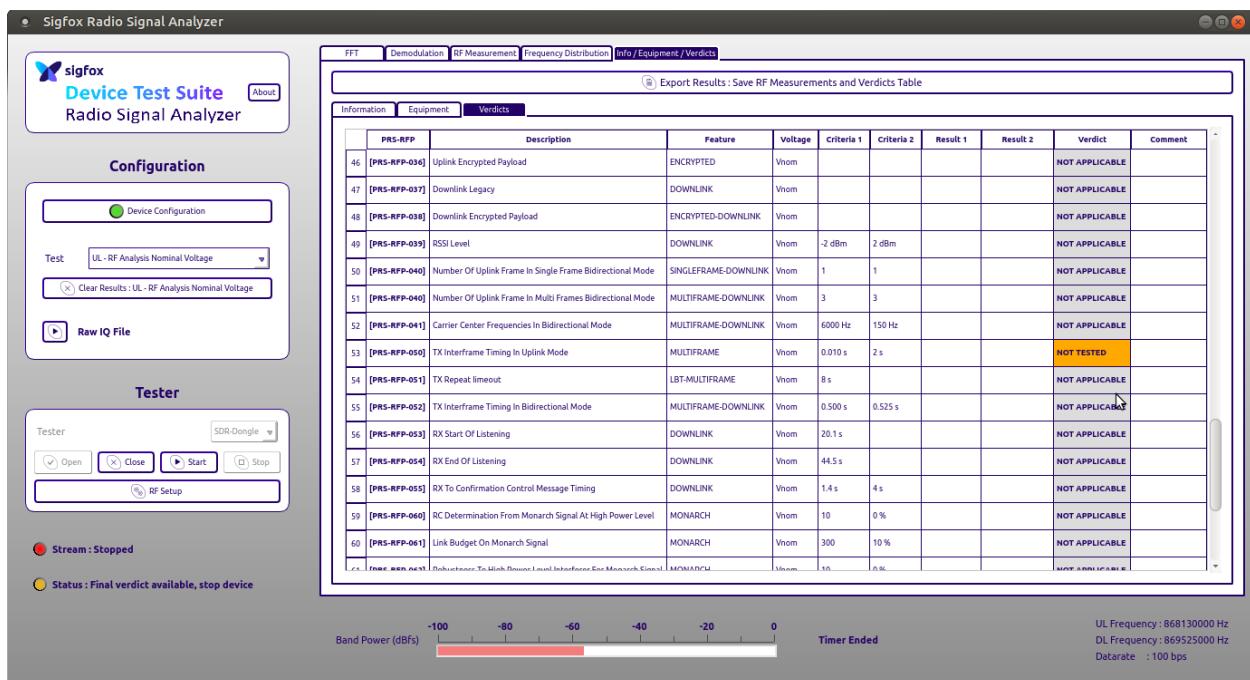


Figure 17: LR11xxMB1DJS RC1 Verdict page 4

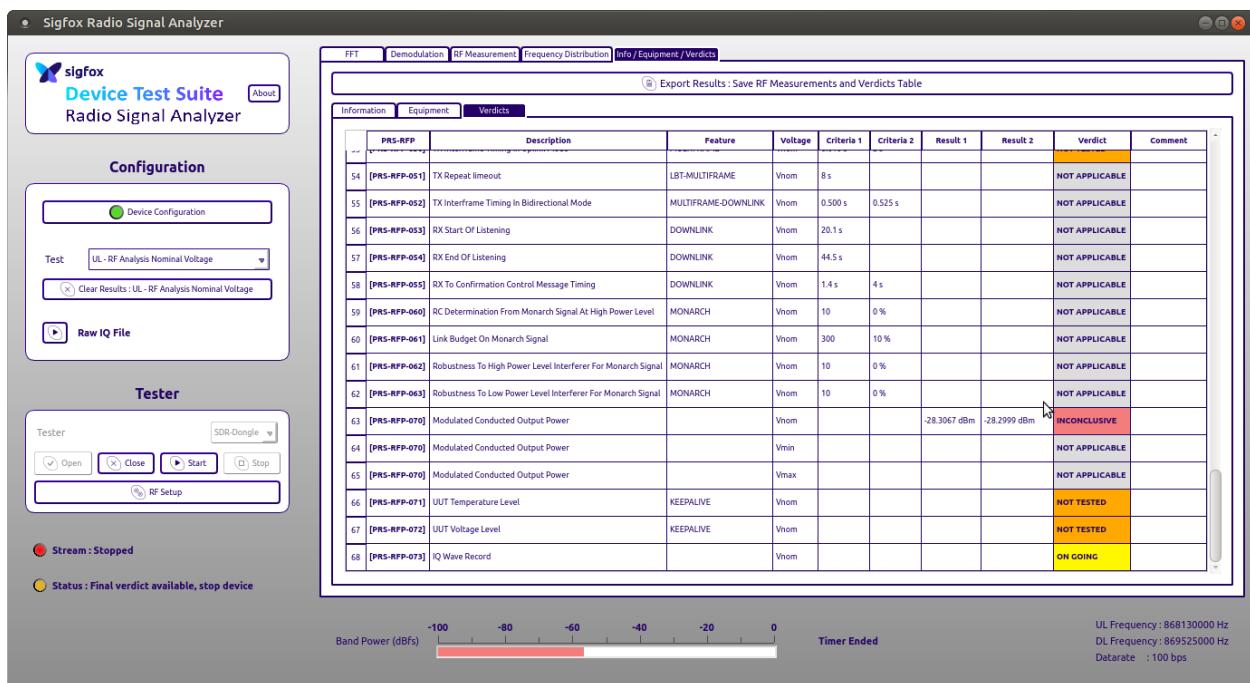


Figure 18: LR11xxMB1DJS RC1 Verdict page 5

6.3 LR11xxMB1DJS Measurement Results, TCXO, Radio Configuration RC2

See Section 6.1 for general information about the Sigfox Radio Signal Analyzer tool test results.

For the RC2 case, note in Figures 6.3.10 and 6.3.11 that the Sigfox Radio Signal Analyzer tool fails to properly measure drift.

6.3.1 Device Configuration

Device Configuration

Library Configuration

Device ID : ID[0] = 0x98 ... ID[3] = 0xFE
 FEDCBA98

Device Private Key : key[0] = 0x01 ... key[15] = 0xEF
 0123456789ABCDEF0123456789ABCDEF

Features

Radio Configuration	RC2
Message Counter Rollover	4096
Downlink capable	<input type="checkbox"/>
Monarch Capable	<input type="checkbox"/>
Public Key switch capable	<input type="checkbox"/>
Payload Encryption Configuration	No Payload Encryption
Payload Encryption Rollover Counter value	0
Multi Frame Capable	<input checked="" type="checkbox"/>
All Message Types Supported	<input checked="" type="checkbox"/> <input type="checkbox"/>

Hardware Configuration

Oscillator Aging for 5 years (ppm)	5.0
Oscillator Temperature Accuracy (ppm) (Taking into account the operating temperature range)	5.0
Product's Population Frequency Accuracy (ppm)	5.0
Minimum Voltage = Nominal Voltage	<input checked="" type="checkbox"/>
Maximum Voltage = Nominal Voltage	<input checked="" type="checkbox"/>

Supported Frame Types Configuration

Frame Type No Payload	<input type="checkbox"/>
Frame Type Bit (False)	<input checked="" type="checkbox"/>
Frame Type Bit (True)	<input checked="" type="checkbox"/>
Keep Alive Frame Type	<input checked="" type="checkbox"/>
1 Byte Frame Type	<input checked="" type="checkbox"/>
2 Bytes Frame Type	<input checked="" type="checkbox"/>
3 Bytes Frame Type	<input checked="" type="checkbox"/>
4 Bytes Frame Type	<input checked="" type="checkbox"/>
5 Bytes Frame Type	<input checked="" type="checkbox"/>
6 Bytes Frame Type	<input checked="" type="checkbox"/>
7 Bytes Frame Type	<input checked="" type="checkbox"/>
8 Bytes Frame Type	<input checked="" type="checkbox"/>
9 Bytes Frame Type	<input checked="" type="checkbox"/>
10 Bytes Frame Type	<input checked="" type="checkbox"/>
11 Bytes Frame Type	<input checked="" type="checkbox"/>
12 Bytes Frame Type	<input checked="" type="checkbox"/>

Apply Settings Cancel

Figure 19: LR11xxMB1DJS RC2 Device Configuration

6.3.2 FFT

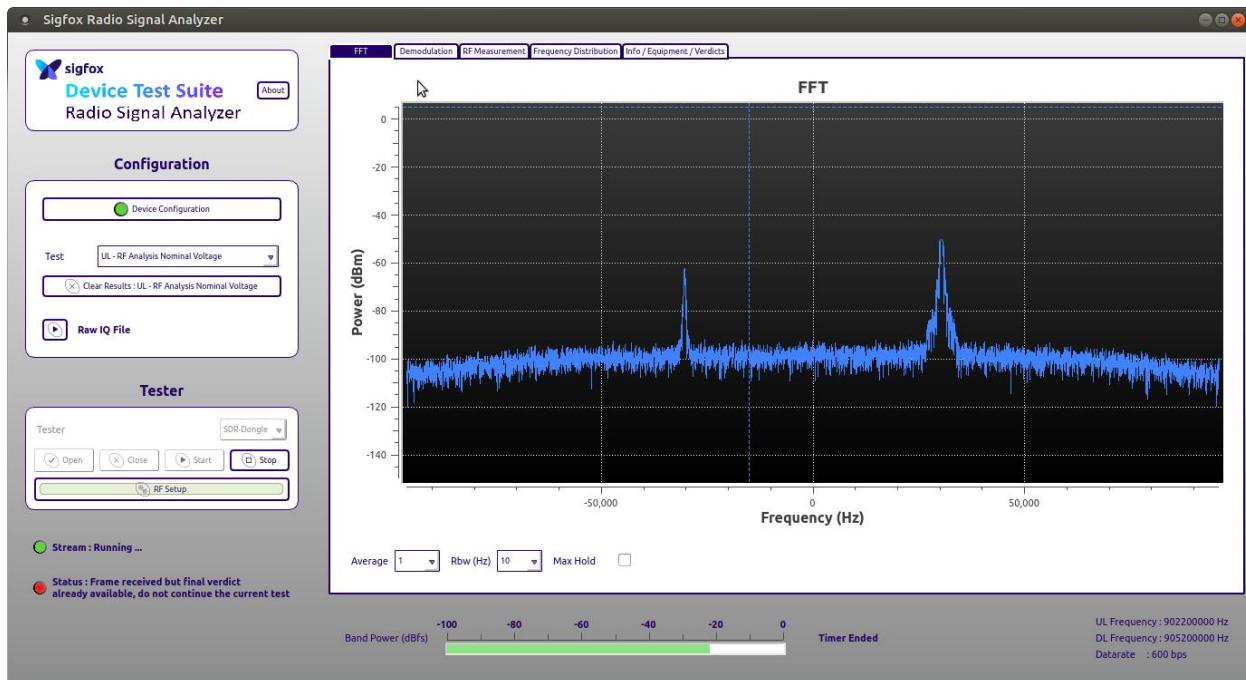


Figure 20: LR11xxMB1DJS RC2 FFT

6.3.3 Demodulation

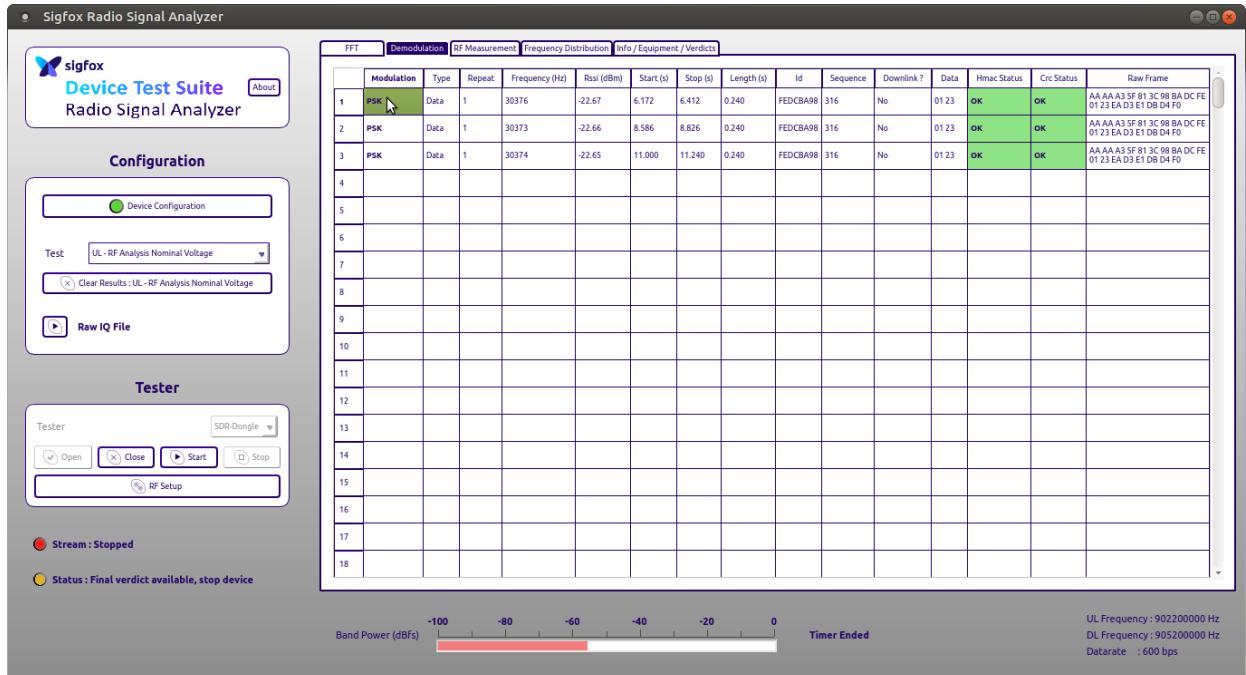


Figure 21: LR11xxMB1DJS RC2 Demodulation

6.3.4 Power Spectral Density for Class 0 & 1

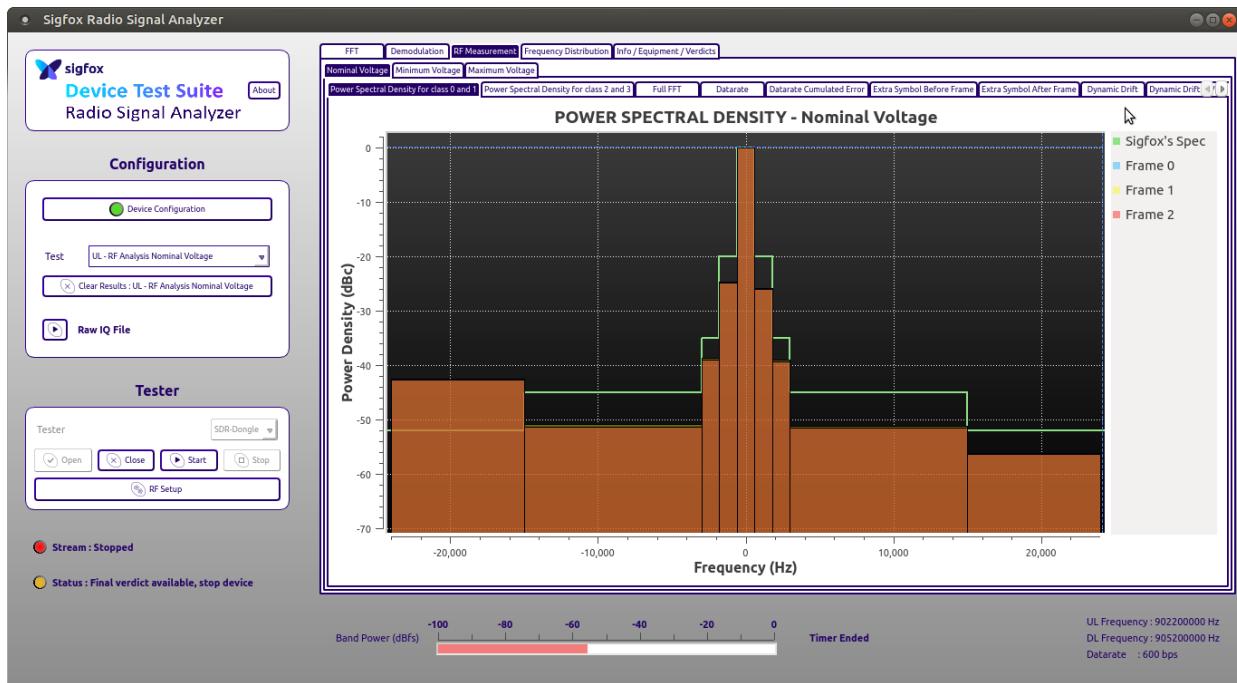


Figure 22: LR11xxMB1DJS RC2 Power Spectral Density for Class 0 & 1

6.3.5 Full FFT

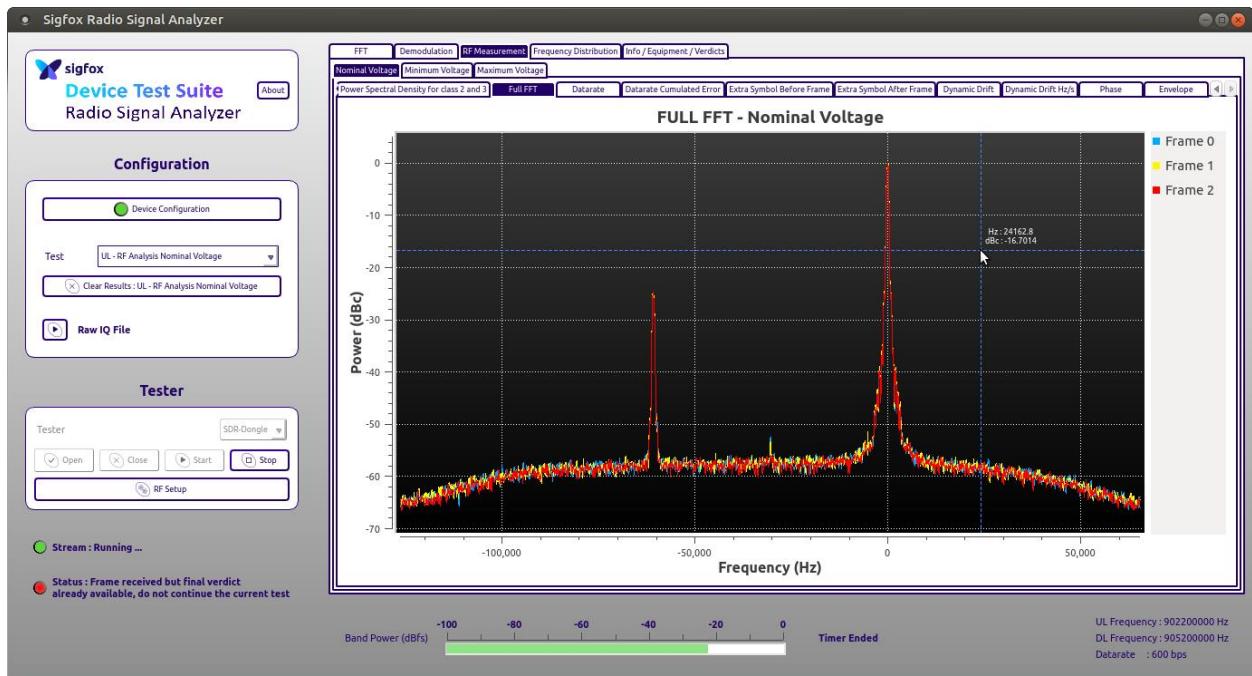


Figure 23: LR11xxMB1DJS RC2 Full FFT

6.3.6 Data Rate

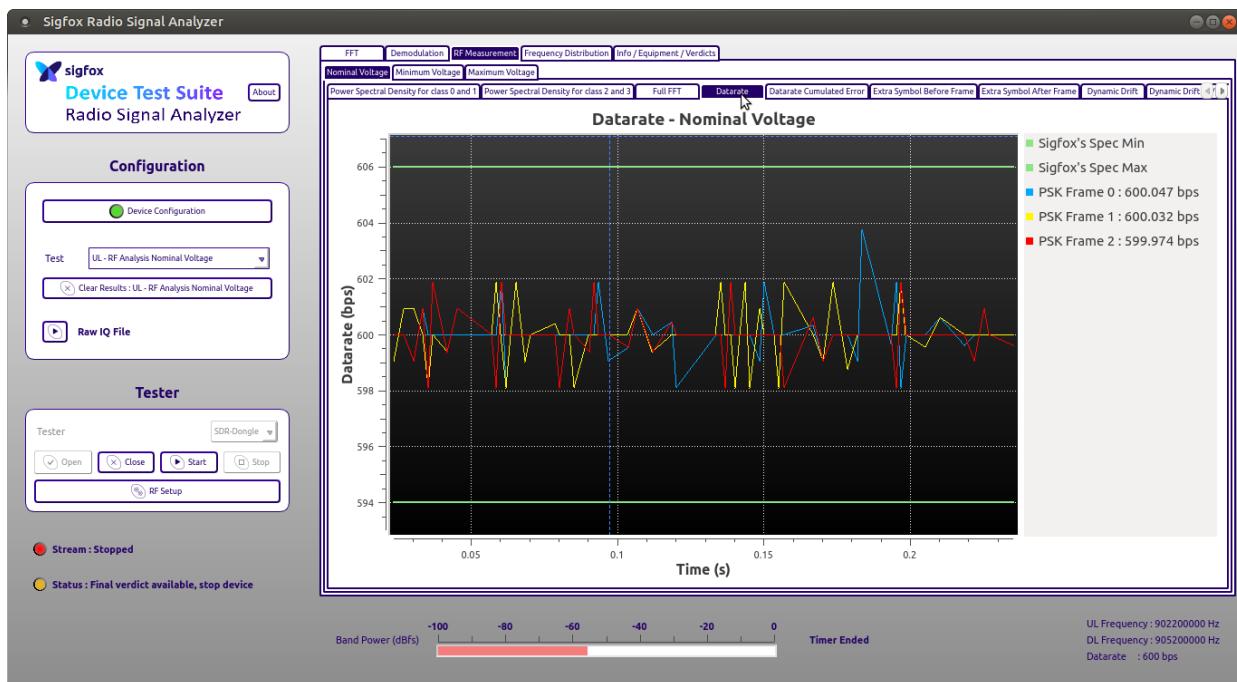


Figure 24: LR11xxMB1DJS RC2 Data Rate

6.3.7 Data Rate Cumulated Error

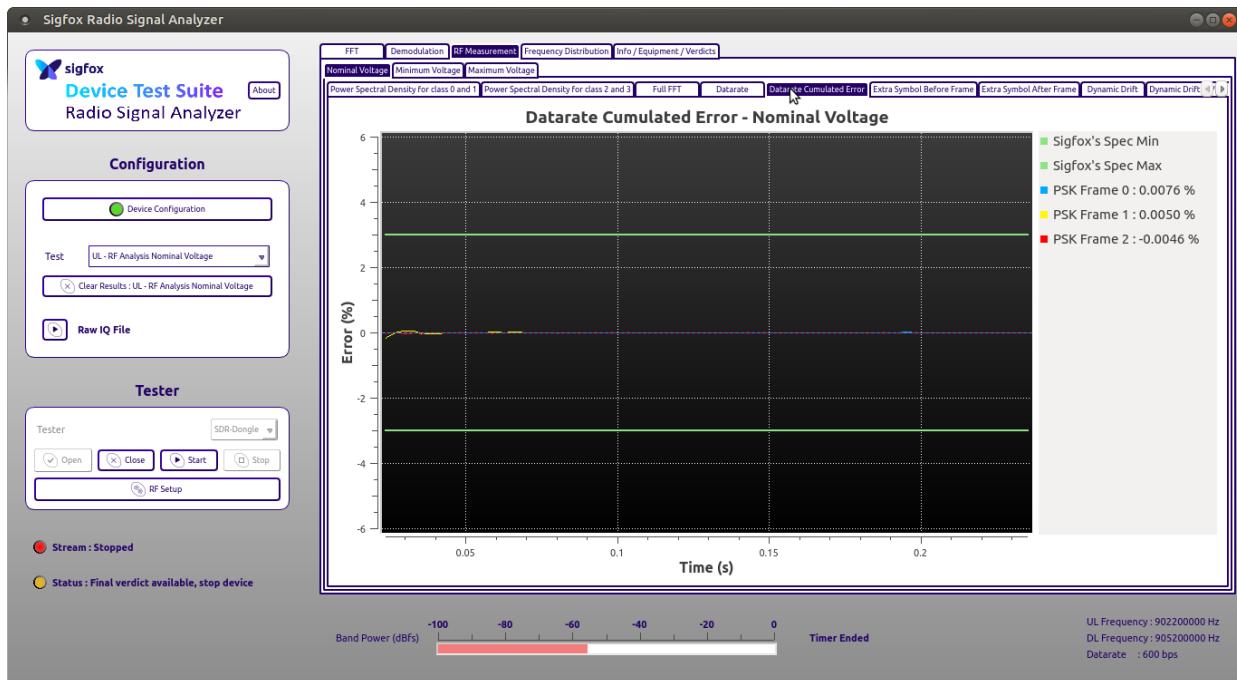


Figure 25: LR11xxMB1DJS RC2 Data Rate Cumulated Error

6.3.8 Extra Symbol Before Transmission

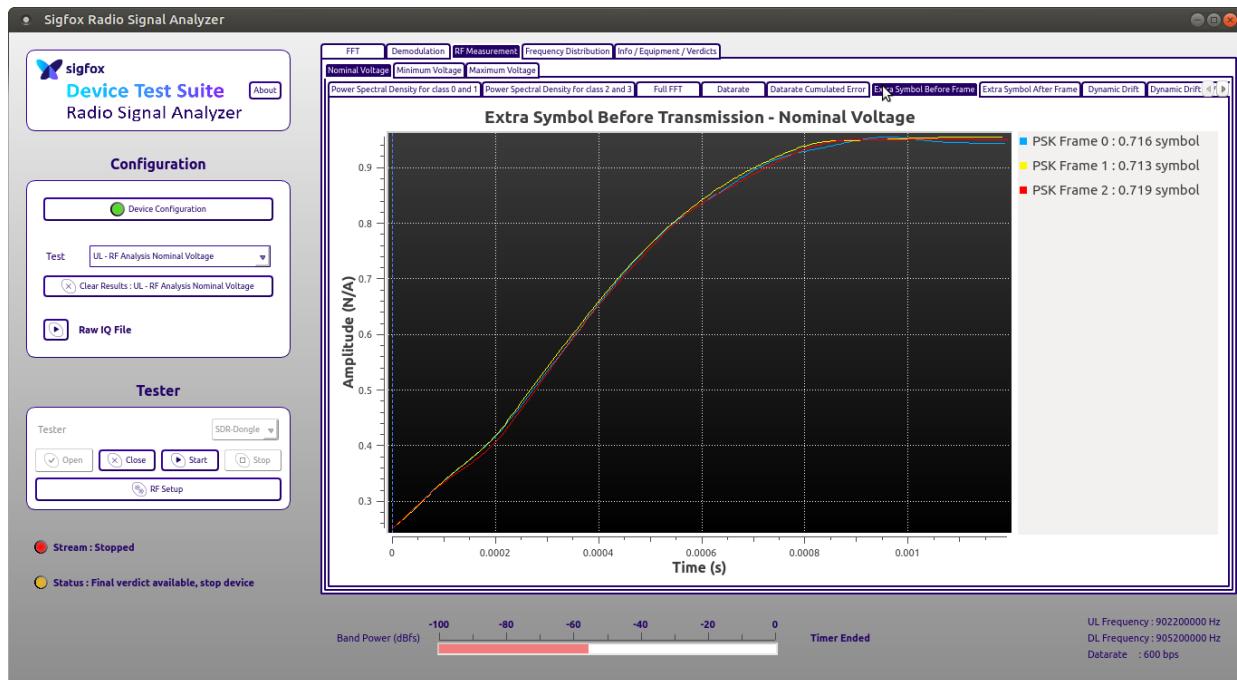


Figure 26: LR11xxMB1DJS RC2 Extra Symbol Before Transmission

6.3.9 Extra Symbol After Transmission

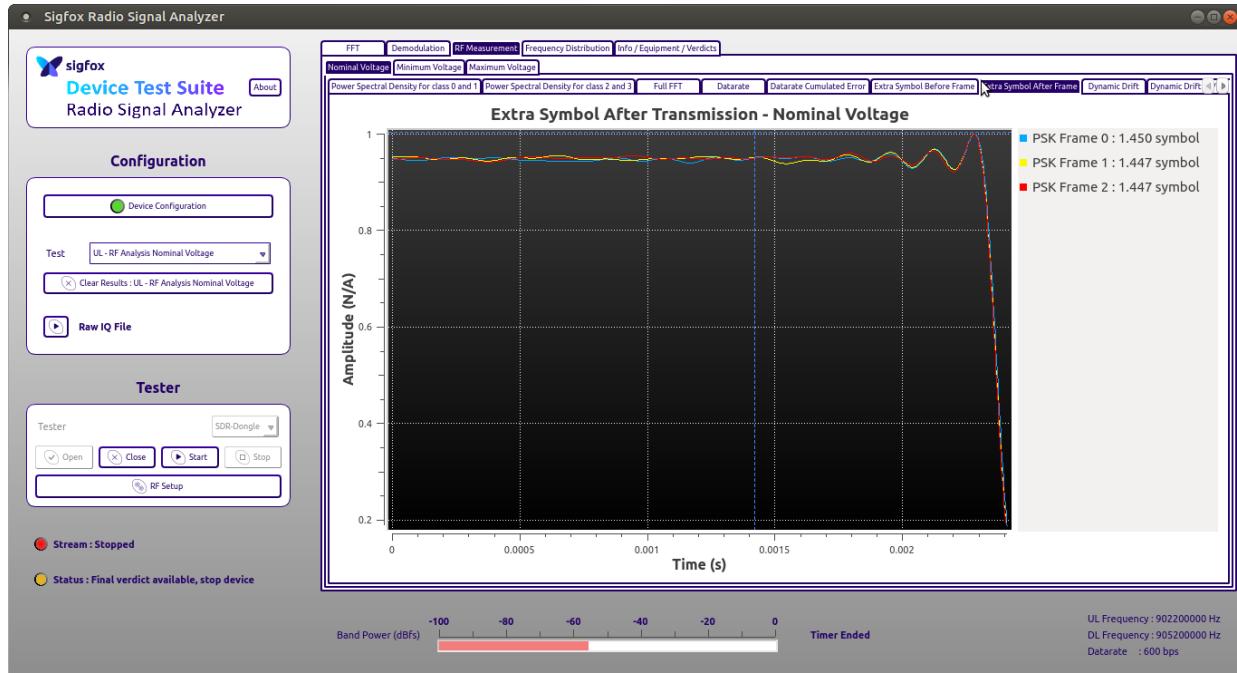


Figure 27: LR11xxMB1DJS RC2 Extra Symbol After Transmission

6.3.10 Dynamic Drift

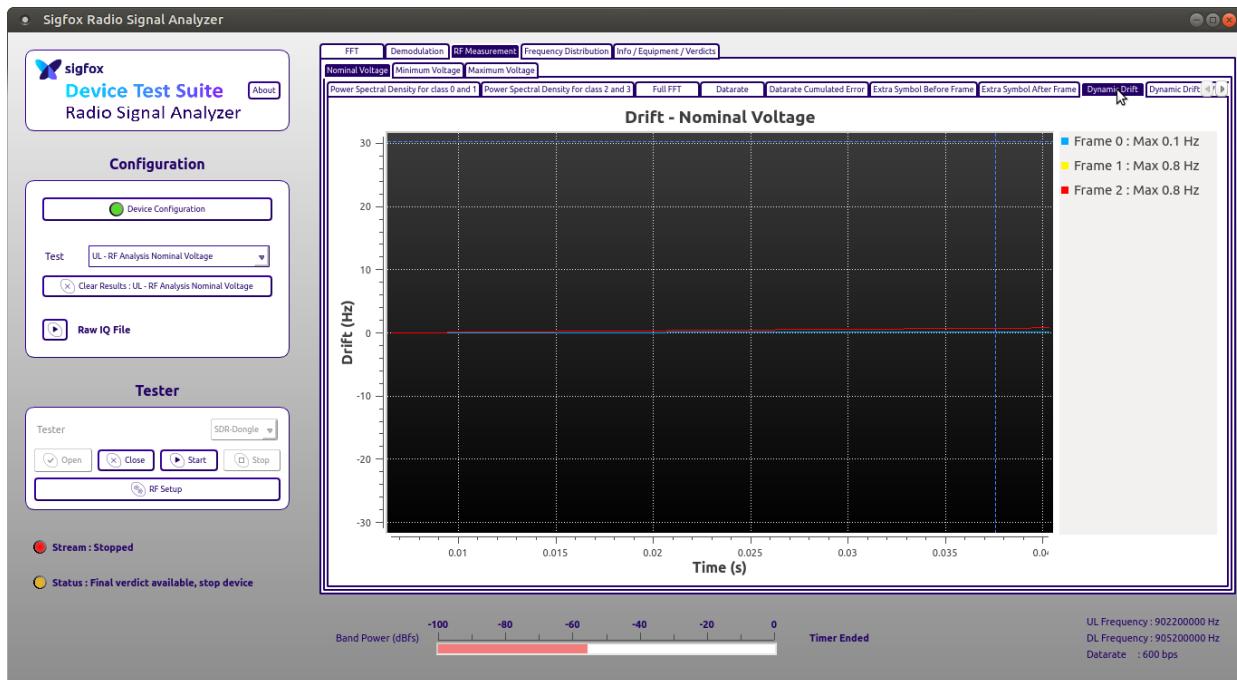


Figure 28: LR11xxMB1DJS RC2 Dynamic Drift

6.3.11 Dynamic Drift per Second

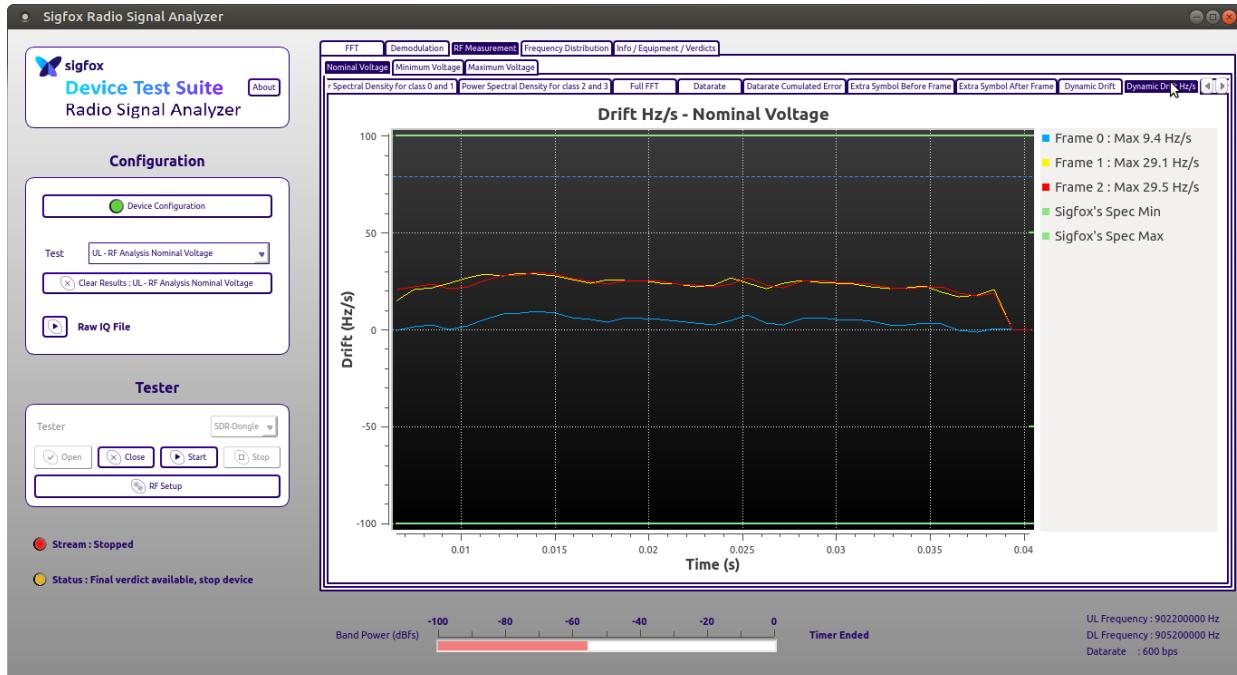


Figure 29: LR11xxMB1DJS RC2 Dynamic Drift per Second

6.3.12 Phase



Figure 30: LR11xxMB1DJS RC2 Phase

6.3.13 Envelope

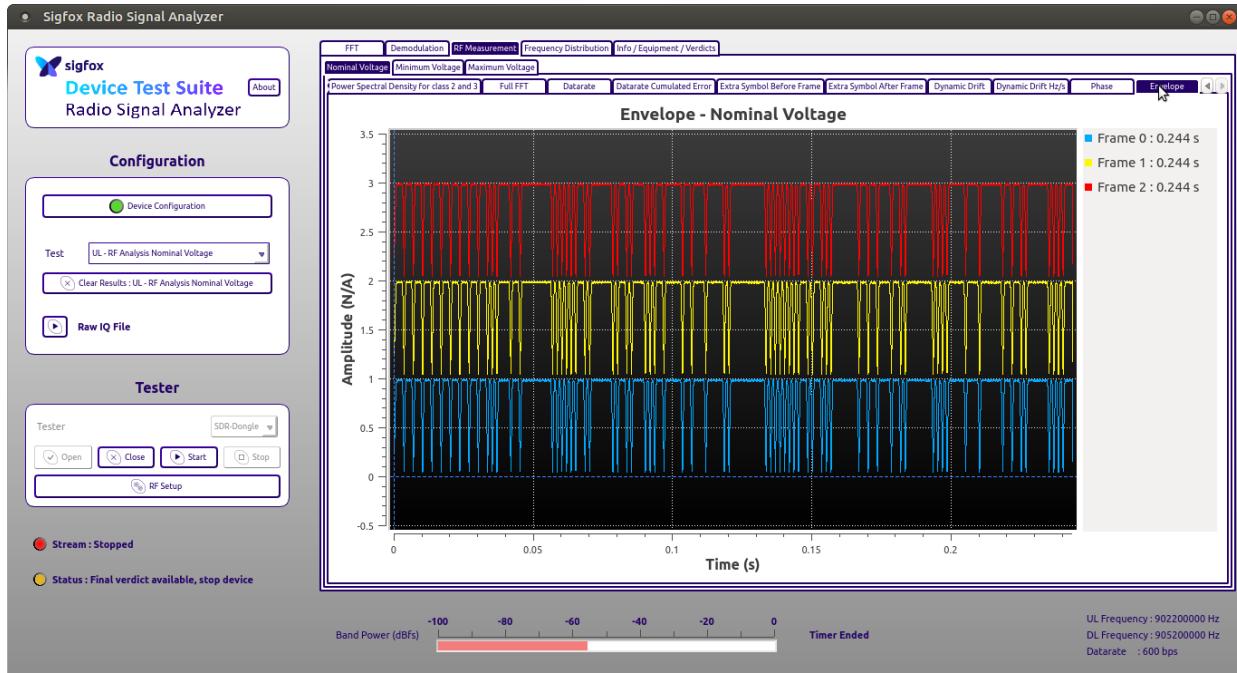


Figure 31: LR11xxMB1DJS RC2 Envelope

6.3.14 Verdict

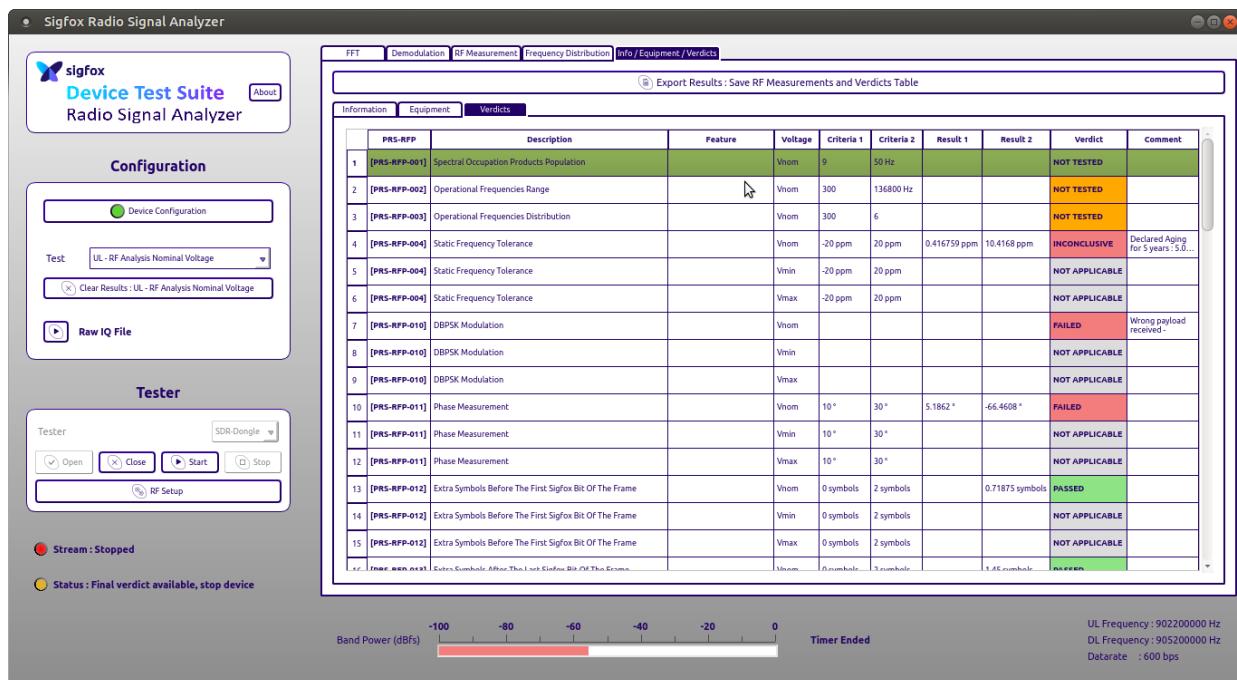


Figure 32: LR11xxMB1DJS RC2 Verdict page 1

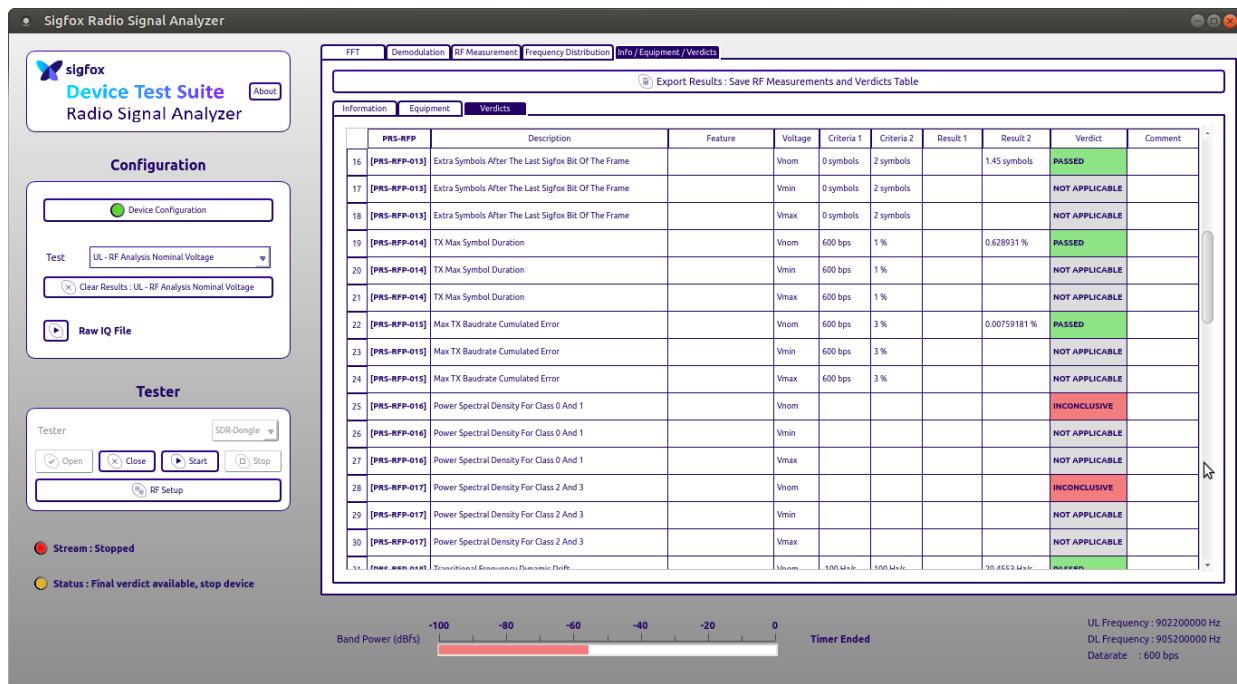


Figure 33: LR11xxMB1DJS RC2 Verdict page 2

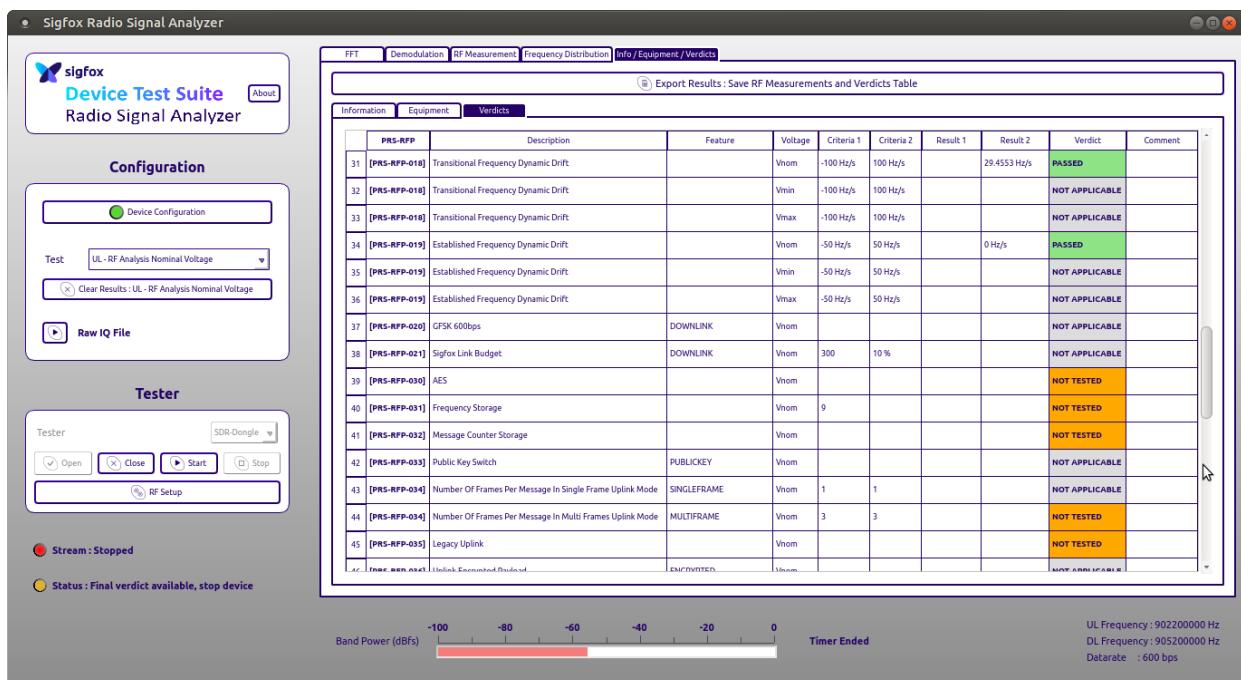


Figure 34: LR11xxMB1DJS RC2 Verdict page 3

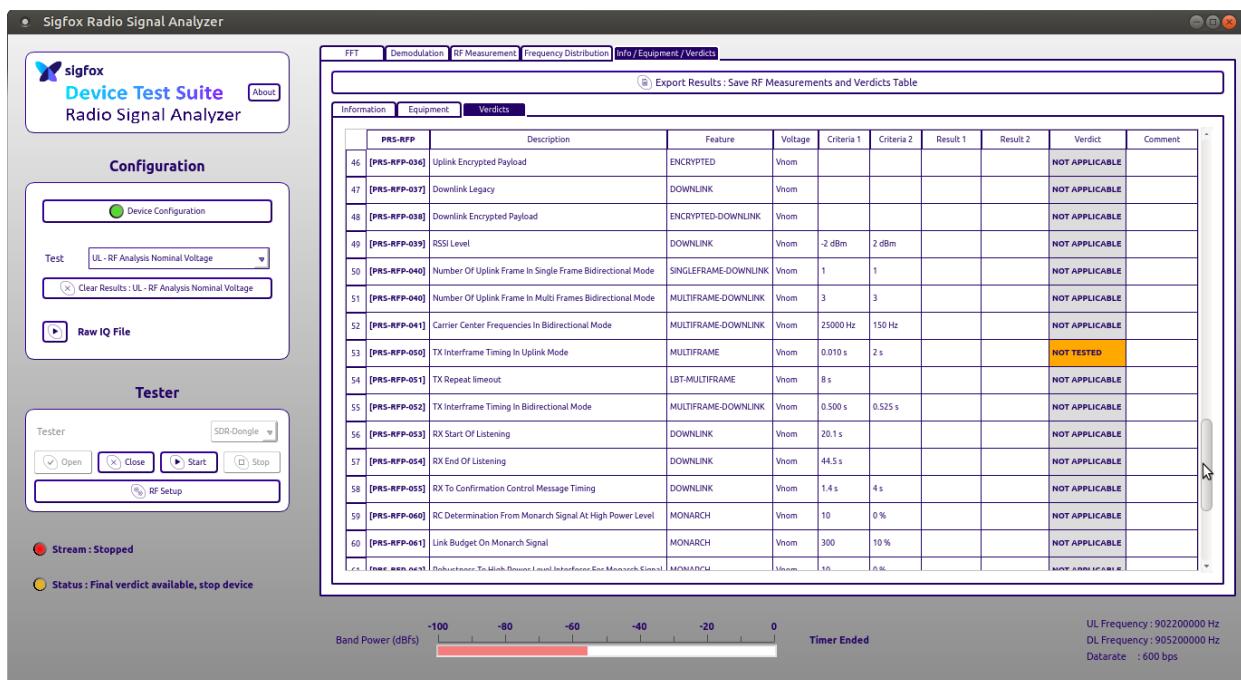


Figure 35: LR11xxMB1DJS RC2 Verdict page 4

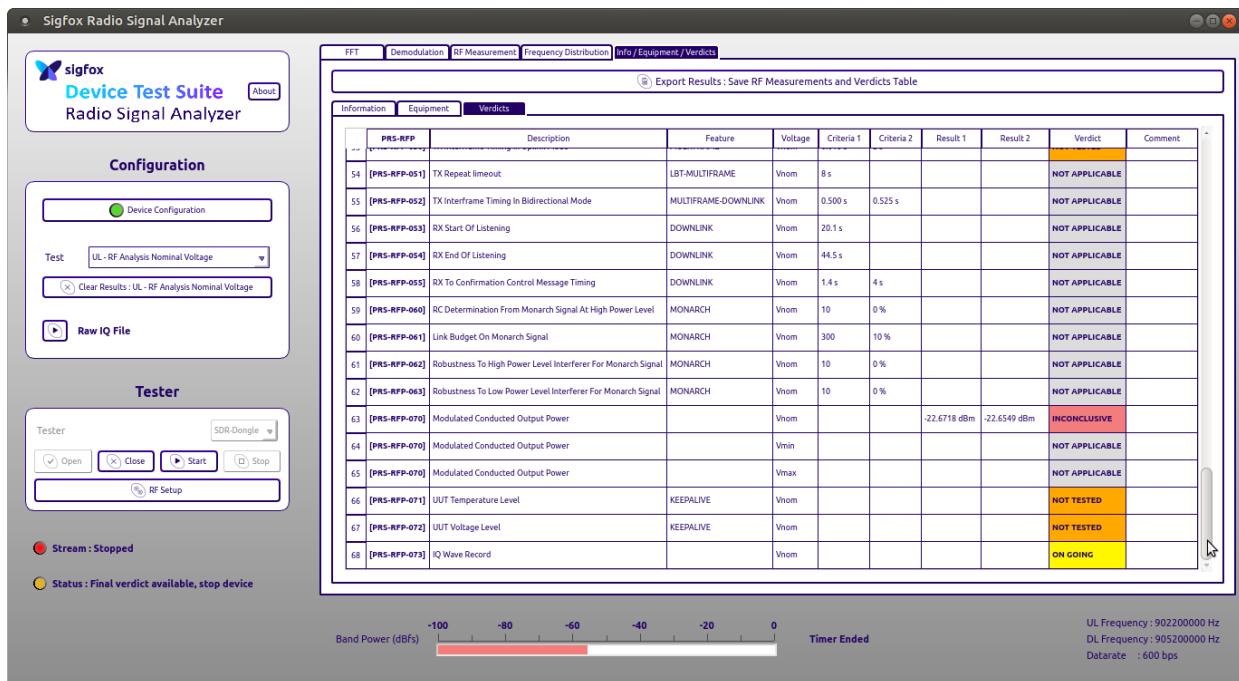


Figure 36: LR11xxMB1DJS RC2 Verdict page 5

6.4 SX1261MB1BAS Measurement Results, XTAL, Radio Configuration RC1

See Section 6.1 for general information about the Sigfox Radio Signal Analyzer tool test results.

6.4.1 Device Configuration

Device Configuration

Library Configuration

- Device ID : ID[0] = 0x98 ... ID[3] = 0xFE FEDCBA98
- Device Private Key : key[0] = 0x01 ... key[15] = 0xEF 0123456789ABCDEF0123456789ABCDEF

Features

- Radio Configuration: RC1
- Message Counter Rollover: 4096
- Downlink capable:
- Monarch Capable:
- Public Key switch capable:
- Payload Encryption Configuration: No Payload Encryption
- Payload Encryption Rollover Counter value: 0
- Multi Frame Capable:
- All Message Types Supported:

Hardware Configuration

- Oscillator Aging for 5 years (ppm): 5.0
- Oscillator Temperature Accuracy (ppm) (Taking into account the operating temperature range): 5.0
- Product's Population Frequency Accuracy (ppm): 5.0
- Minimum Voltage = Nominal Voltage:
- Maximum Voltage = Nominal Voltage:

Supported Frame Types Configuration

Frame Type	Status
Frame Type No Payload	<input type="checkbox"/>
Frame Type Bit (False)	<input checked="" type="checkbox"/>
Frame Type Bit (True)	<input checked="" type="checkbox"/>
Keep Alive Frame Type	<input checked="" type="checkbox"/>
1 Byte Frame Type	<input checked="" type="checkbox"/>
2 Bytes Frame Type	<input checked="" type="checkbox"/>
3 Bytes Frame Type	<input checked="" type="checkbox"/>
4 Bytes Frame Type	<input checked="" type="checkbox"/>
5 Bytes Frame Type	<input checked="" type="checkbox"/>
6 Bytes Frame Type	<input checked="" type="checkbox"/>
7 Bytes Frame Type	<input checked="" type="checkbox"/>
8 Bytes Frame Type	<input checked="" type="checkbox"/>
9 Bytes Frame Type	<input checked="" type="checkbox"/>
10 Bytes Frame Type	<input checked="" type="checkbox"/>
11 Bytes Frame Type	<input checked="" type="checkbox"/>
12 Bytes Frame Type	<input checked="" type="checkbox"/>

Buttons: Apply Settings, Cancel

Figure 37: SX1261MB1BAS RC1 Device Configuration

6.4.2 FFT

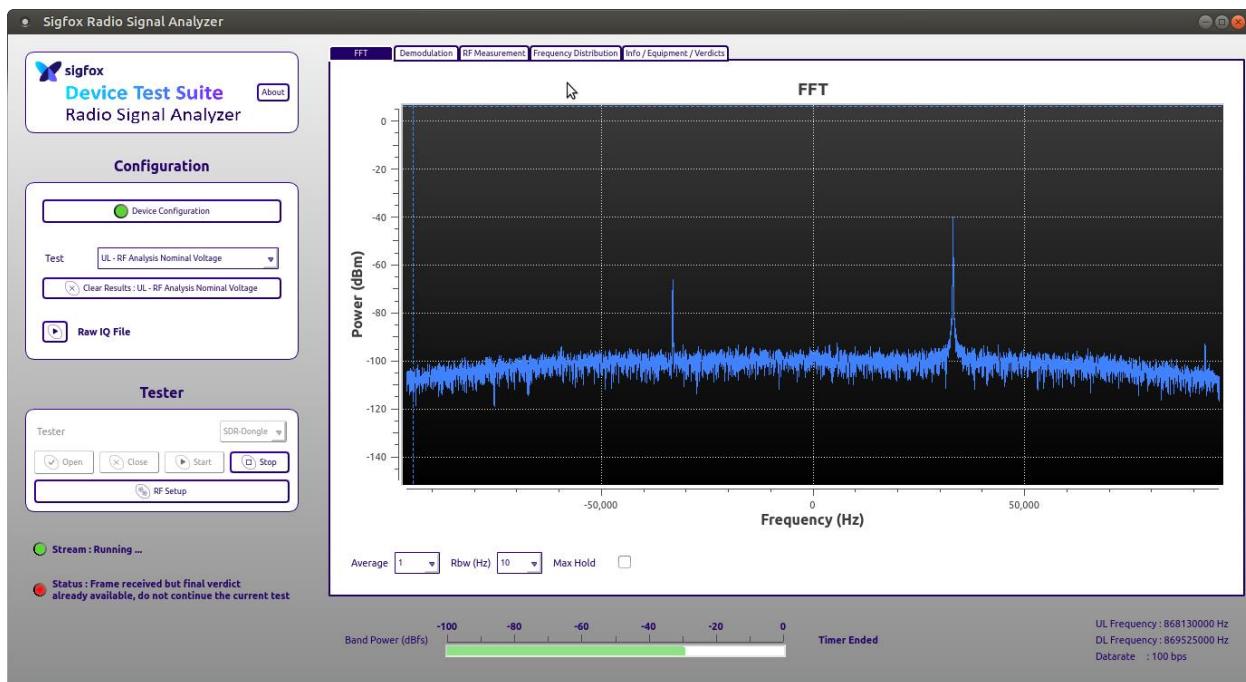


Figure 38: SX1261MB1BAS RC1 FFT

6.4.3 Demodulation

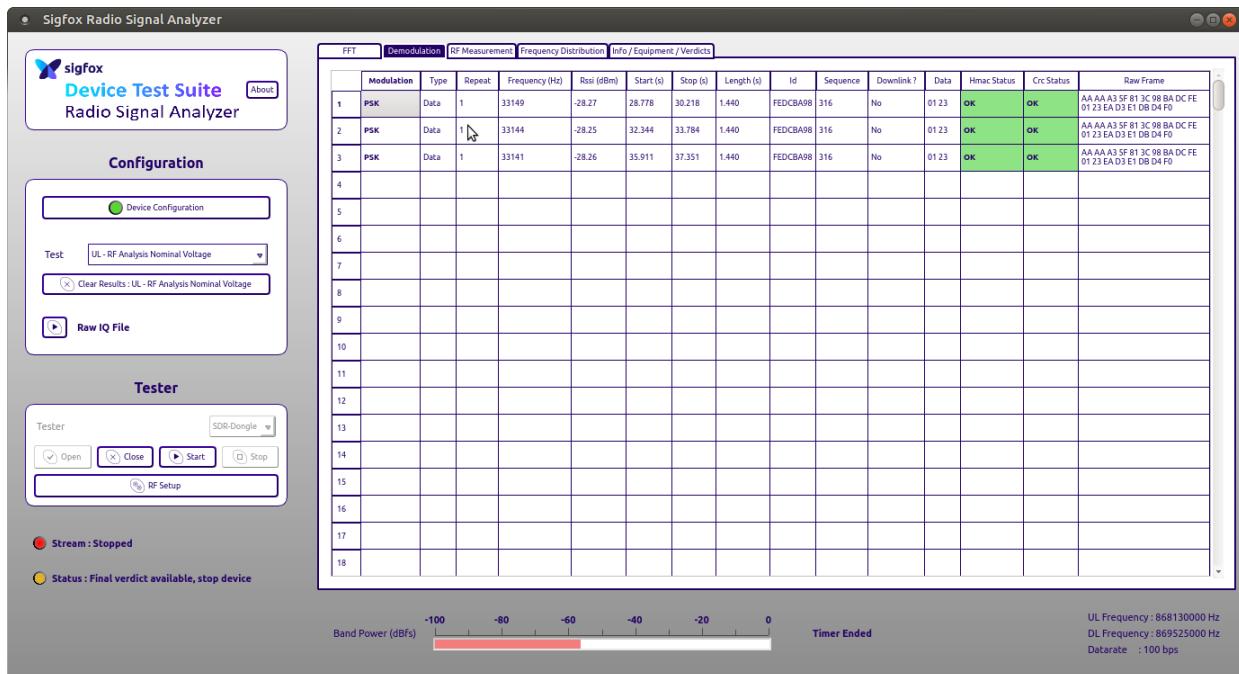


Figure 39: SX1261MB1BAS RC1 Demodulation

6.4.4 Power Spectral Density for Class 0 & 1

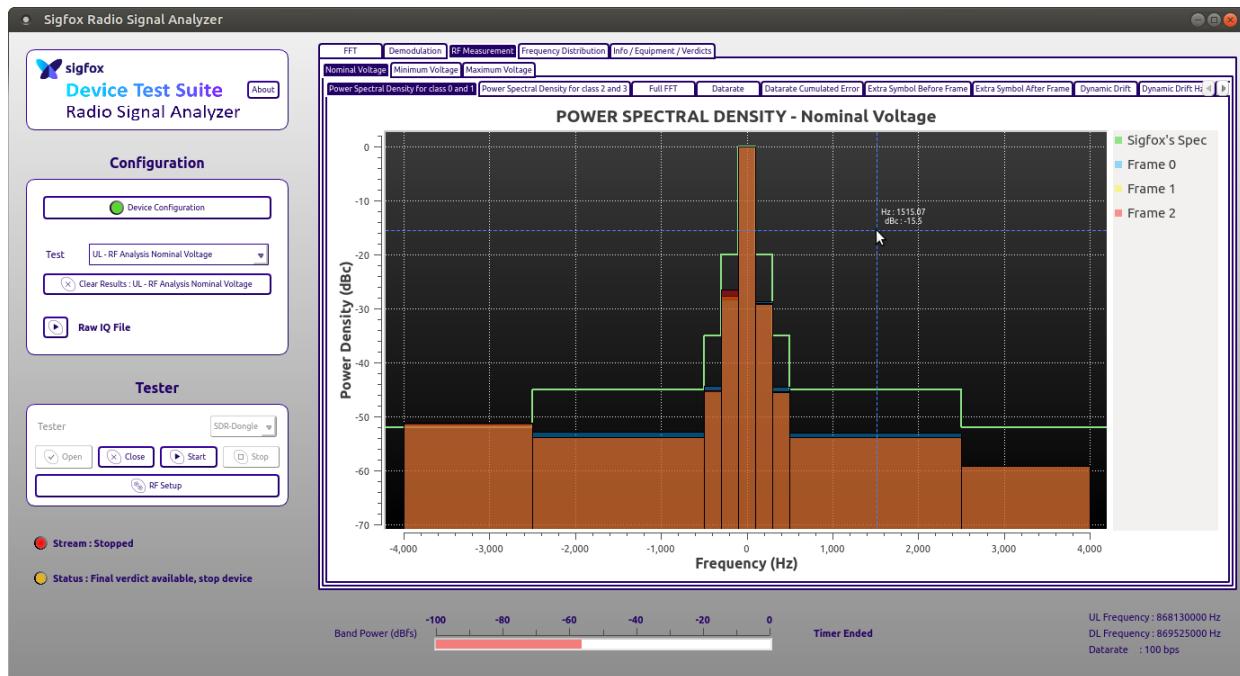


Figure 40: SX1261MB1BAS RC1 Power Spectral Density for Class 0 & 1

6.4.5 Full FFT

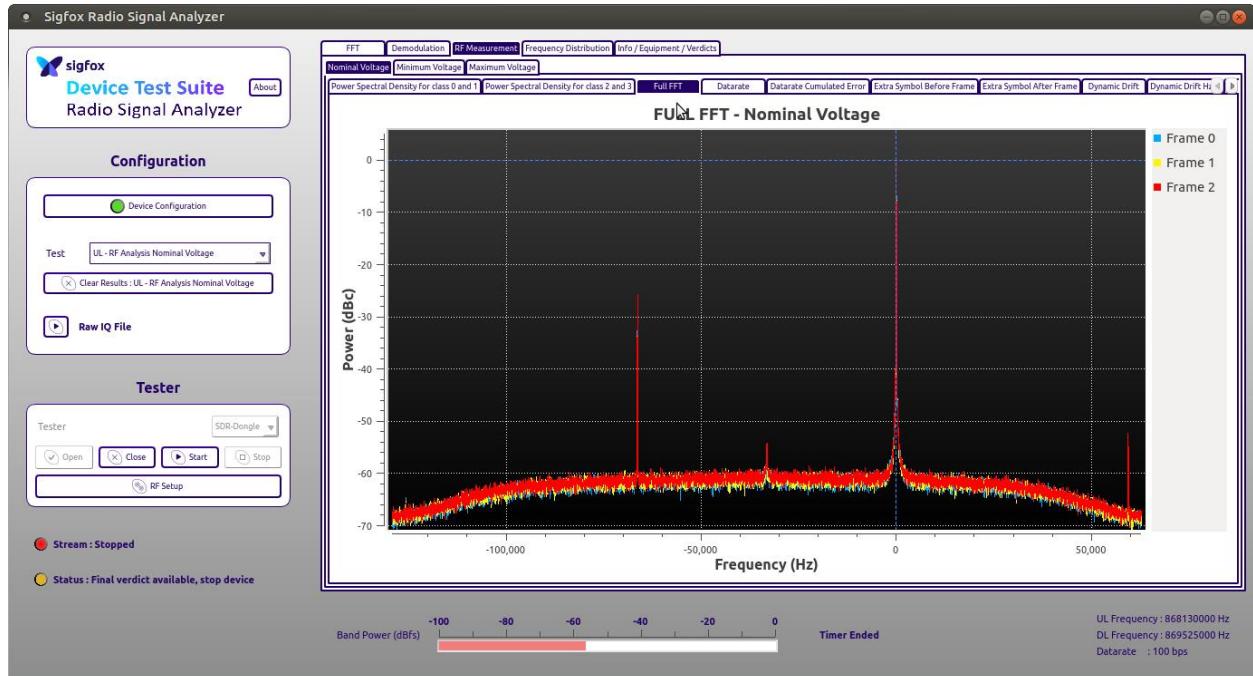


Figure 41: SX1261MB1BAS RC1 Full FFT

6.4.6 Data Rate

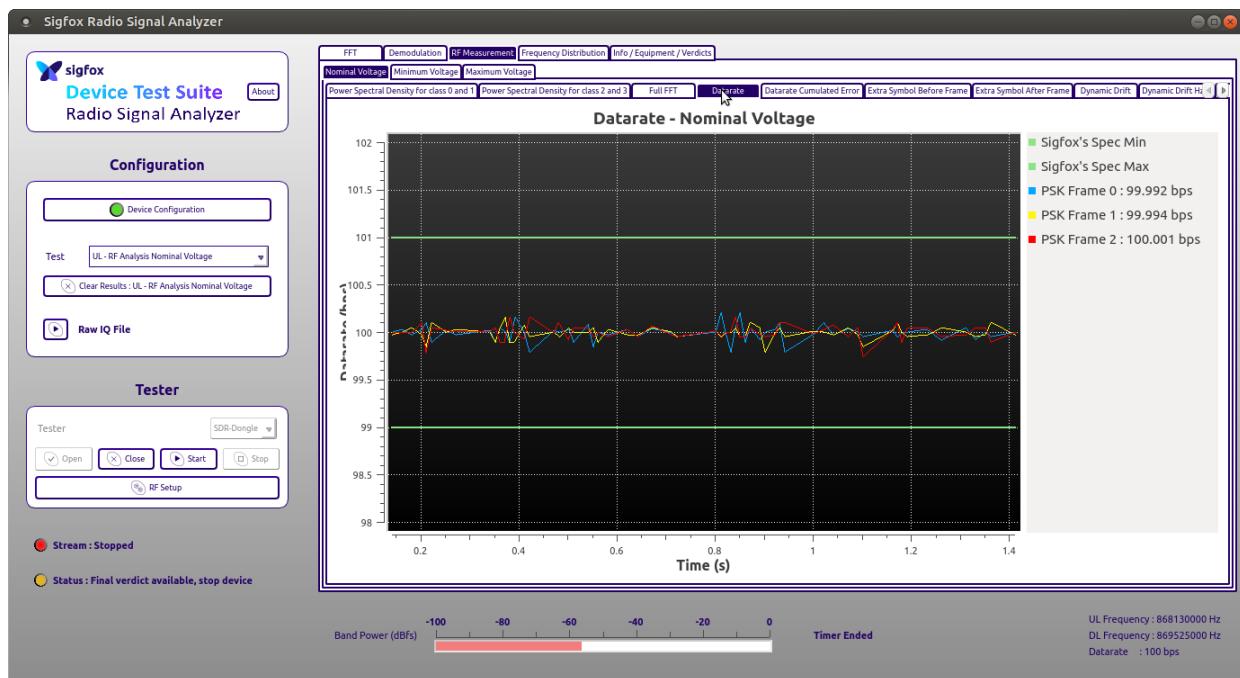


Figure 42: SX1261MB1BAS RC1 Data Rate

6.4.7 Data Rate Cumulated Error

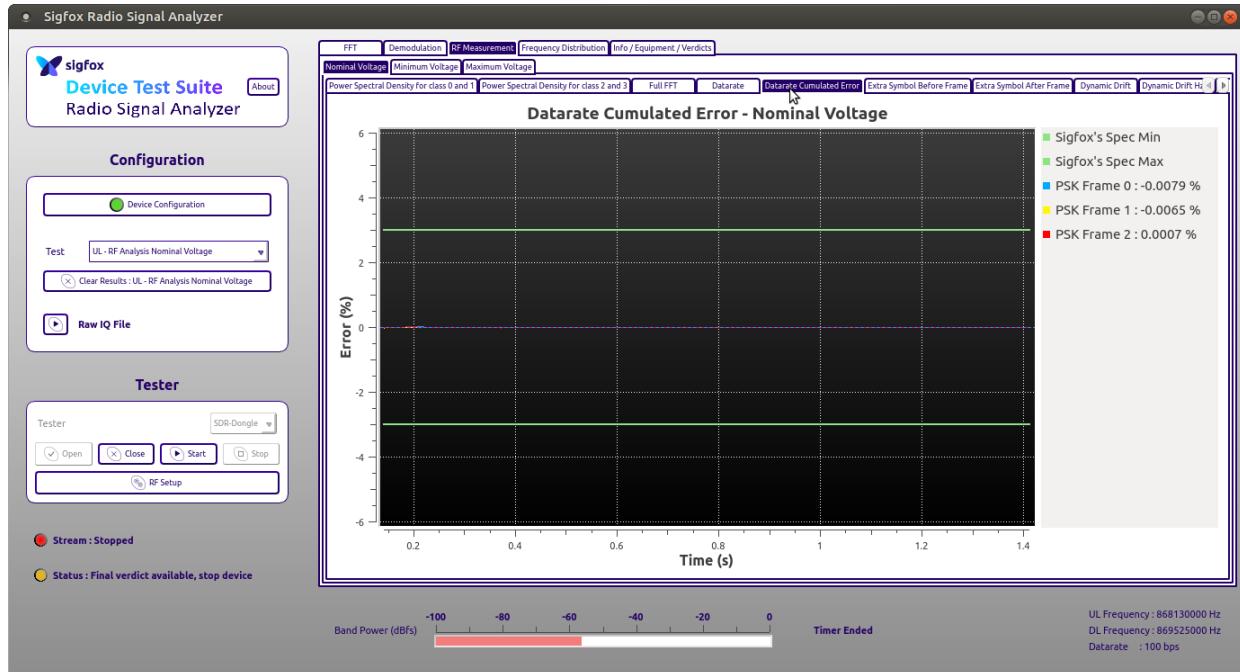


Figure 43: SX1261MB1BAS RC1 Data Rate Cumulated Error

6.4.8 Extra Symbol Before Transmission

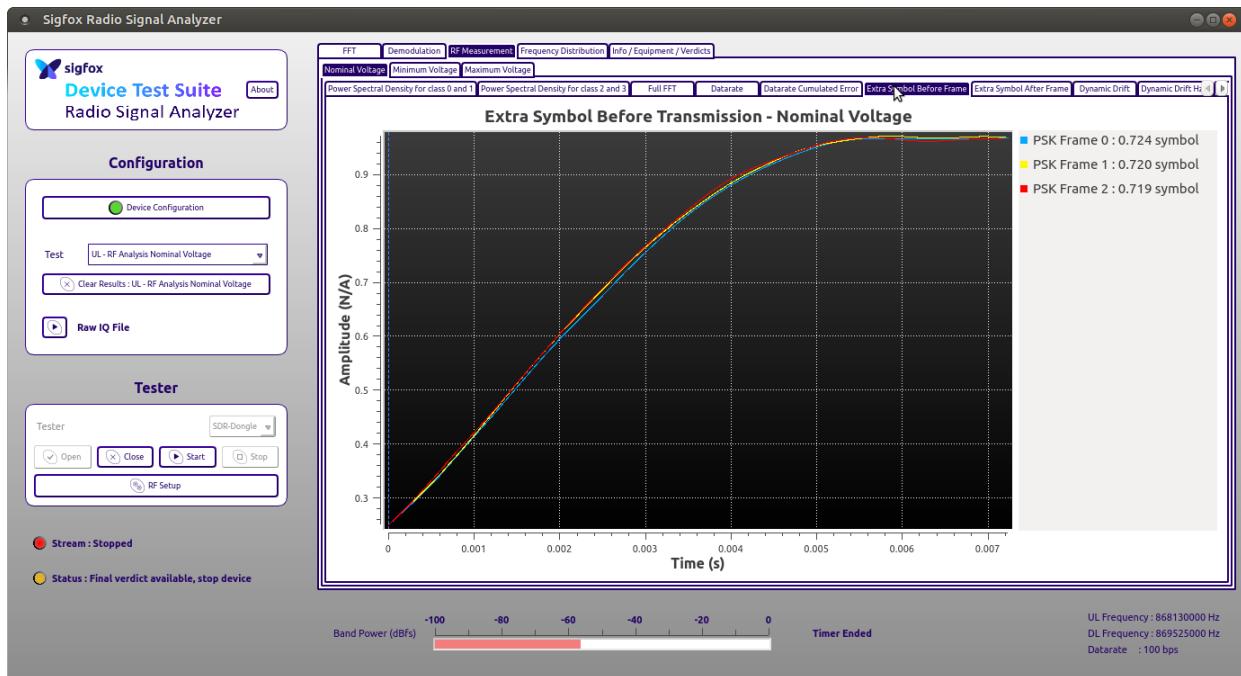


Figure 44: SX1261MB1BAS RC1 Extra Symbol Before Transmission

6.4.9 Extra Symbol After Transmission

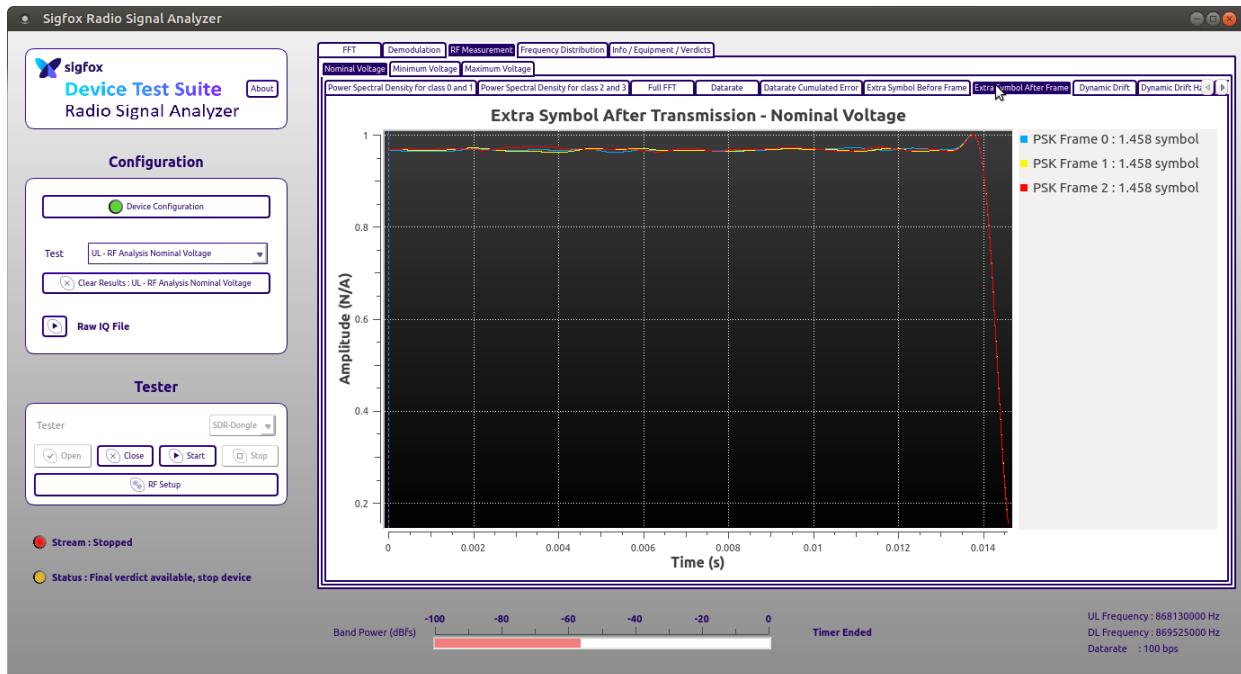


Figure 45: SX1261MB1BAS RC1 Extra Symbol After Transmission

6.4.10 Dynamic Drift

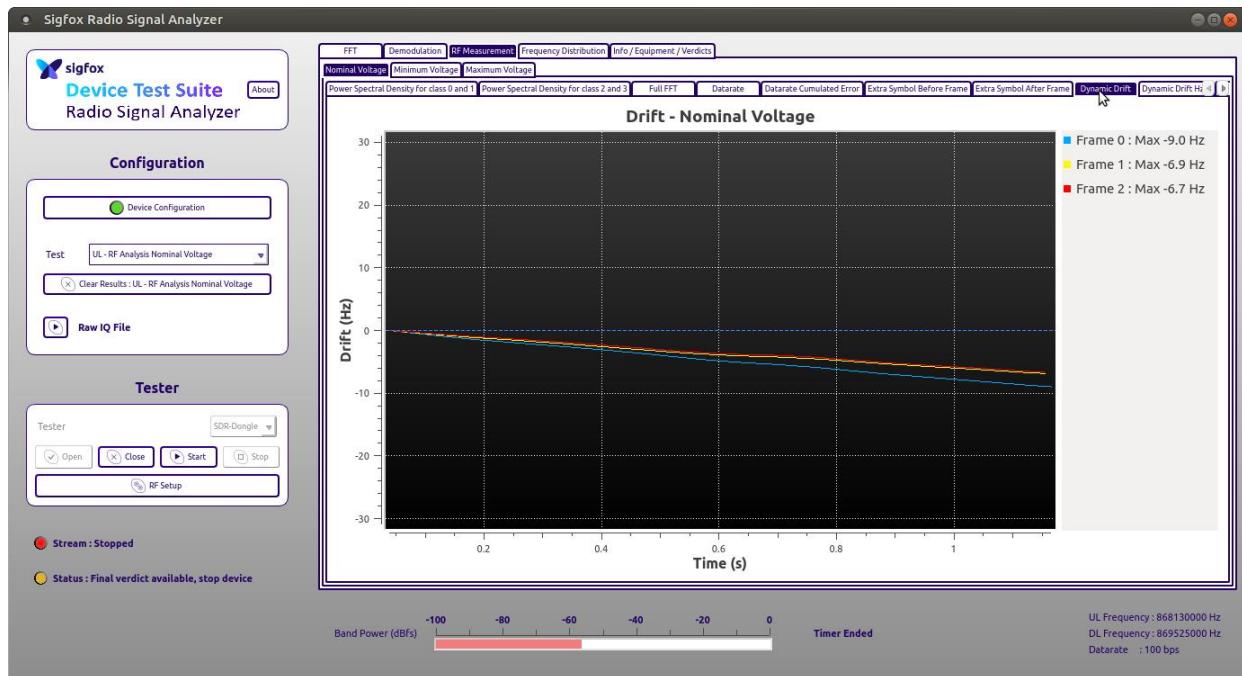


Figure 46: SX1261MB1BAS RC1 Dynamic Drift

6.4.11 Dynamic Drift per Second

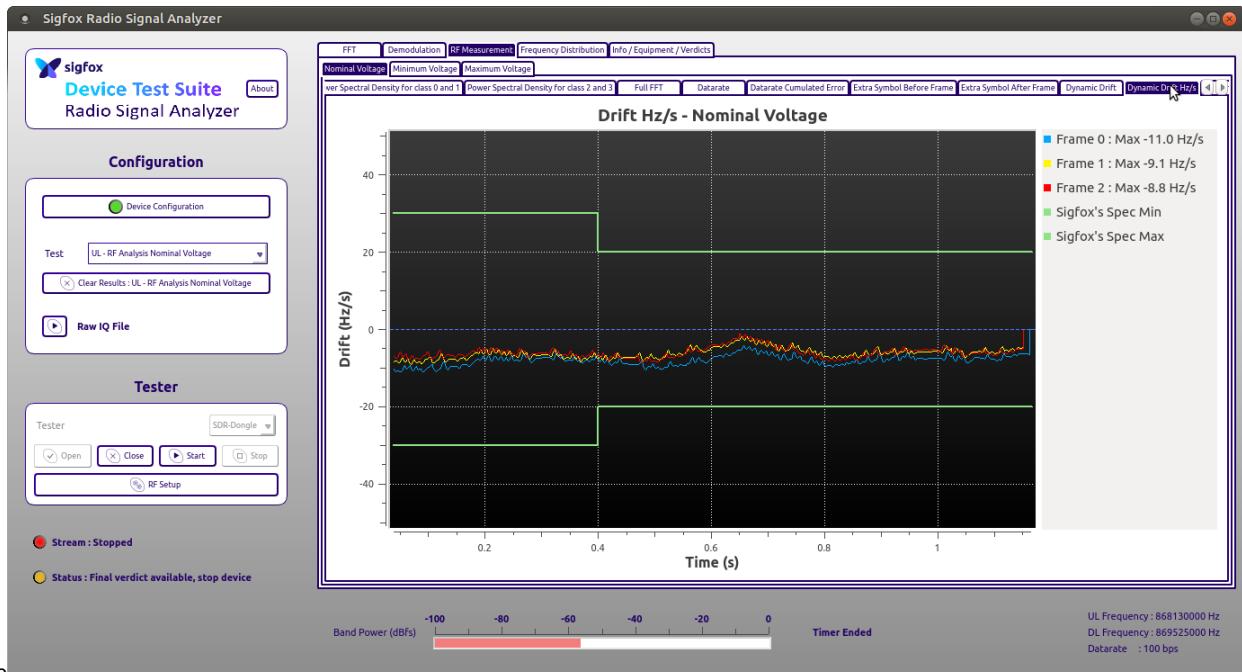


Figure 47: SX1261MB1BAS RC1 Dynamic Drift per Second

6.4.12 Phase

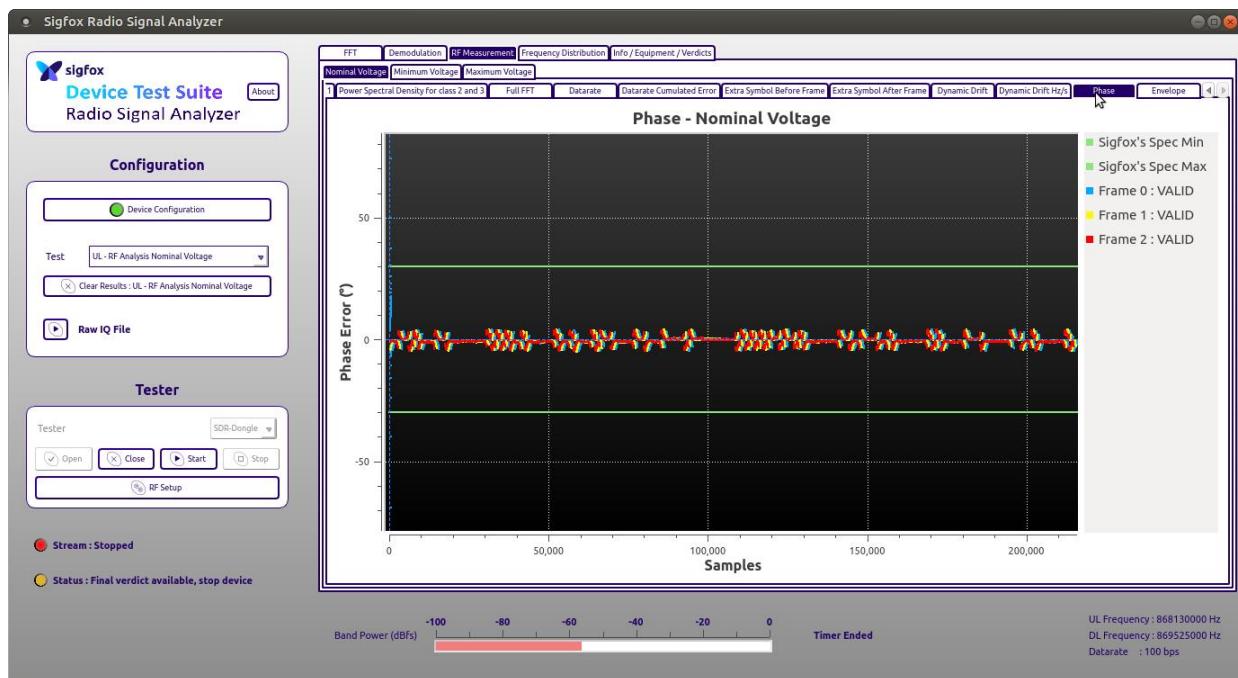


Figure 48: SX1261MB1BAS RC1 Phase

6.4.13 Envelope

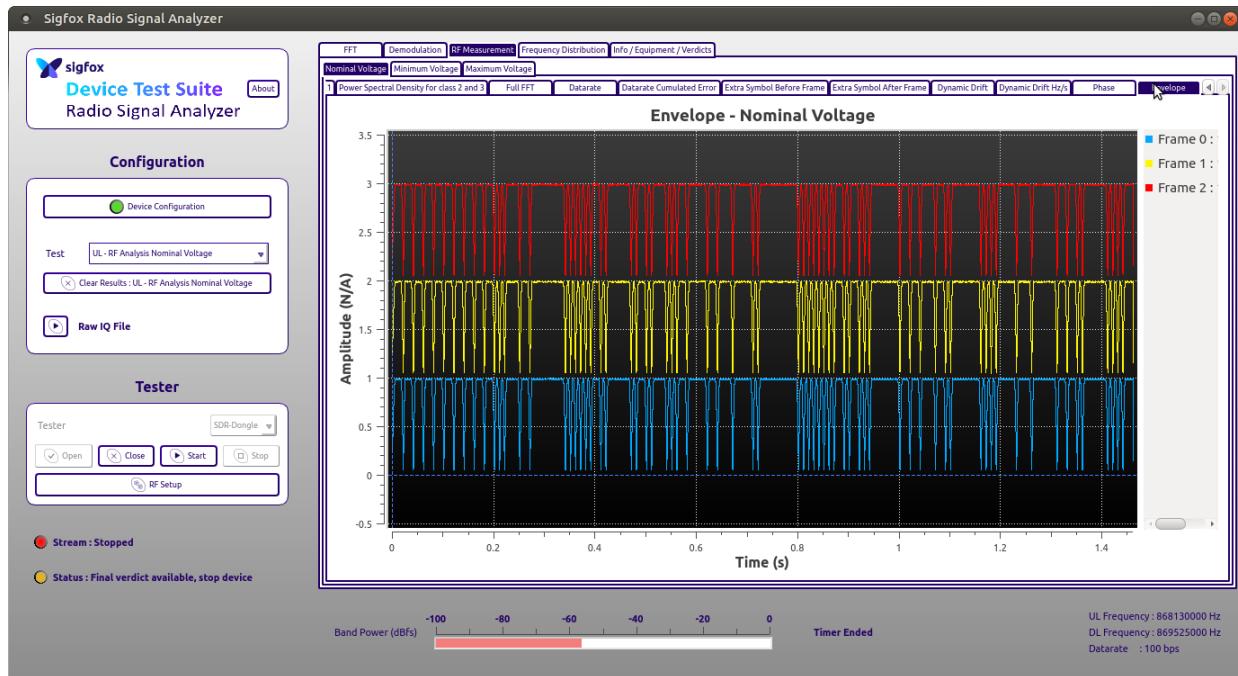


Figure 49: SX1261MB1BAS RC1 Envelope

6.4.14 Verdict

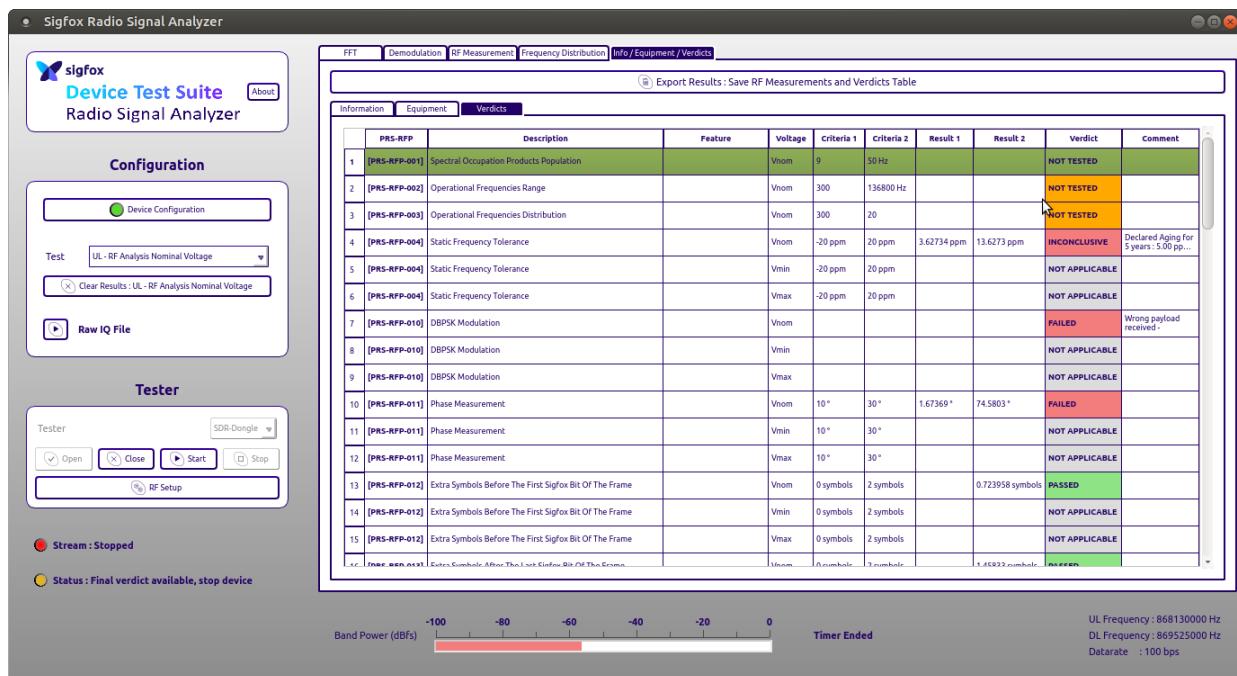


Figure 50: SX1261MB1BAS RC1 Verdict Page 1

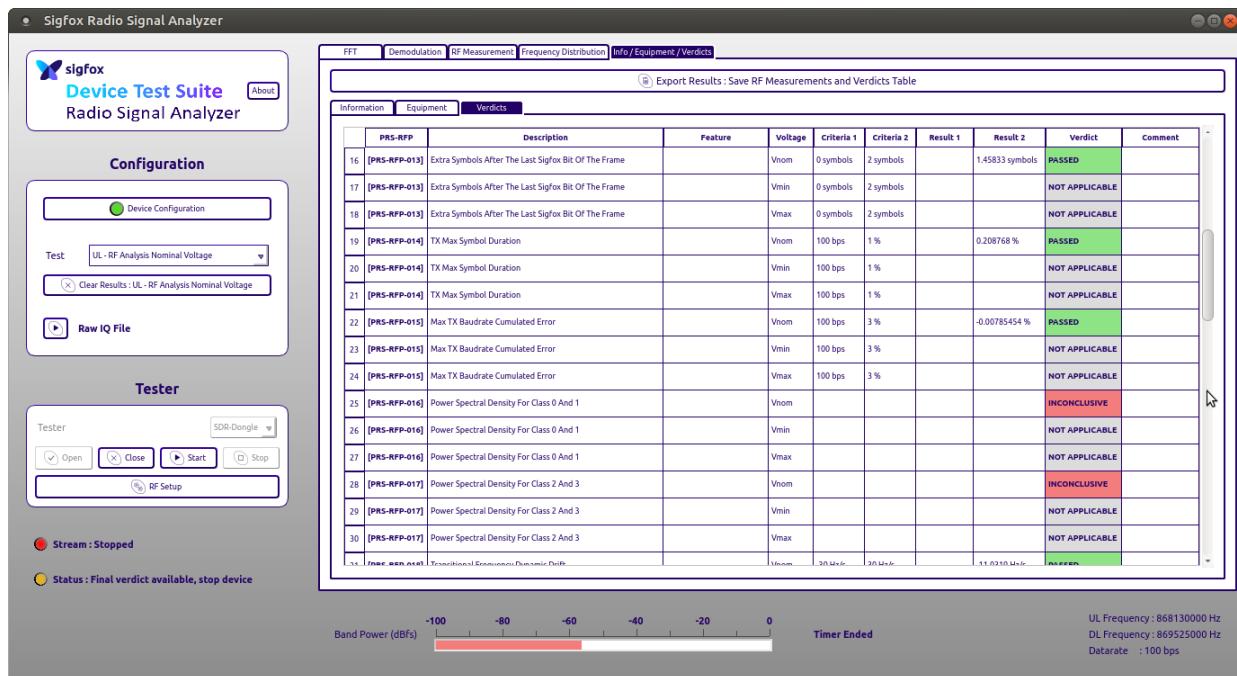


Figure 51: SX1261MB1BAS RC1 Verdict Page 2

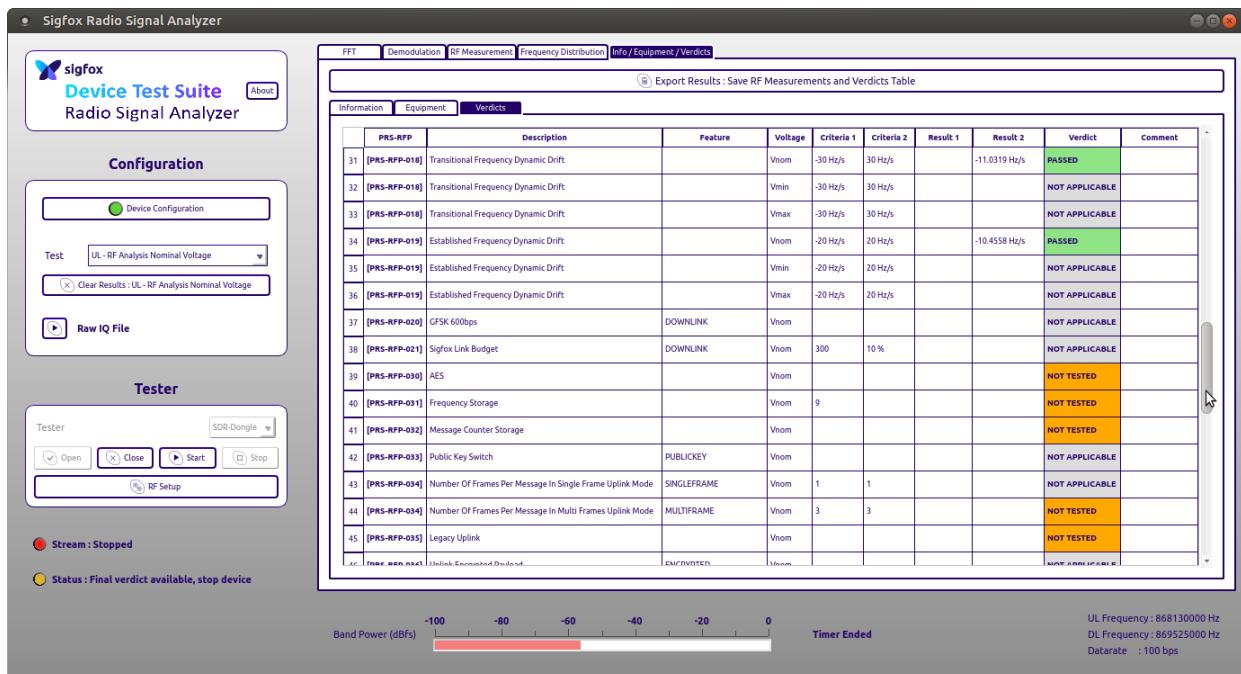


Figure 52: SX1261MB1BAS RC1 Verdict Page 3

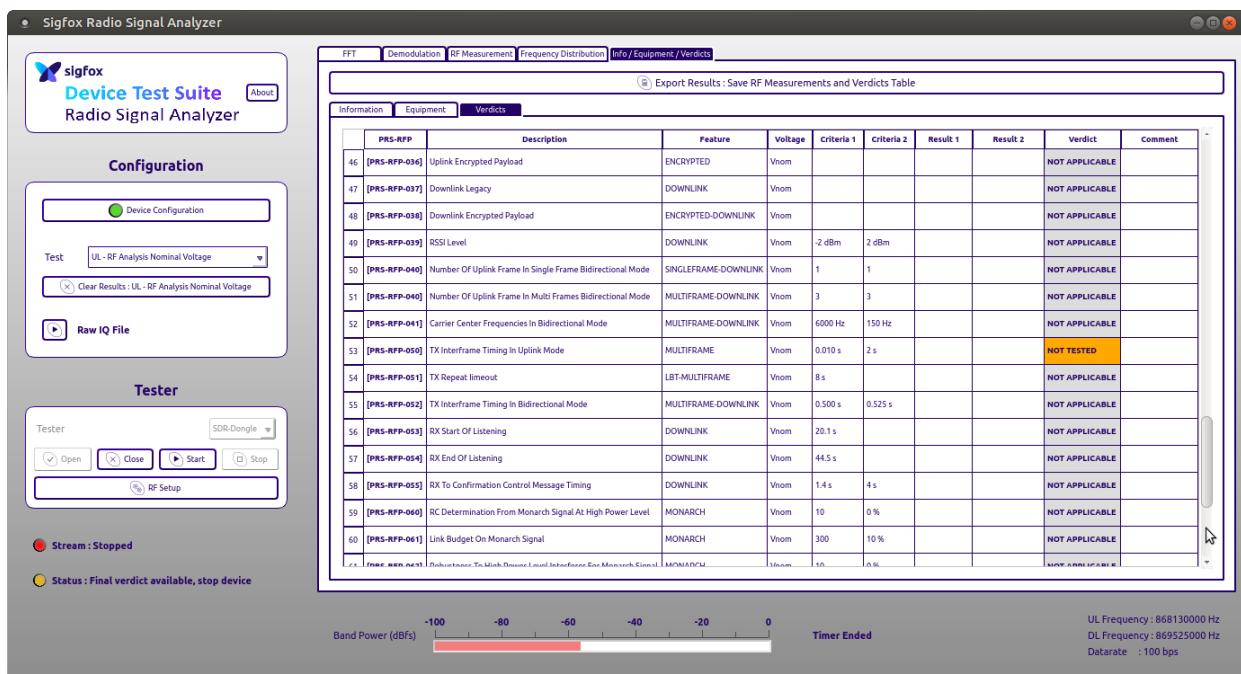


Figure 53: SX1261MB1BAS RC1 Verdict Page 4

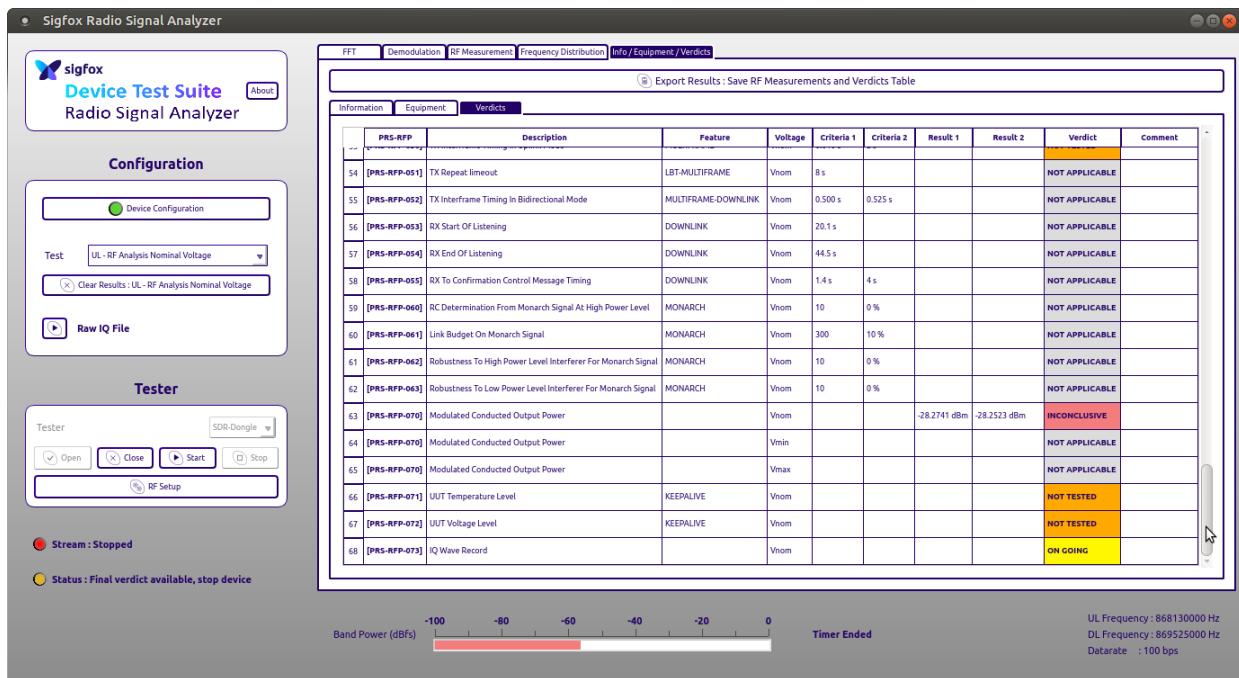


Figure 54: SX1261MB1BAS RC1 Verdict page 5

6.5 SX1261MB1BAS Measurement Results, XTAL, Radio Configuration RC2

See Section 6.1 for general information about the Sigfox Radio Signal Analyzer tool test results.

For the RC2 case, note:

- in Figures 6.5.6 and 6.5.14, the test “PRS-RFP-014: Tx Max symbol duration” fails due to XTAL inaccuracy.
- in Figures 6.5.10 and 6.5.11, the Sigfox Radio Signal Analyzer tool fails to properly measure drift.

6.5.1 Device Configuration

Device Configuration

Library Configuration

Features

Hardware Configuration

Supported Frame Types Configuration

Any change of static configuration will result in a RESET of Information / Verdicts tables

Frame Type	Status
Frame Type No Payload	<input type="checkbox"/>
Frame Type Bit (False)	<input checked="" type="checkbox"/>
Frame Type Bit (True)	<input checked="" type="checkbox"/>
Keep Alive Frame Type	<input checked="" type="checkbox"/>
1 Byte Frame Type	<input checked="" type="checkbox"/>
2 Bytes Frame Type	<input checked="" type="checkbox"/>
3 Bytes Frame Type	<input checked="" type="checkbox"/>
4 Bytes Frame Type	<input checked="" type="checkbox"/>
5 Bytes Frame Type	<input checked="" type="checkbox"/>
6 Bytes Frame Type	<input checked="" type="checkbox"/>
7 Bytes Frame Type	<input checked="" type="checkbox"/>
8 Bytes Frame Type	<input checked="" type="checkbox"/>
9 Bytes Frame Type	<input checked="" type="checkbox"/>
10 Bytes Frame Type	<input checked="" type="checkbox"/>
11 Bytes Frame Type	<input checked="" type="checkbox"/>
12 Bytes Frame Type	<input checked="" type="checkbox"/>

Figure 55: SX1261MB1BAS RC2 Device Configuration

6.5.2 FFT

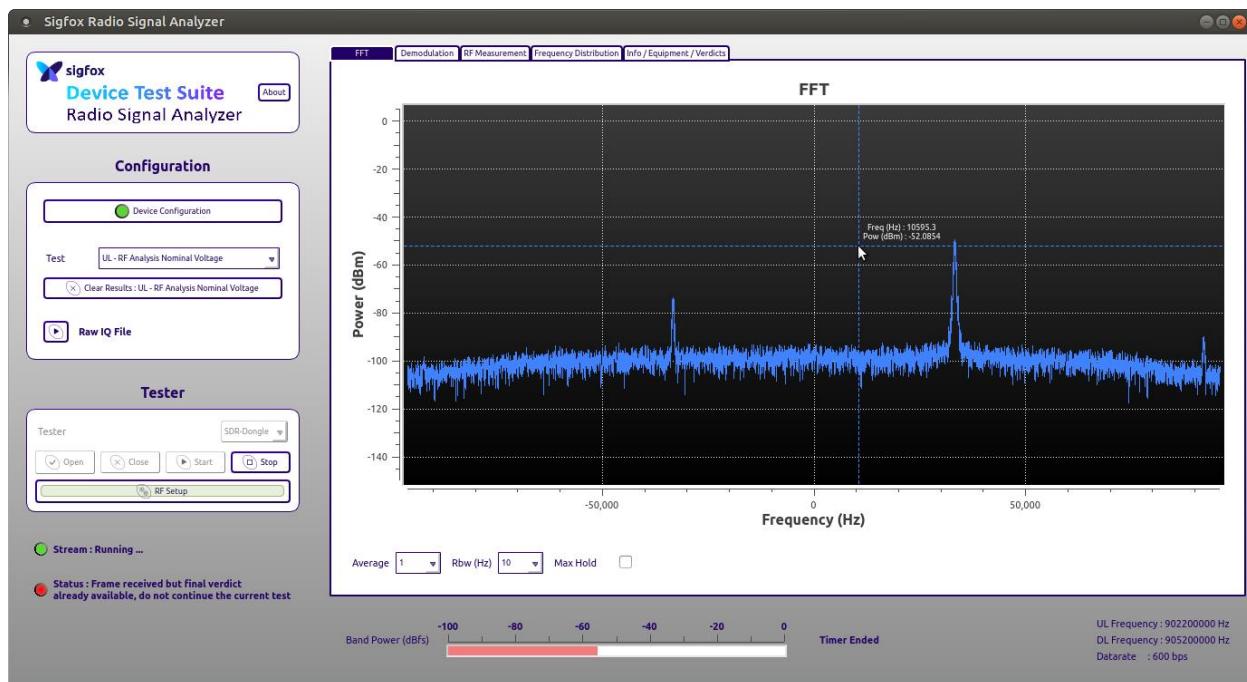


Figure 56: SX1261MB1BAS RC2 FFT

6.5.3 Demodulation

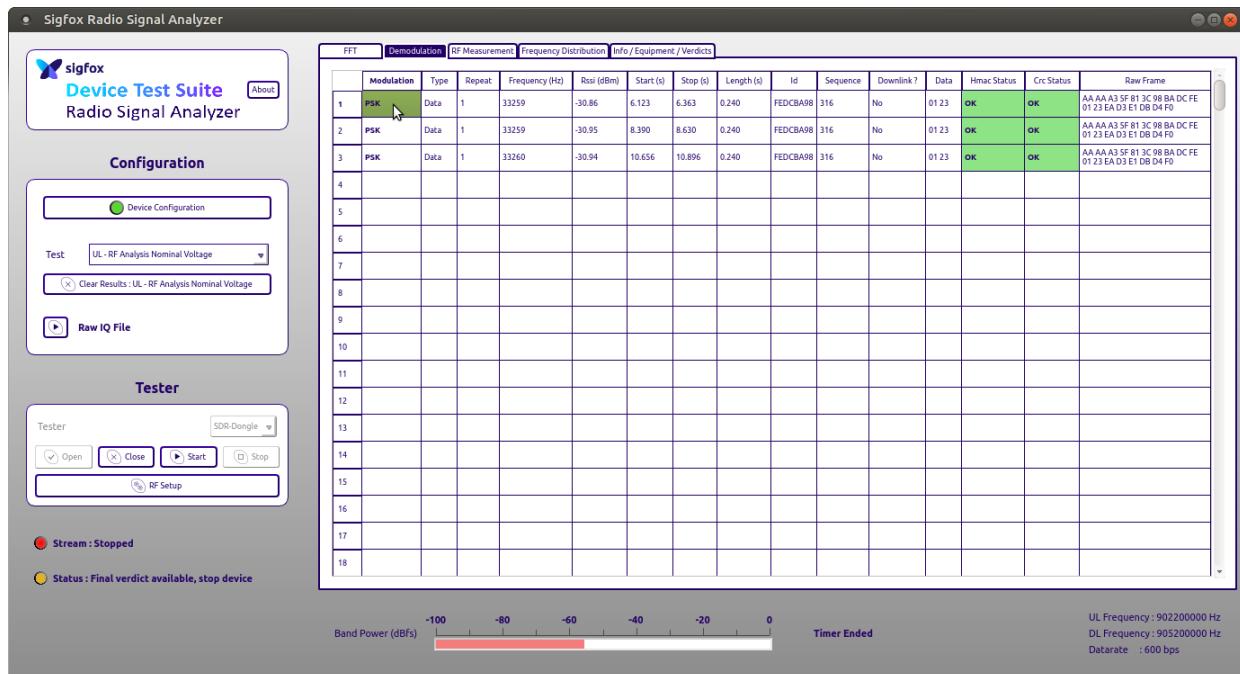


Figure 57: SX1261MB1BAS RC2 Demodulation

6.5.4 Power Spectral Density for Class 0 & 1

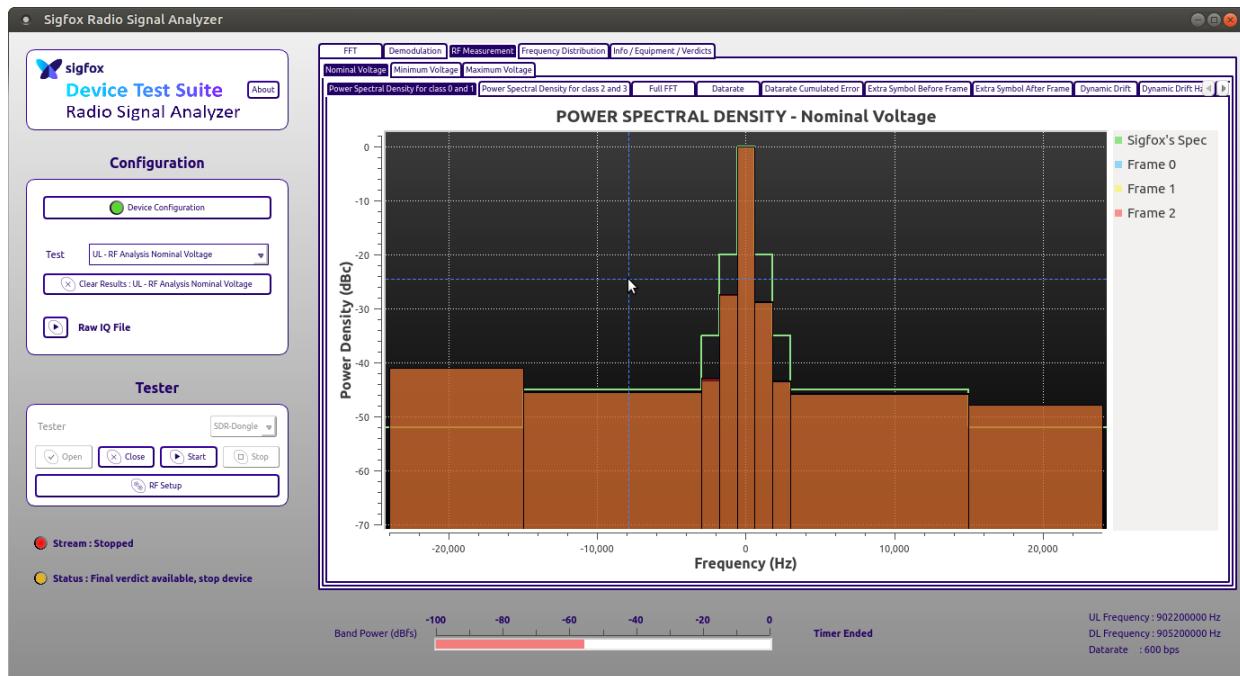


Figure 58: SX1261MB1BAS RC2 Power Spectral Density for Class 0 & 1

6.5.5 Full FFT

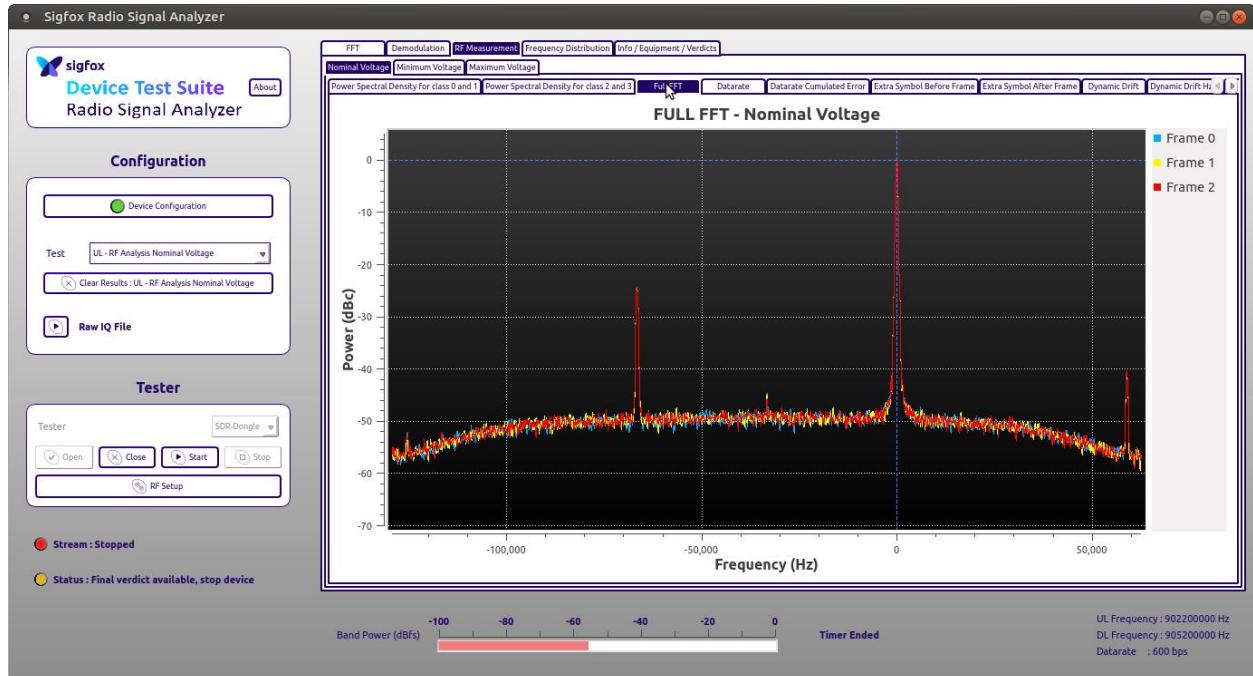


Figure 59: SX1261MB1BAS RC2 Full FFT

6.5.6 Data Rate

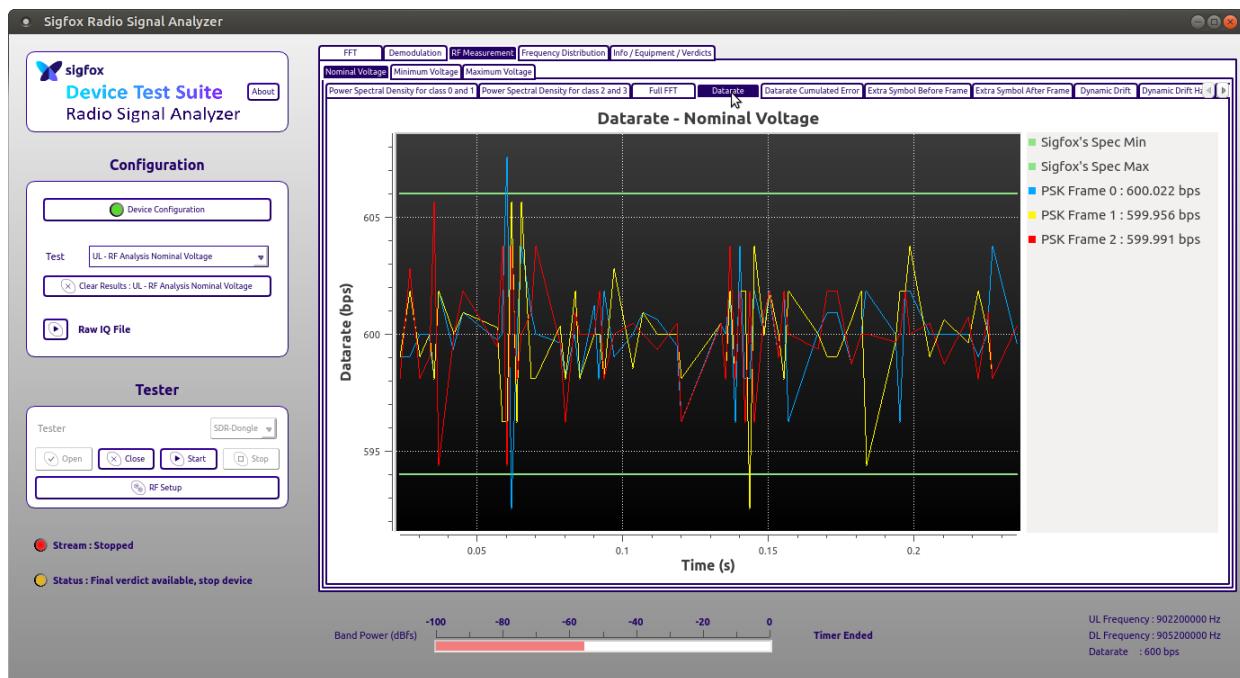


Figure 60: SX1261MB1BAS RC2 Data Rate

6.5.7 Data Rate Cumulated Error

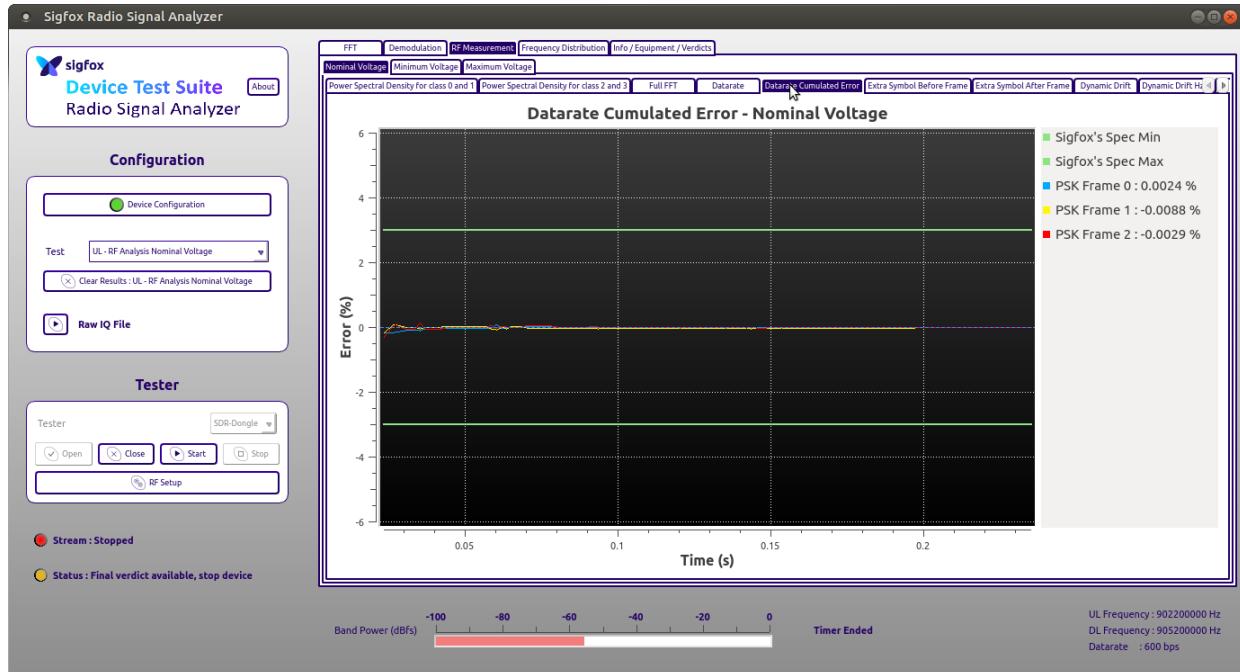


Figure 61: SX1261MB1BAS RC2 Data Rate Cumulated Error

6.5.8 Extra Symbol Before Transmission

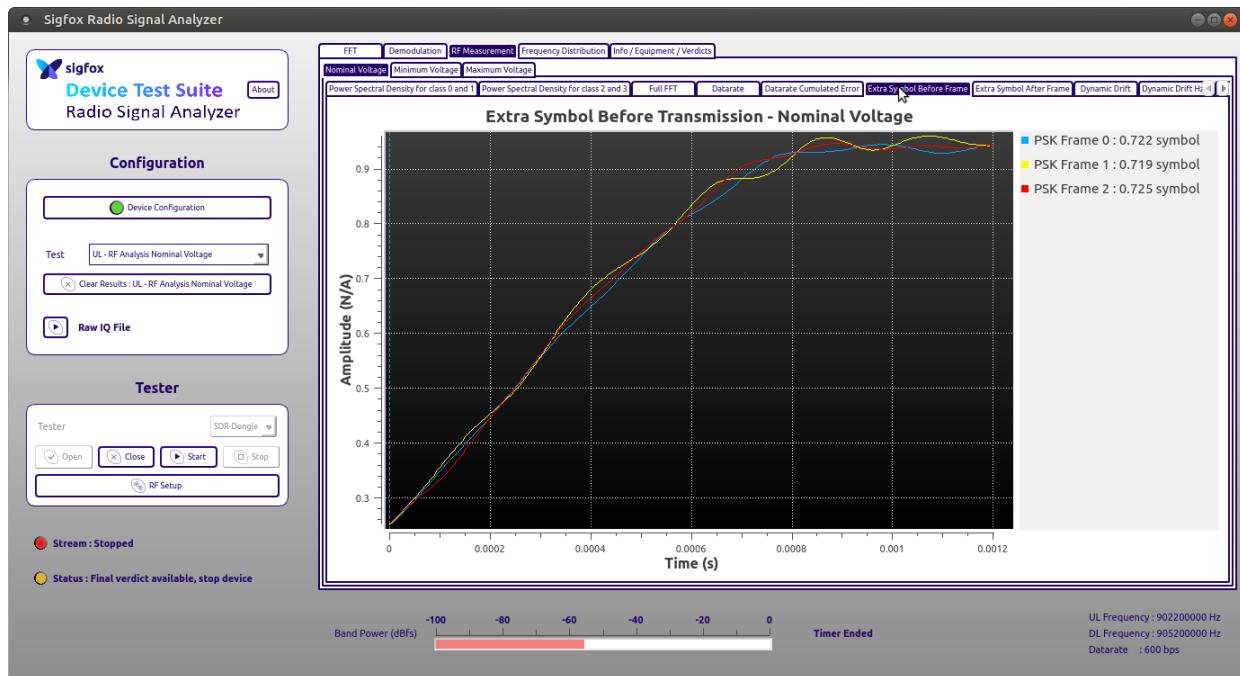


Figure 62: SX1261MB1BAS RC2 Extra Symbol Before Transmission

6.5.9 Extra Symbol After Transmission

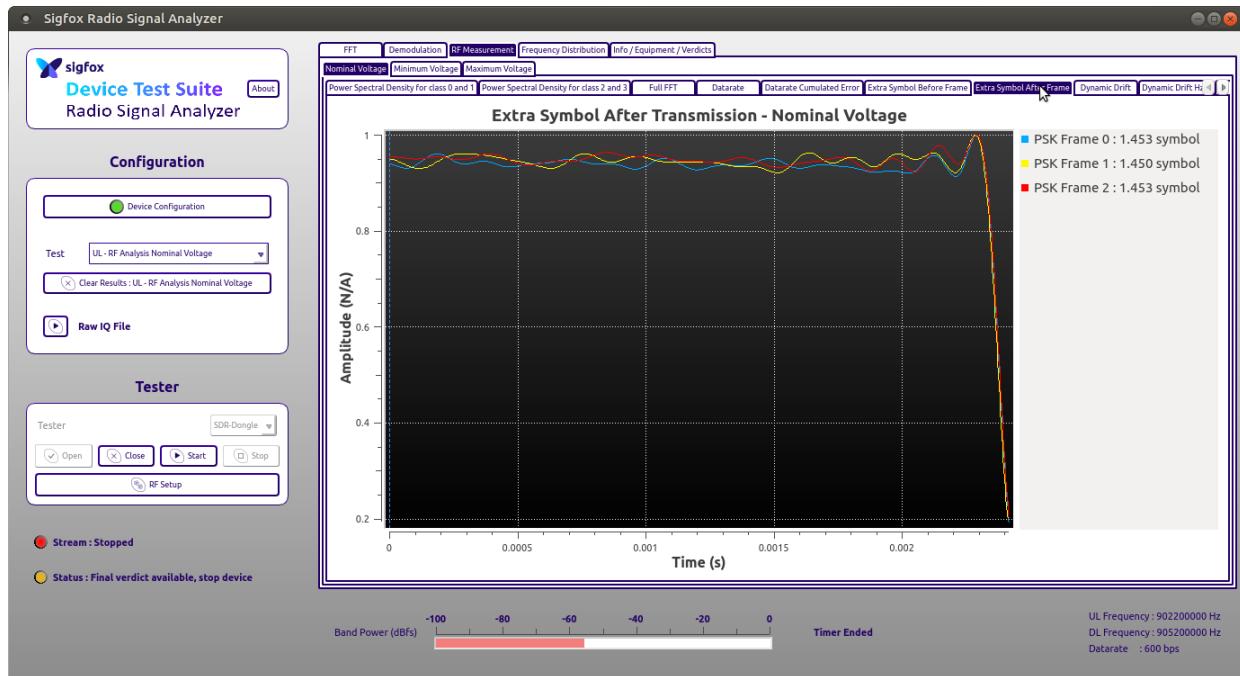


Figure 63: SX1261MB1BAS RC2 Extra Symbol After Transmission

6.5.10 Dynamic Drift

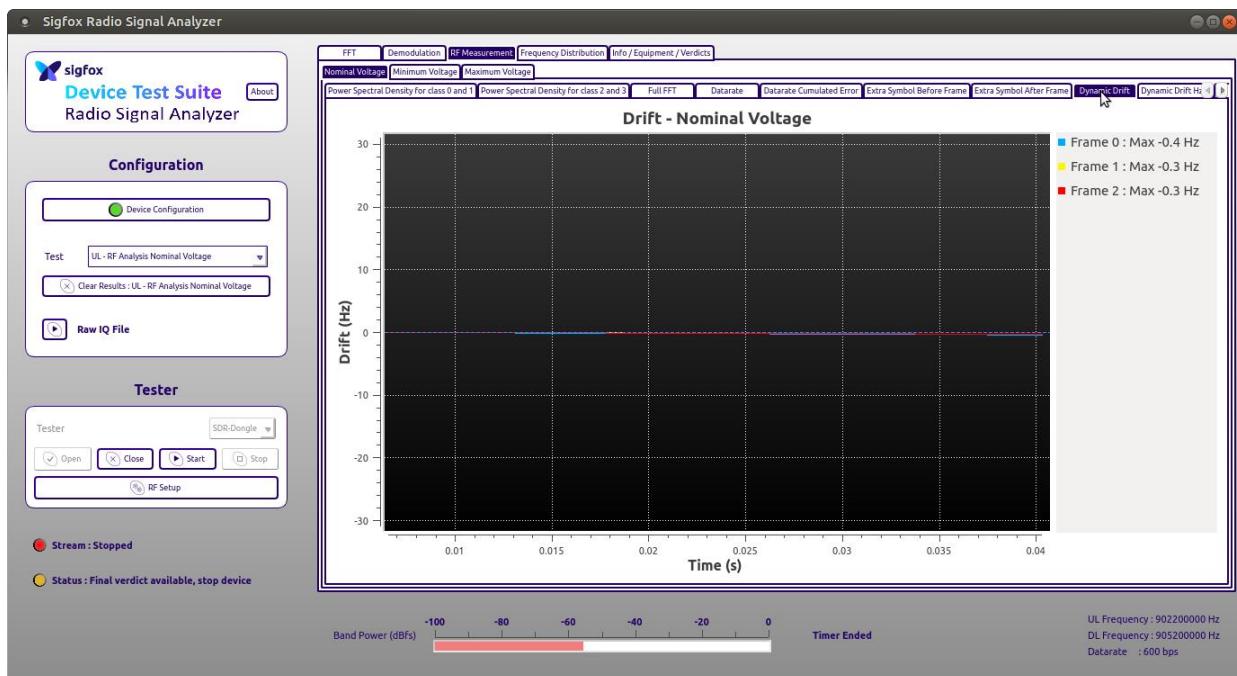


Figure 64: SX1261MB1BAS RC2 Dynamic Drift

6.5.11 Dynamic Drift per Second

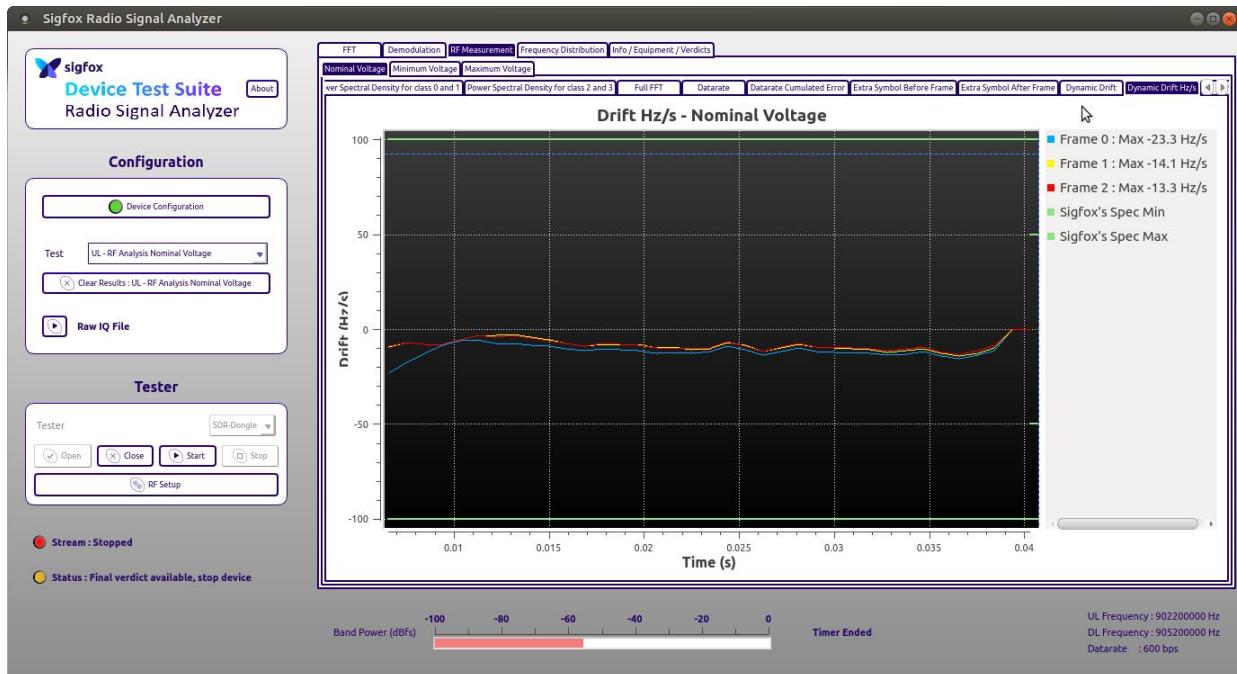


Figure 65: SX1261MB1BAS RC2 Dynamic Drift per Second

6.5.12 Phase

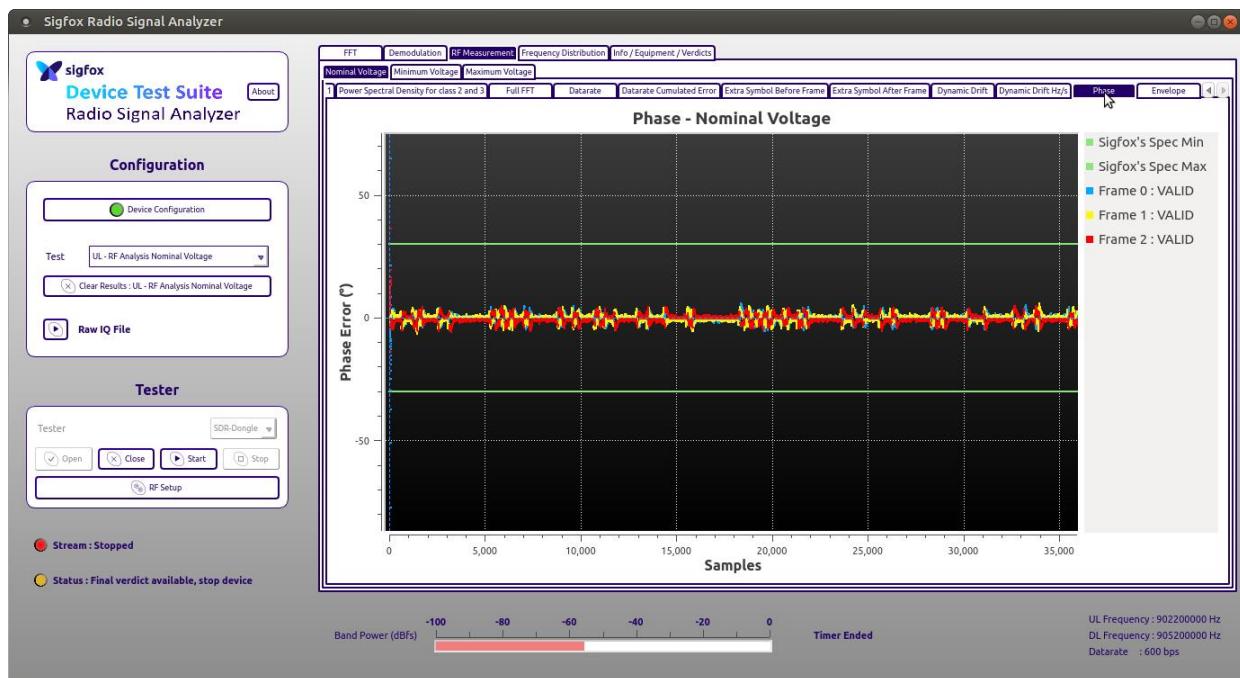


Figure 66: SX1261MB1BAS RC2 Phase

6.5.13 Envelope

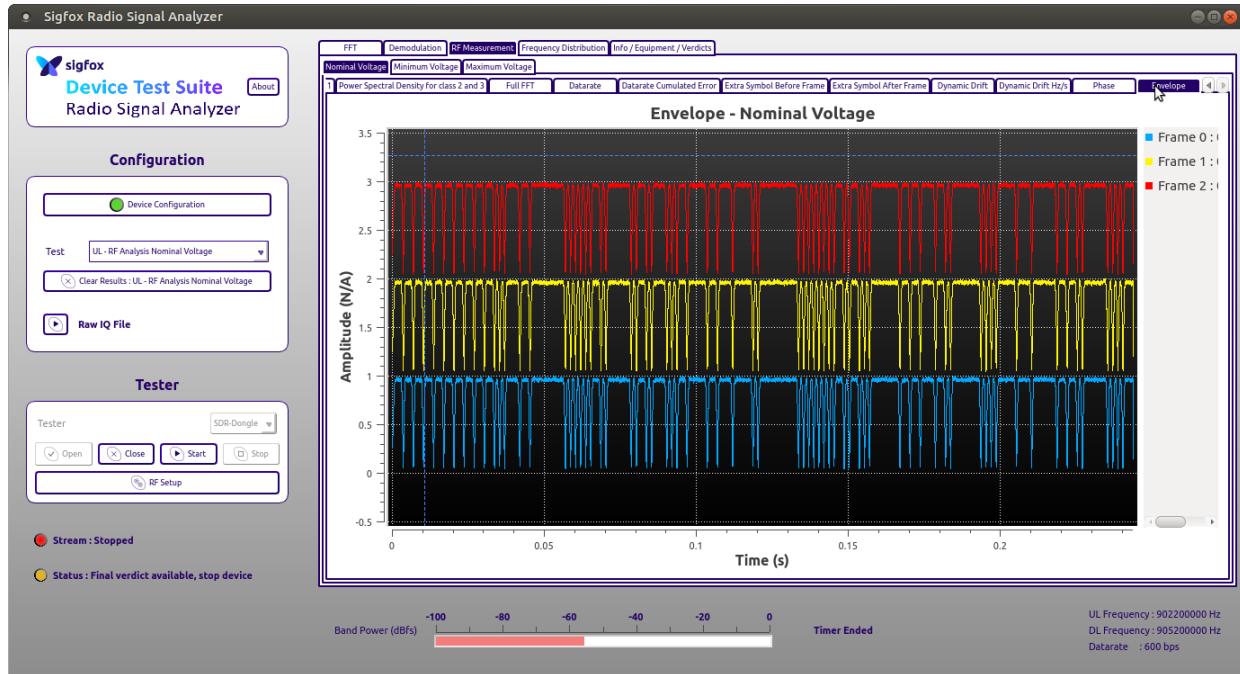


Figure 67: SX1261MB1BAS RC2 Envelope

6.5.14 Verdict

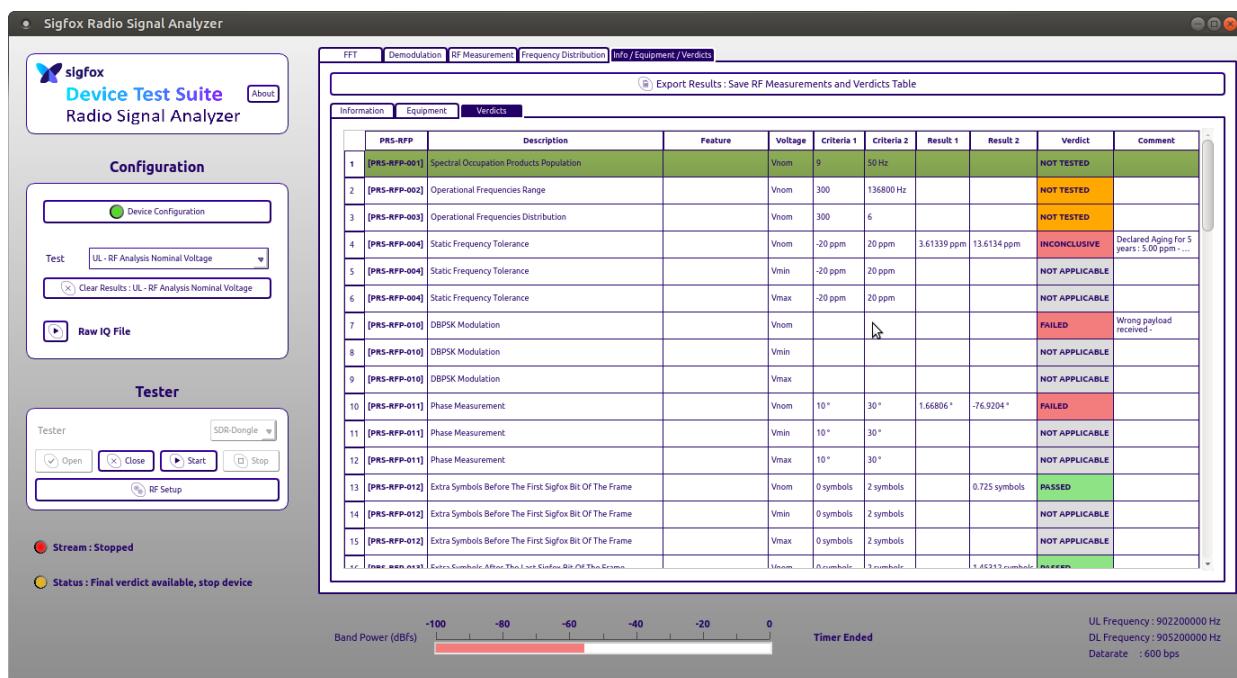


Figure 68: SX1261MB1BAS RC2 Verdict Page 1

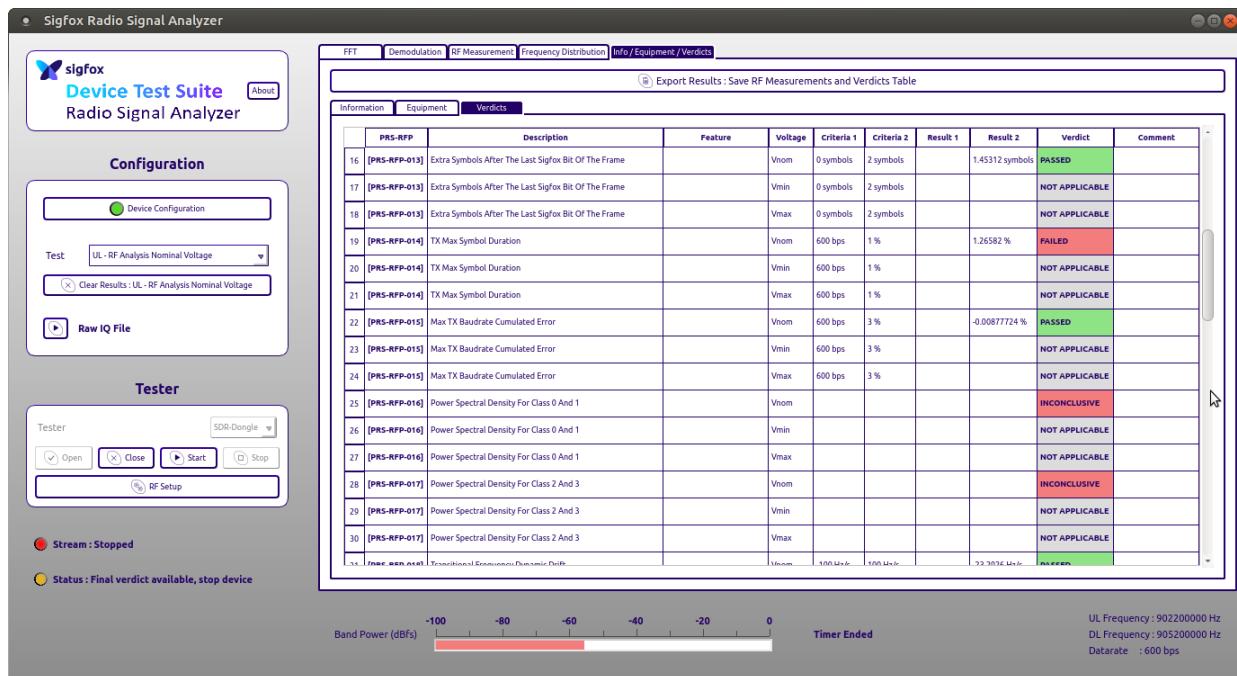


Figure 69: SX1261MB1BAS RC2 Verdict Page 2

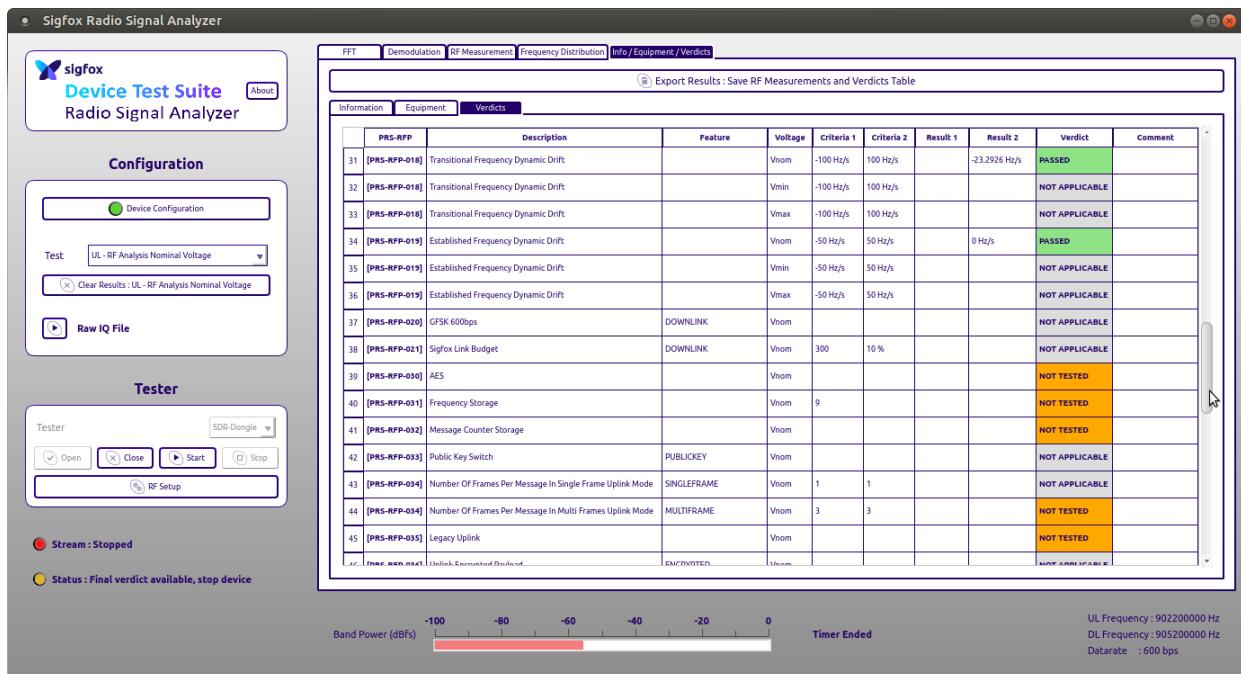


Figure 70: SX1261MB1BAS RC2 Verdict Page 3

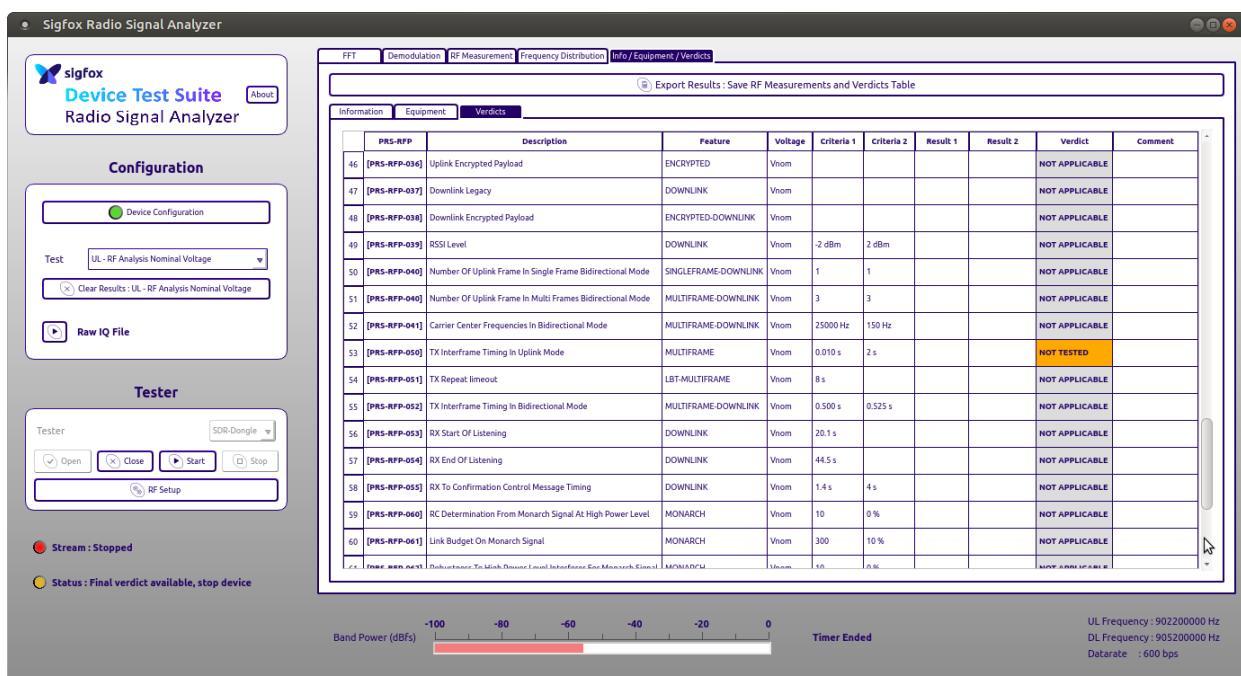


Figure 71: SX1261MB1BAS RC2 Verdict Page 4

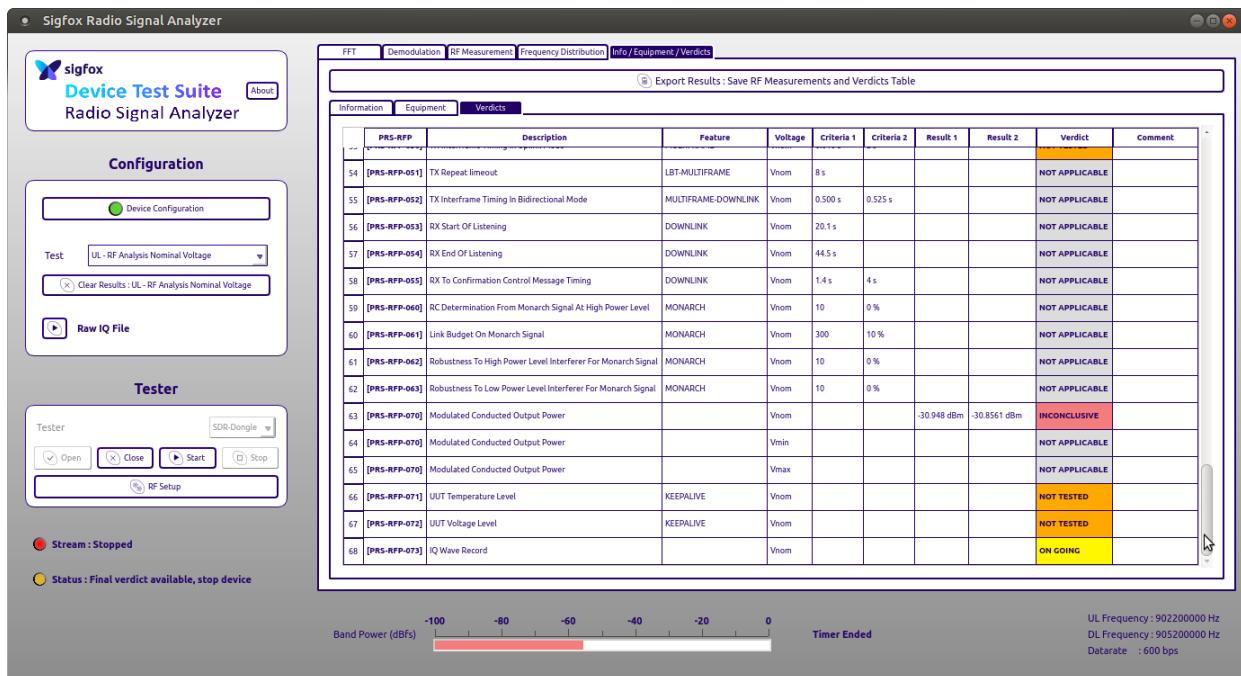


Figure 72: SX1261MB1BAS RC2 Verdict page 5

6.6 SX1262DVK1DAS Measurement Results, TCXO, Radio Configuration RC1

See Section 6.1 for general information about the Sigfox Radio Signal Analyzer tool test results.

6.6.1 Device Configuration

Device Configuration

Library Configuration

Features

Hardware Configuration

Supported Frame Types Configuration

(Note: The following table lists the supported frame types and their current status.)

Frame Type	Status
Frame Type No Payload	<input type="checkbox"/>
Frame Type Bit (False)	<input checked="" type="checkbox"/>
Frame Type Bit (True)	<input checked="" type="checkbox"/>
Keep Alive Frame Type	<input checked="" type="checkbox"/>
1 Byte Frame Type	<input checked="" type="checkbox"/>
2 Bytes Frame Type	<input checked="" type="checkbox"/>
3 Bytes Frame Type	<input checked="" type="checkbox"/>
4 Bytes Frame Type	<input checked="" type="checkbox"/>
5 Bytes Frame Type	<input checked="" type="checkbox"/>
6 Bytes Frame Type	<input checked="" type="checkbox"/>
7 Bytes Frame Type	<input checked="" type="checkbox"/>
8 Bytes Frame Type	<input checked="" type="checkbox"/>
9 Bytes Frame Type	<input checked="" type="checkbox"/>
10 Bytes Frame Type	<input checked="" type="checkbox"/>
11 Bytes Frame Type	<input checked="" type="checkbox"/>
12 Bytes Frame Type	<input checked="" type="checkbox"/>

Figure 73: SX1262DVK1DAS RC1 Device Configuration

6.6.2 FFT

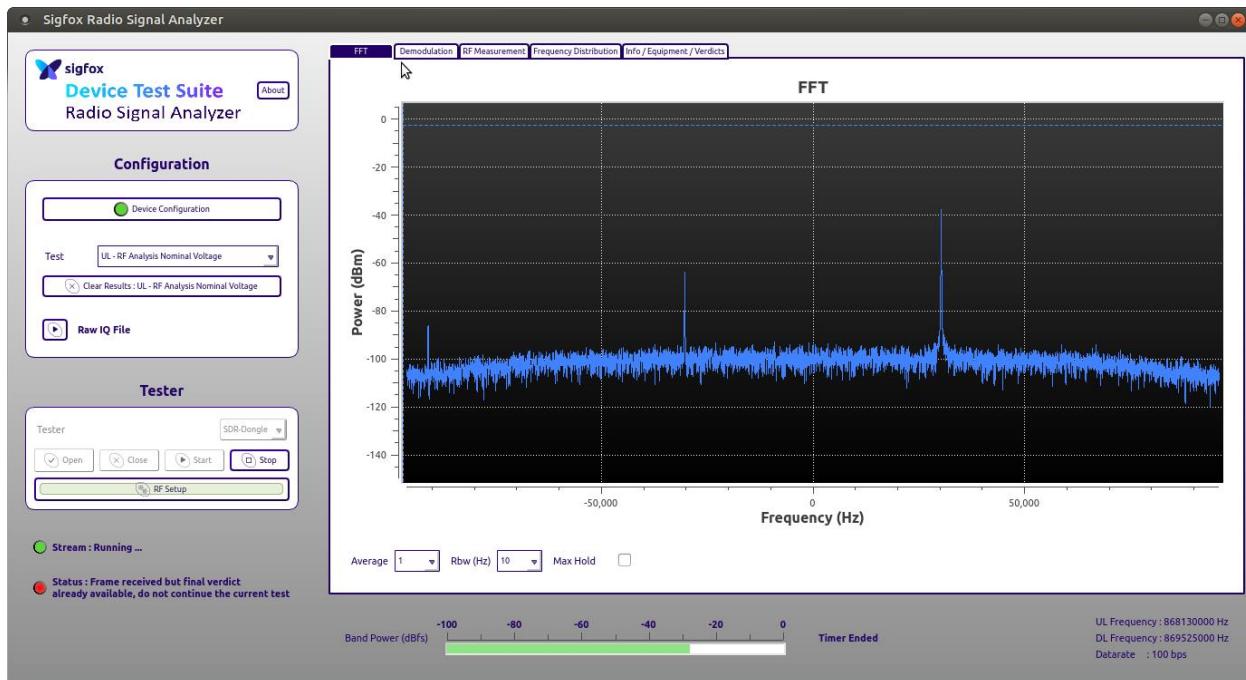


Figure 74: SX1262DVK1DAS RC1 FFT

6.6.3 Demodulation

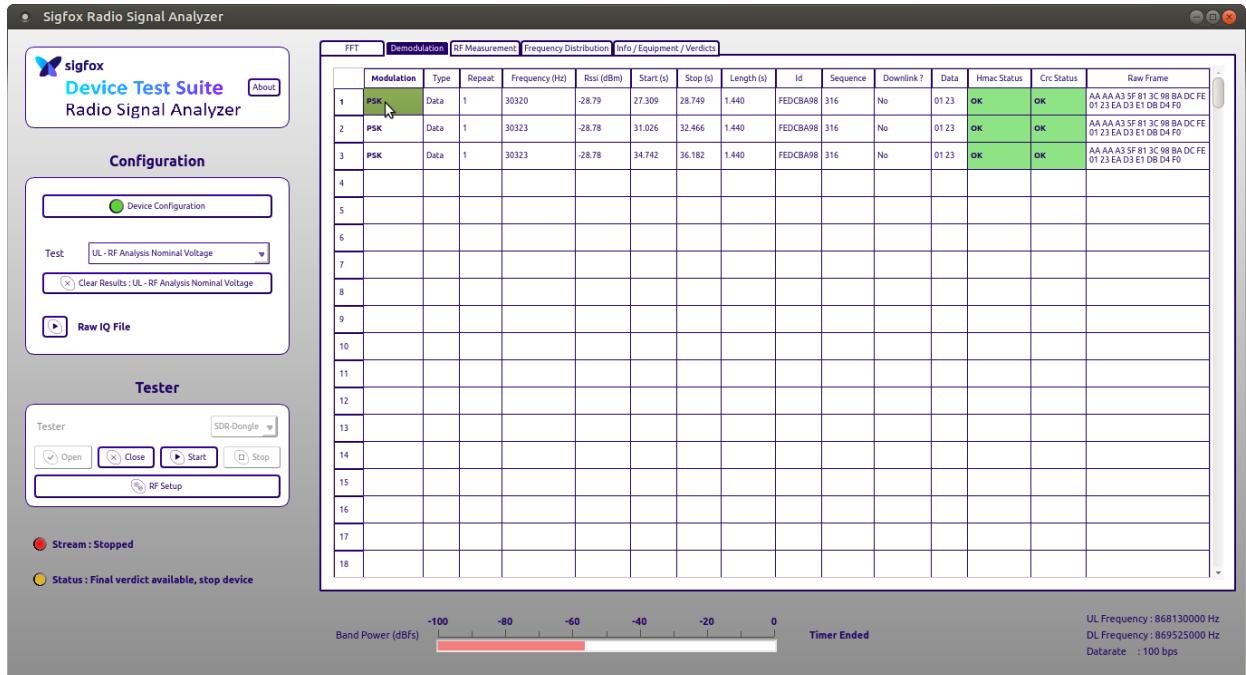


Figure 75: SX1262DVK1DAS RC1 Demodulation

6.6.4 Power Spectral Density for Class 0 & 1

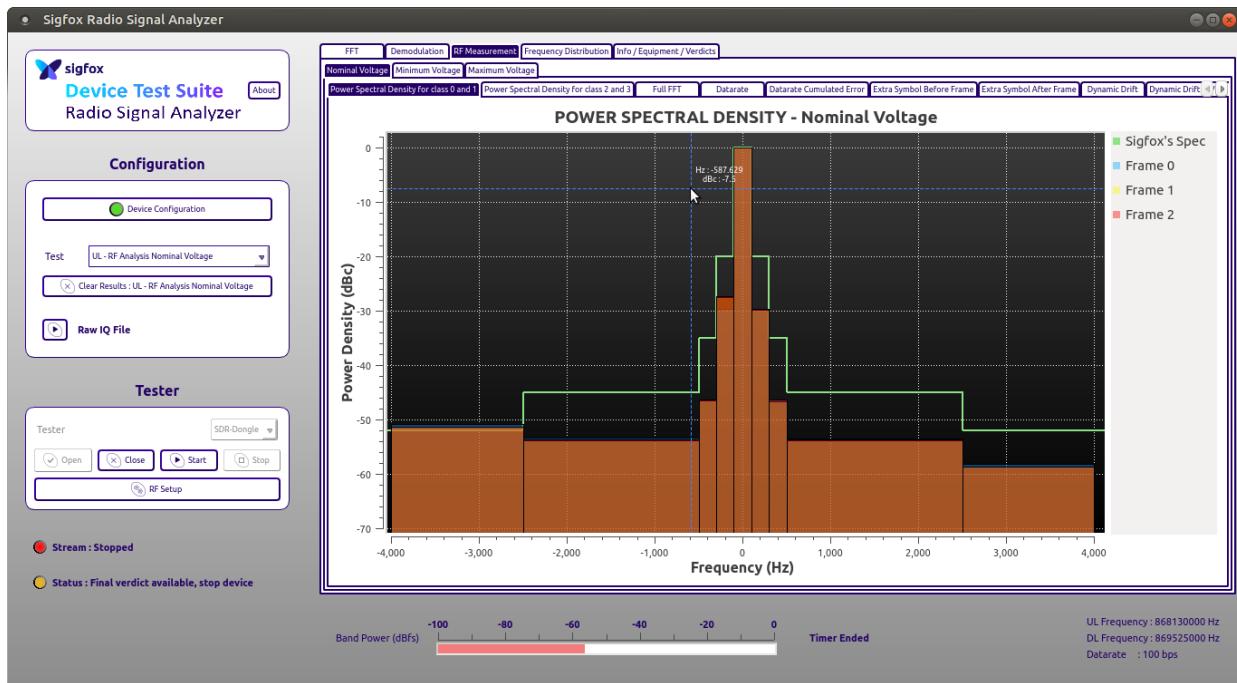


Figure 76: SX1262DVK1DAS RC1 Power Spectral Density for Class 0 & 1

6.6.5 Full FFT

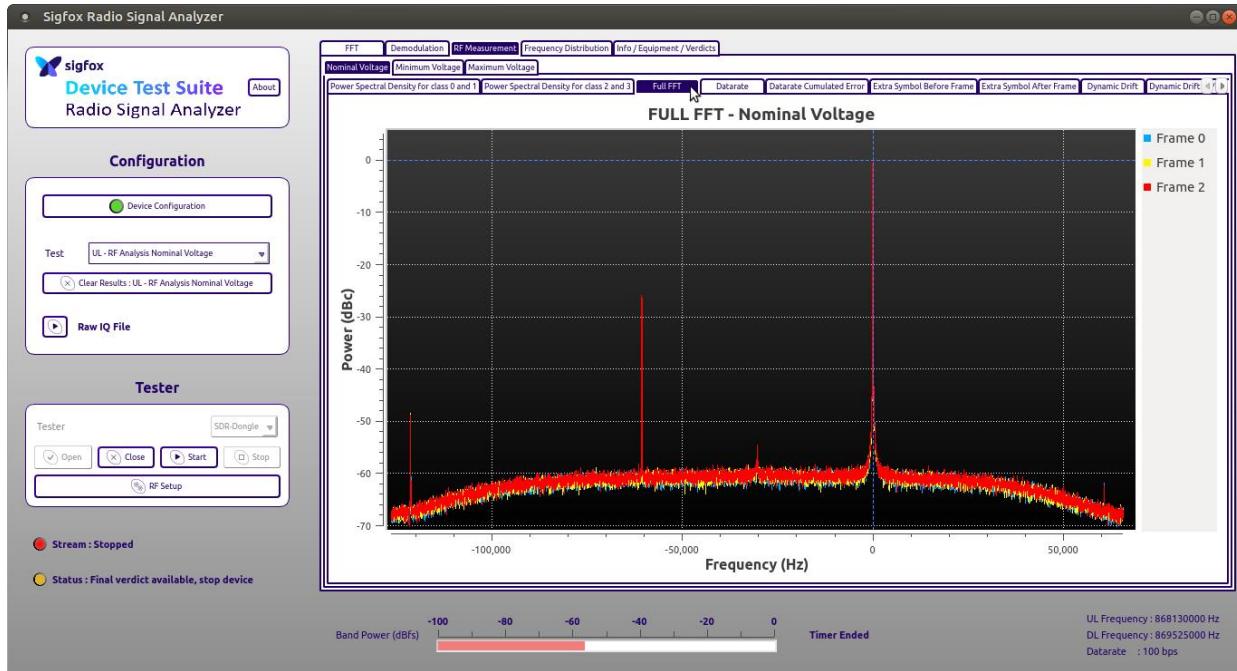


Figure 77: SX1262DVK1DAS RC1 Full FFT

6.6.6 Data Rate

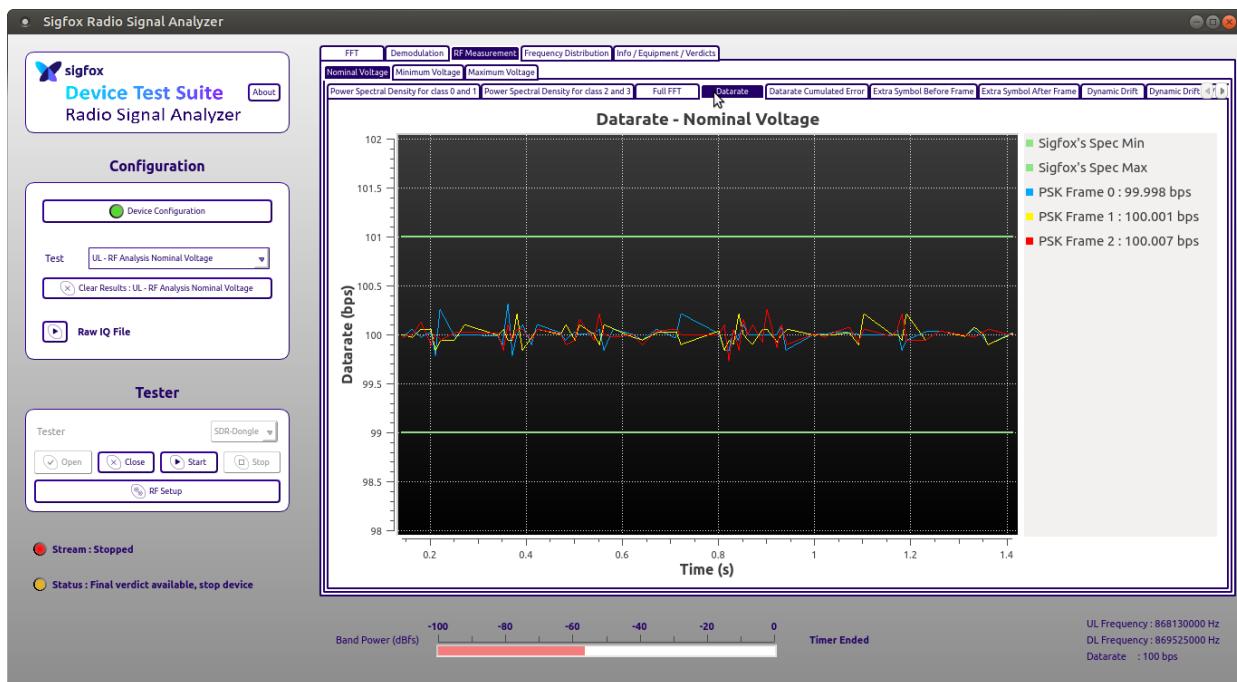


Figure 78: SX1262DVK1DAS RC1 Data Rate

6.6.7 Data Rate Cumulated Error

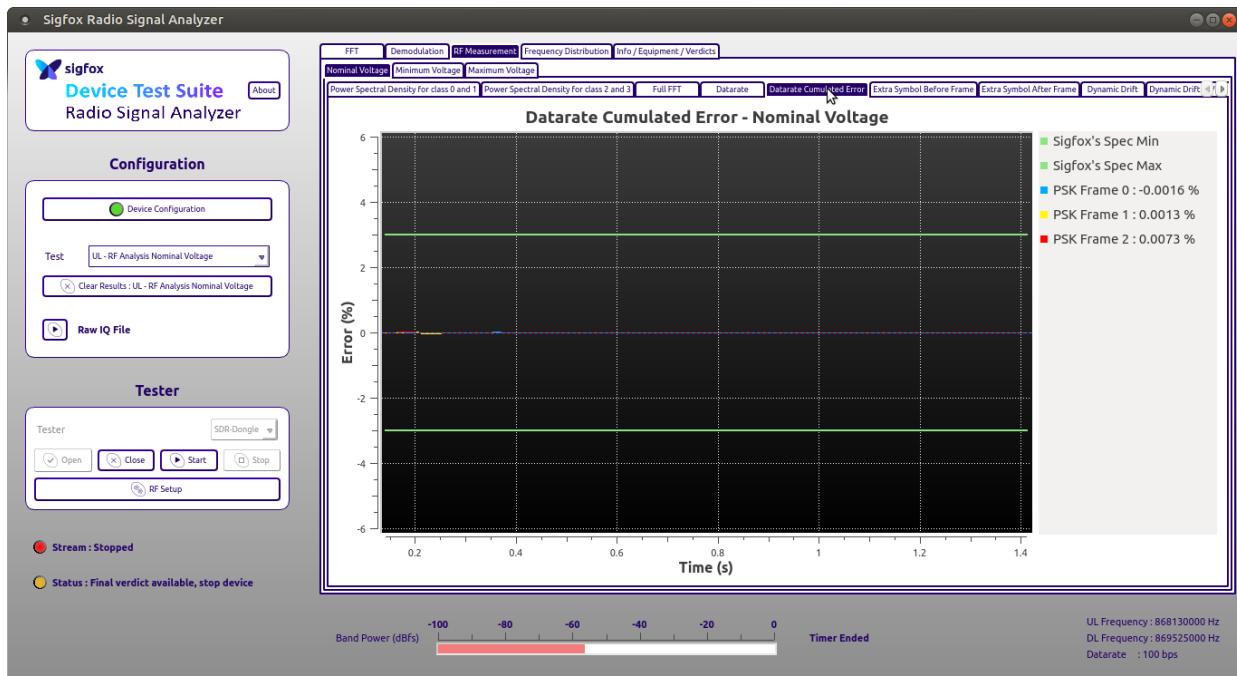


Figure 79: SX1262DVK1DAS RC1 Data Rate Cumulated Error

6.6.8 Extra Symbol Before Transmission

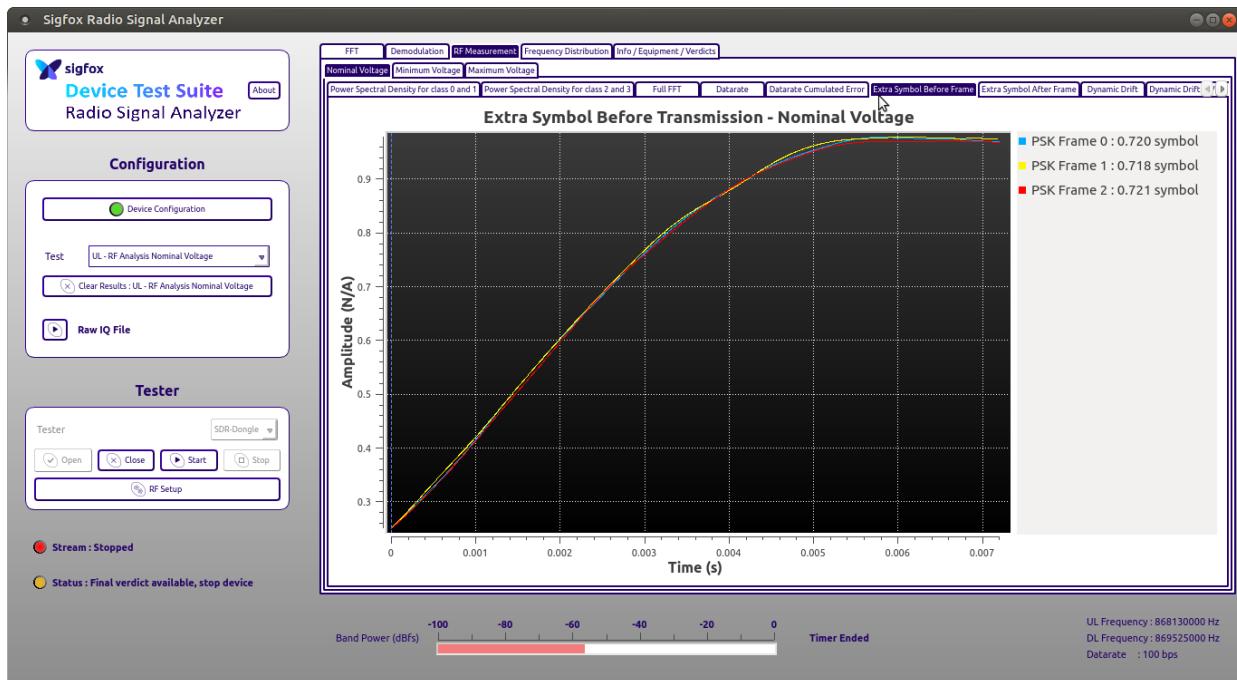


Figure 80: SX1262DVK1DAS RC1 Extra Symbol Before Transmission

6.6.9 Extra Symbol After Transmission

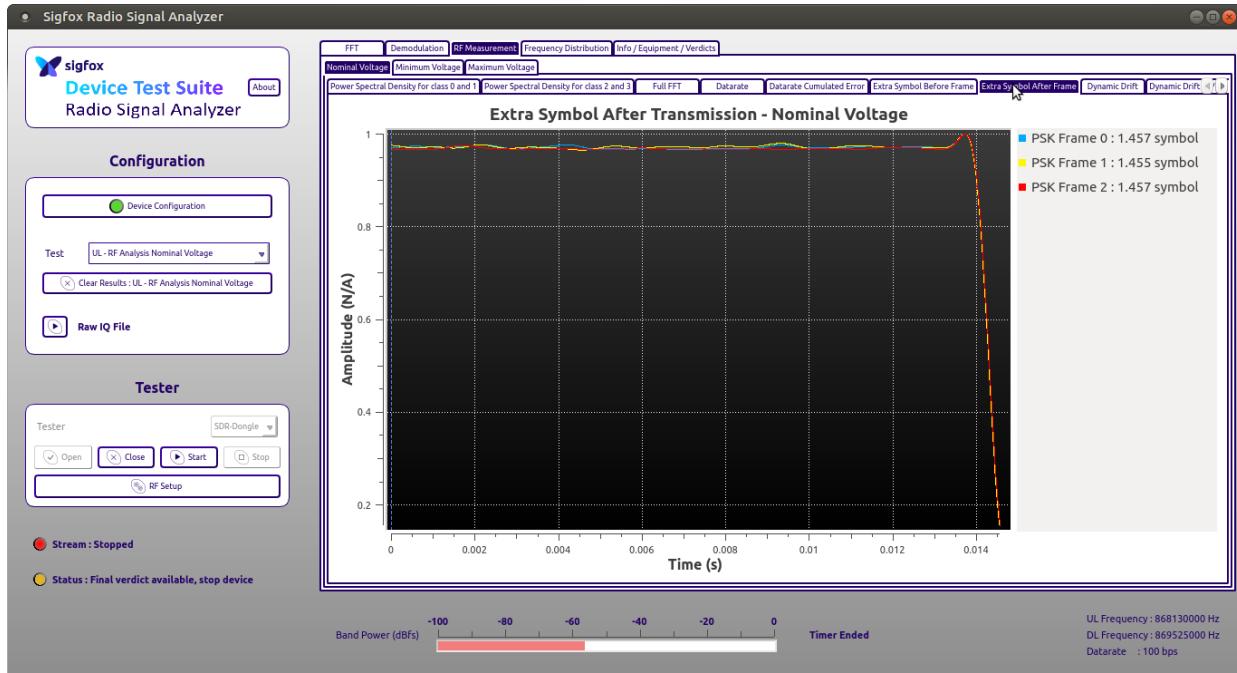


Figure 81: SX1262DVK1DAS RC1 Extra Symbol After Transmission

6.6.10 Dynamic Drift

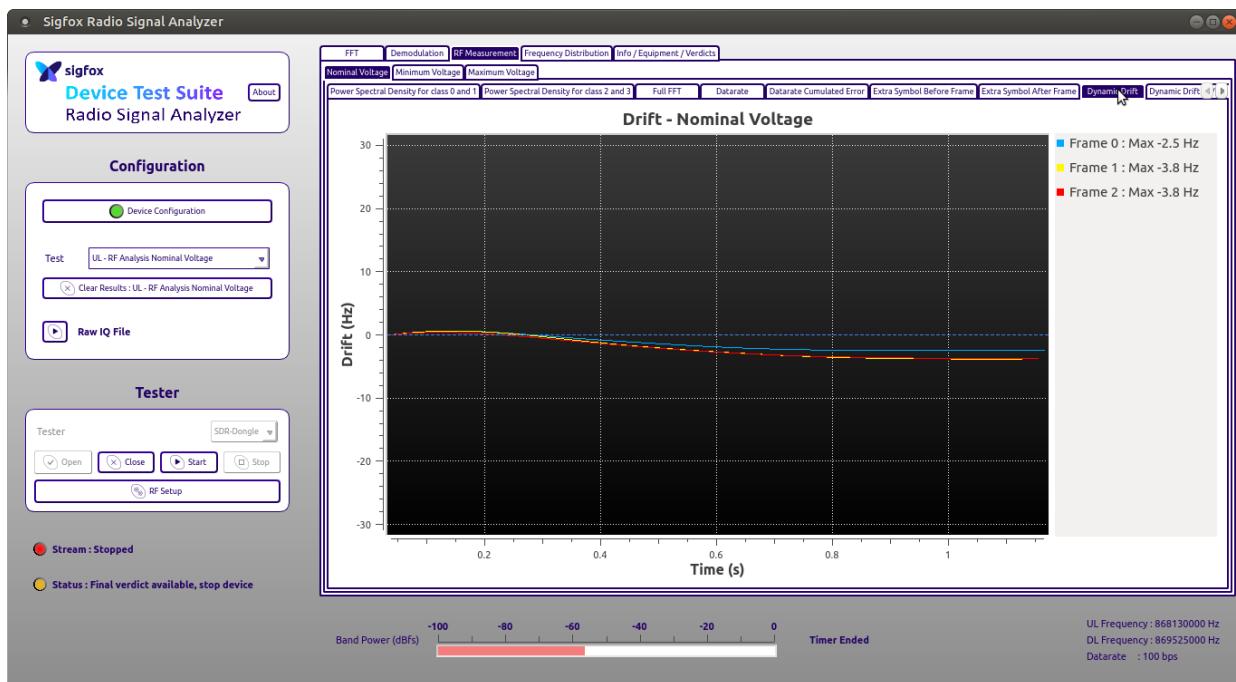


Figure 82: SX1262DVK1DAS RC1 Dynamic Drift

6.6.11 Dynamic Drift per Second

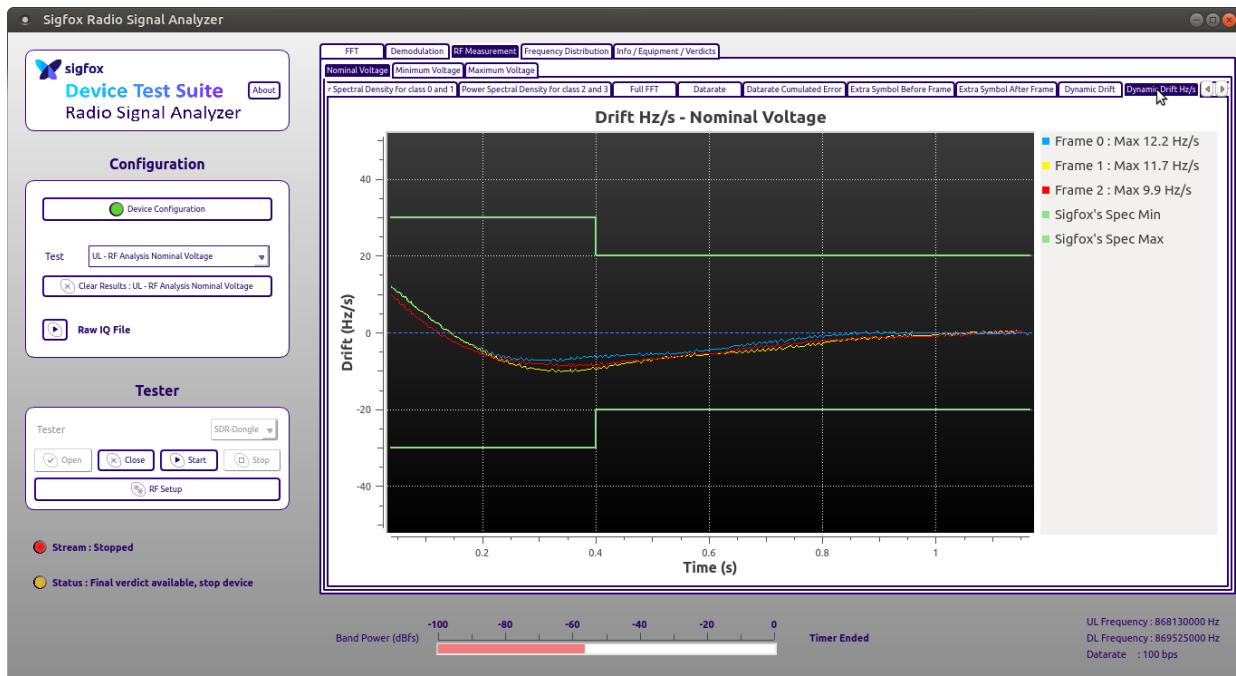


Figure 83: SX1262DVK1DAS RC1 Dynamic Drift per Second

6.6.12 Phase

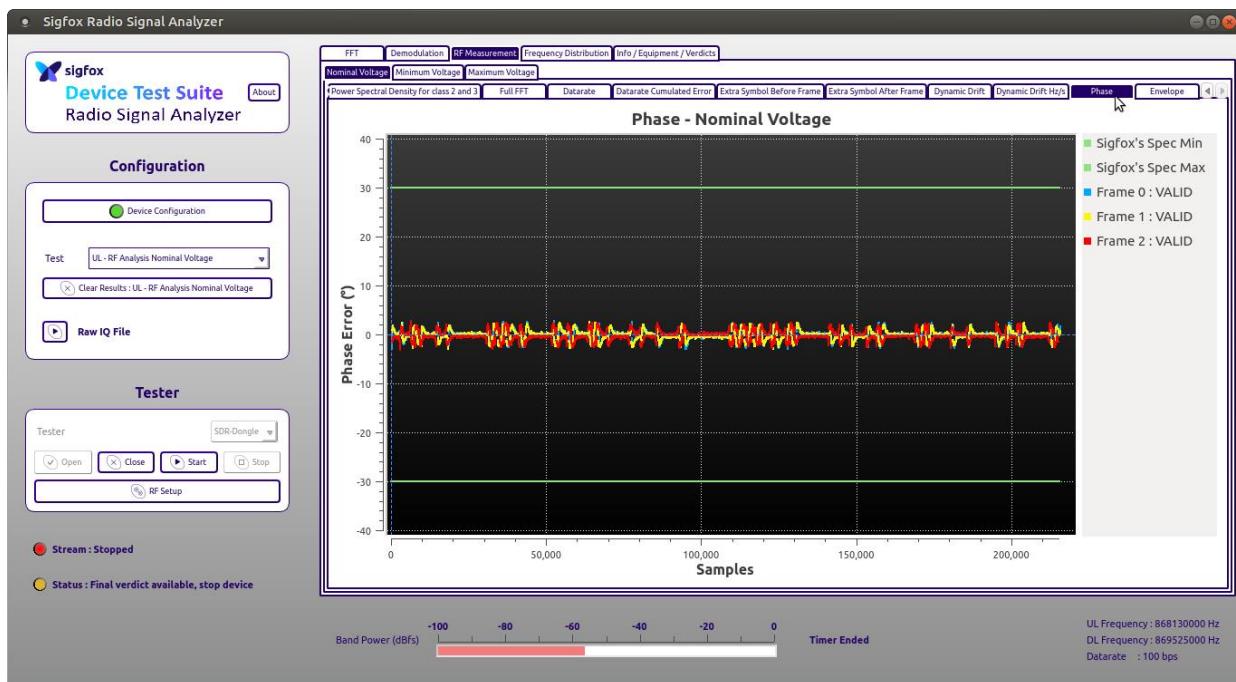


Figure 84: SX1262DVK1DAS RC1 Phase

6.6.13 Envelope

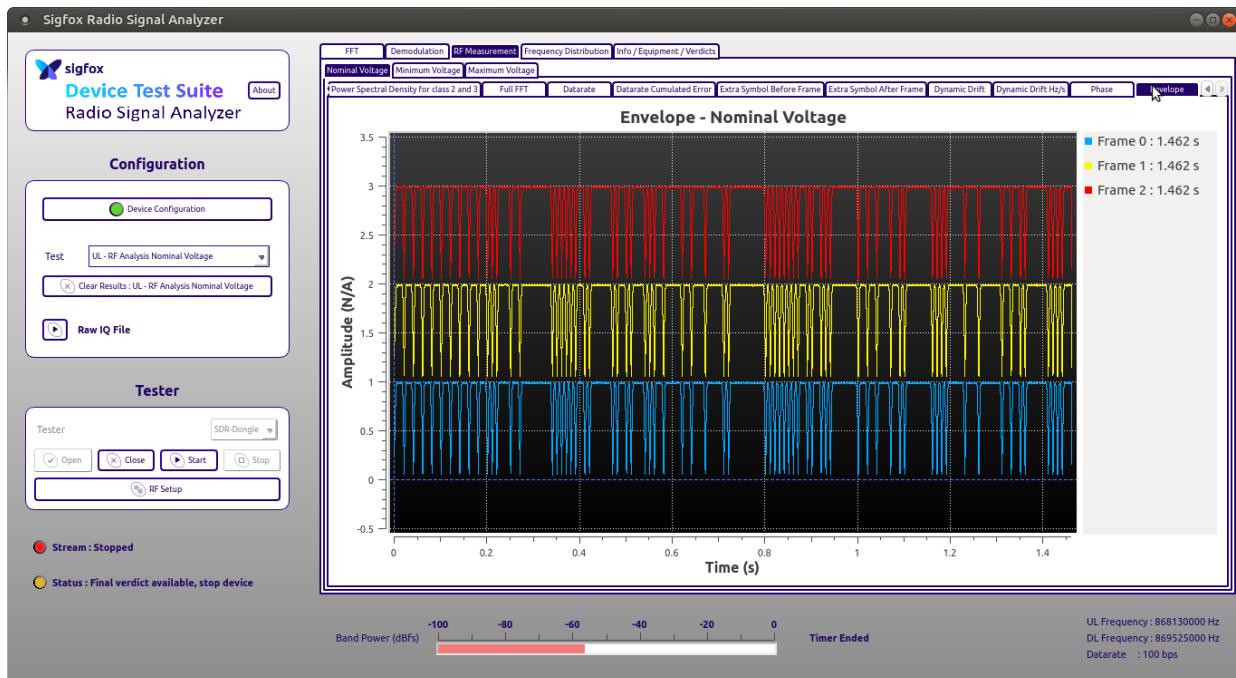


Figure 85: SX1262DVK1DAS RC1 Envelope

6.6.14 Verdict

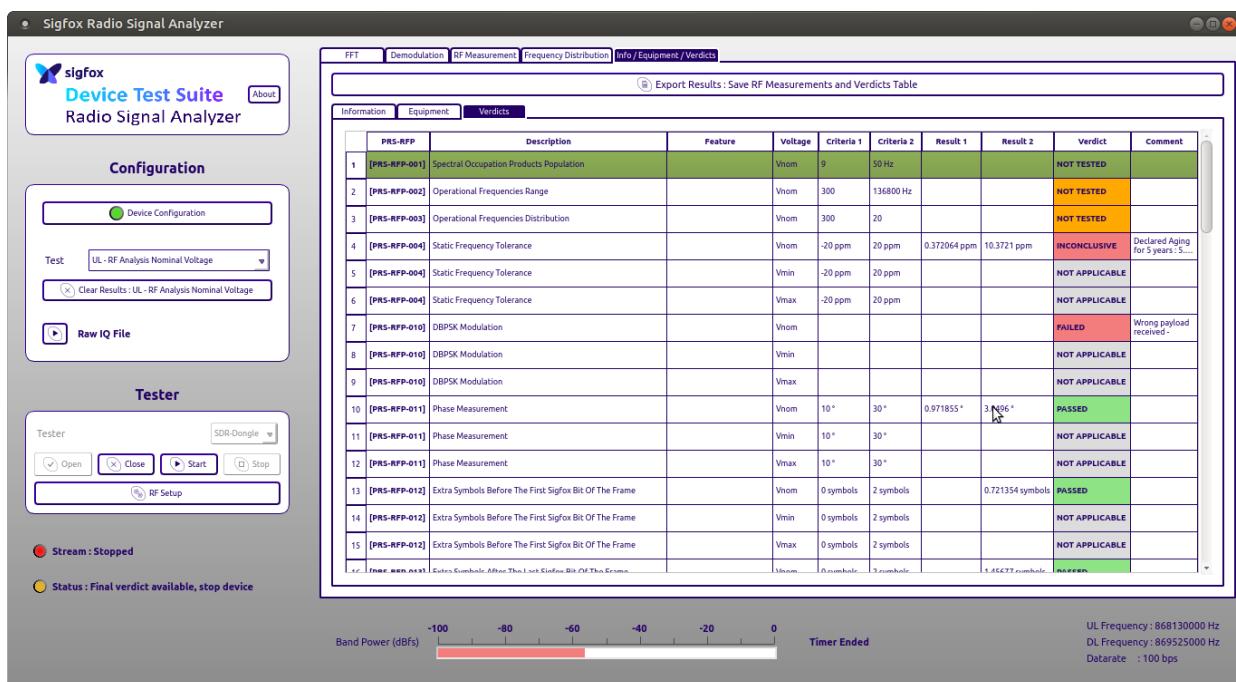


Figure 86: SX1262DVK1DAS RC1 Verdict Page 1

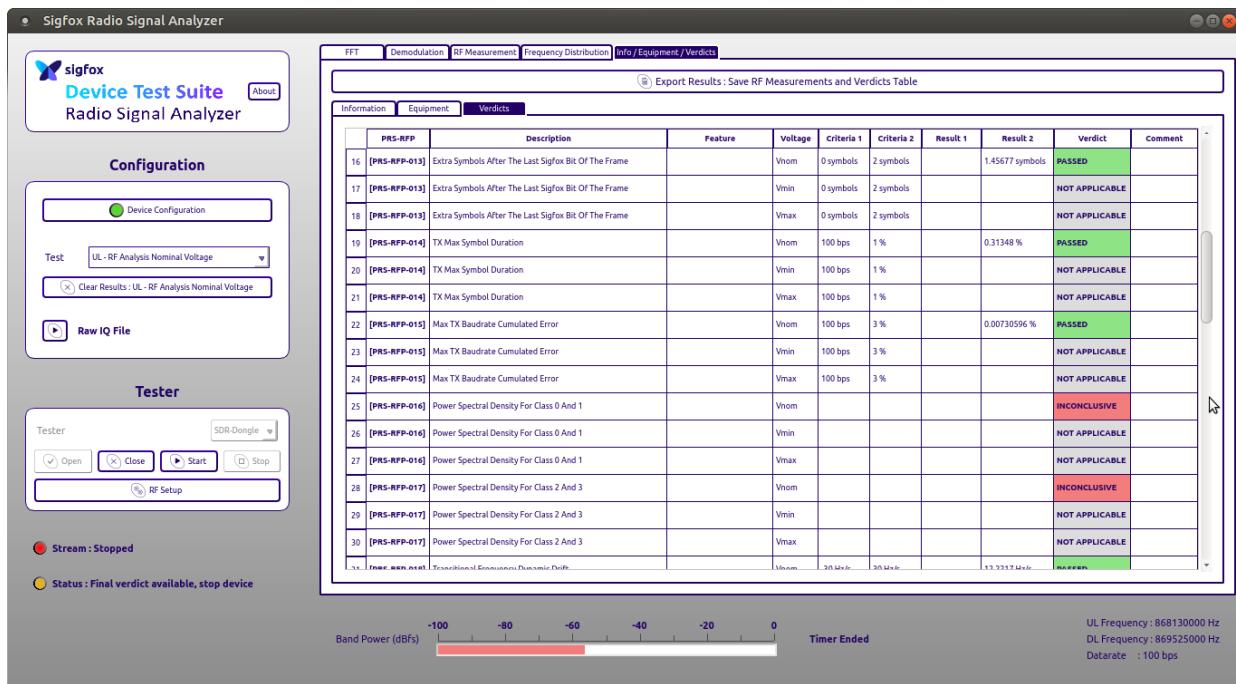


Figure 87: SX1262DVK1DAS RC1 Verdict Page 2

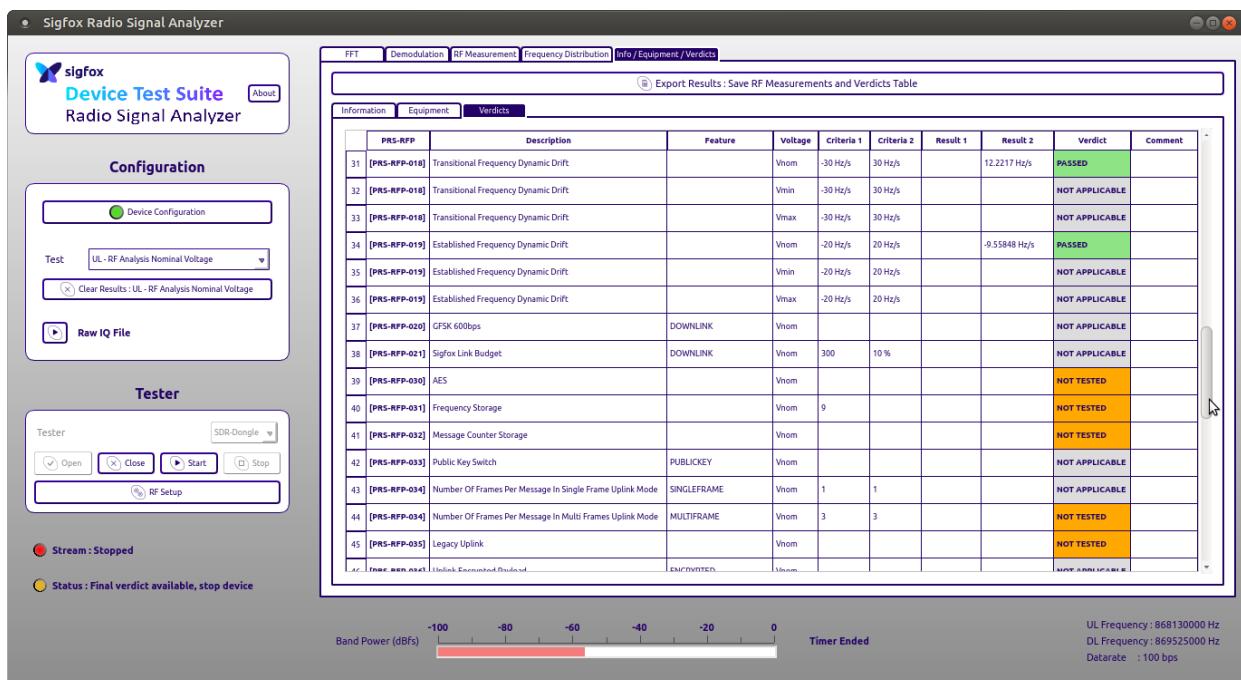


Figure 88: SX1262DVK1DAS RC1 Verdict Page 3

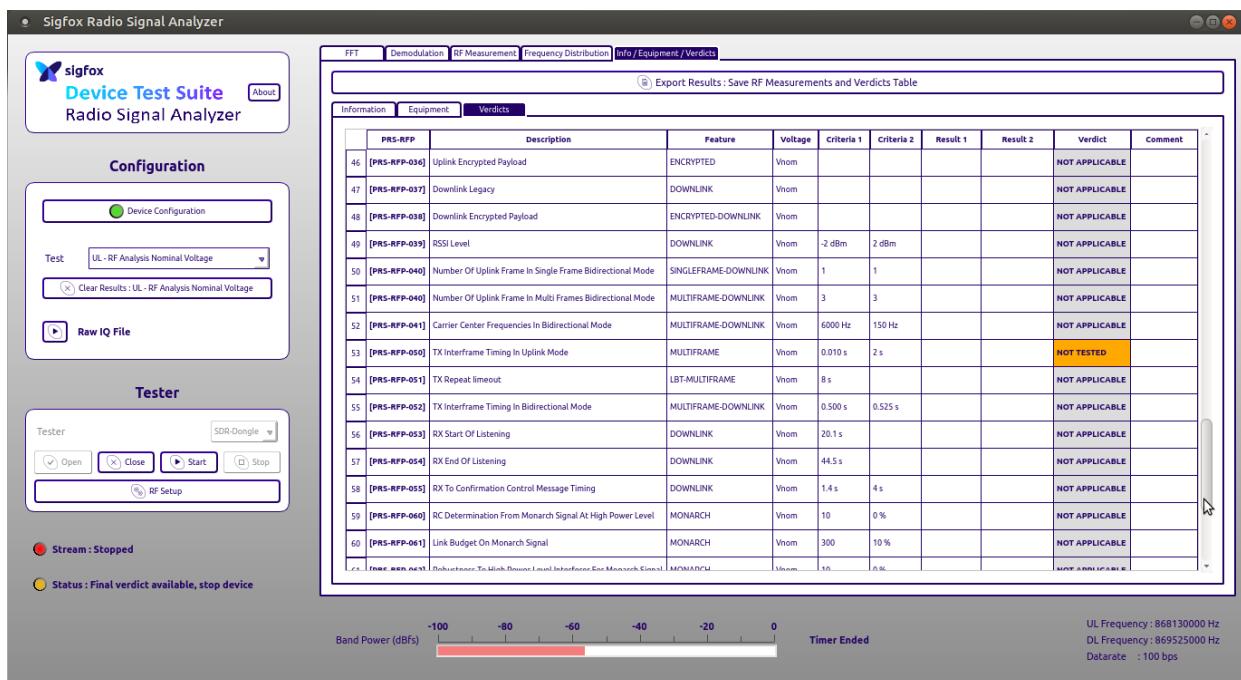


Figure 89: SX1262DVK1DAS RC1 Verdict Page 4

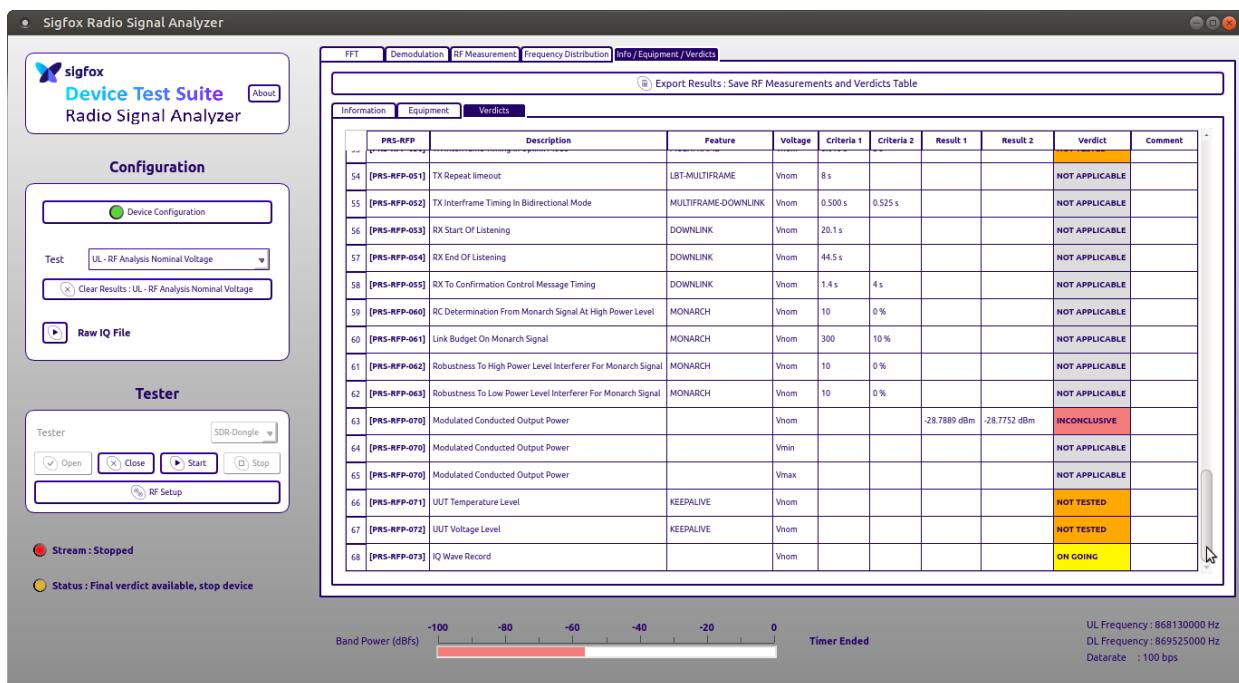


Figure 90: SX1262DVK1DAS RC1 Verdict page 5

6.7 SX1262DVK1DAS Measurement Results, TCXO, Radio Configuration RC2

See Section 6.1 for general information about the Sigfox Radio Signal Analyzer tool test results.

For the RC2 case, note that in Figures 6.7.10 and 6.7.11, the Sigfox Radio Signal Analyzer tool fails to properly measure drift.

6.7.1 Device Configuration

Device Configuration

Any change of static configuration will result in a RESET of Information / Verdicts tables

Library Configuration		Supported Frame Types Configuration	
Device ID : ID[0] = 0x98 ... ID[3] = 0xFE <input checked="" type="radio"/> FEDCBA98 Device Private Key : key[0] = 0x01 ... key[15] = 0xEF <input checked="" type="radio"/> 0123456789ABCDEF0123456789ABCDEF		Frame Type No Payload <input type="checkbox"/> Frame Type Bit (False) <input checked="" type="checkbox"/> Frame Type Bit (True) <input checked="" type="checkbox"/> Keep Alive Frame Type <input checked="" type="checkbox"/> 1 Byte Frame Type <input checked="" type="checkbox"/> 2 Bytes Frame Type <input checked="" type="checkbox"/> 3 Bytes Frame Type <input checked="" type="checkbox"/> 4 Bytes Frame Type <input checked="" type="checkbox"/> 5 Bytes Frame Type <input checked="" type="checkbox"/> 6 Bytes Frame Type <input checked="" type="checkbox"/> 7 Bytes Frame Type <input checked="" type="checkbox"/> 8 Bytes Frame Type <input checked="" type="checkbox"/> 9 Bytes Frame Type <input checked="" type="checkbox"/> 10 Bytes Frame Type <input checked="" type="checkbox"/> 11 Bytes Frame Type <input checked="" type="checkbox"/> 12 Bytes Frame Type <input checked="" type="checkbox"/>	
Features			
Radio Configuration : RC2 Message Counter Rollover : 4096 Downlink capable : <input type="checkbox"/> Monarch Capable : <input type="checkbox"/> Public Key switch capable : <input type="checkbox"/> Payload Encryption Configuration : No Payload Encryption Payload Encryption Rollover Counter value : 0 Multi Frame Capable : <input checked="" type="checkbox"/> All Message Types Supported : <input checked="" type="checkbox"/>			
Hardware Configuration			
Oscillator Aging for 5 years (ppm) : 5.0 Oscillator Temperature Accuracy (ppm) (Taking into account the operating temperature range) : 5.0 Product's Population Frequency Accuracy (ppm) : 5.0 Minimum Voltage = Nominal Voltage : <input checked="" type="checkbox"/> Maximum Voltage = Nominal Voltage : <input checked="" type="checkbox"/>			
<input type="button" value="Apply Settings"/> <input type="button" value="Cancel"/>			

Figure 91: SX1262DVK1DAS RC2 Device Configuration

6.7.2 FFT

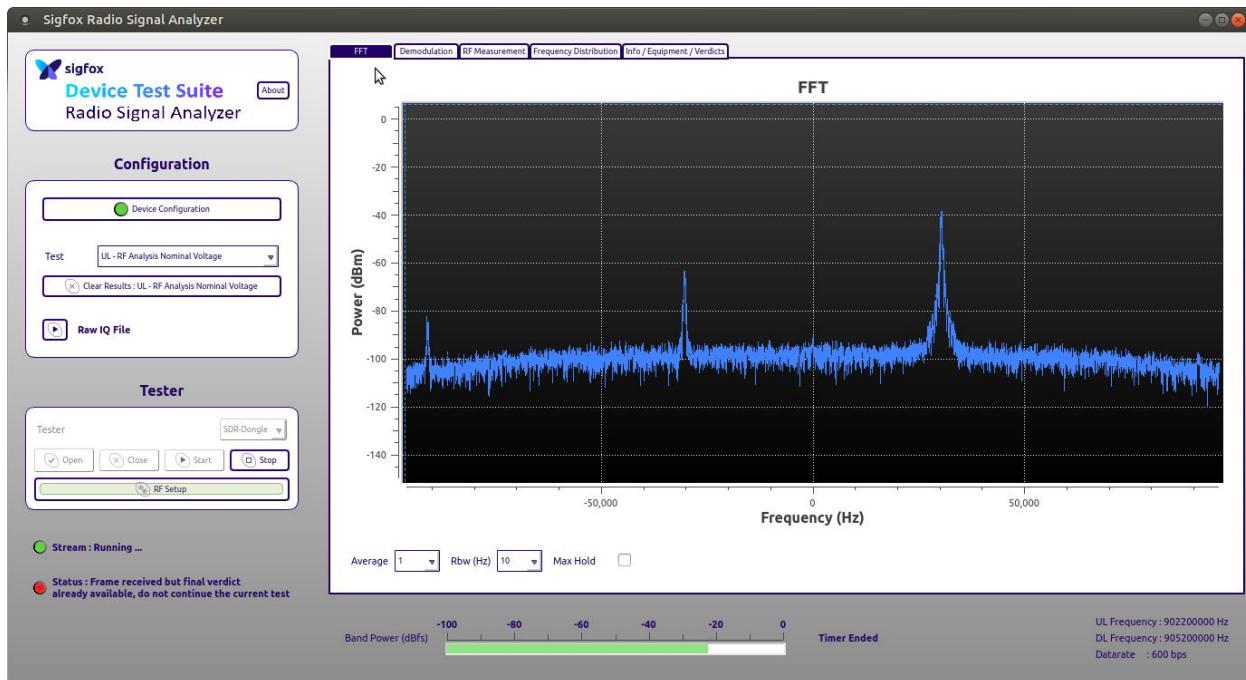


Figure 92: SX1262DVK1DAS RC2 FFT

6.7.3 Demodulation



Figure 93: SX1262DVK1DAS RC2 Demodulation

6.7.4 Power Spectral Density for Class 0 & 1

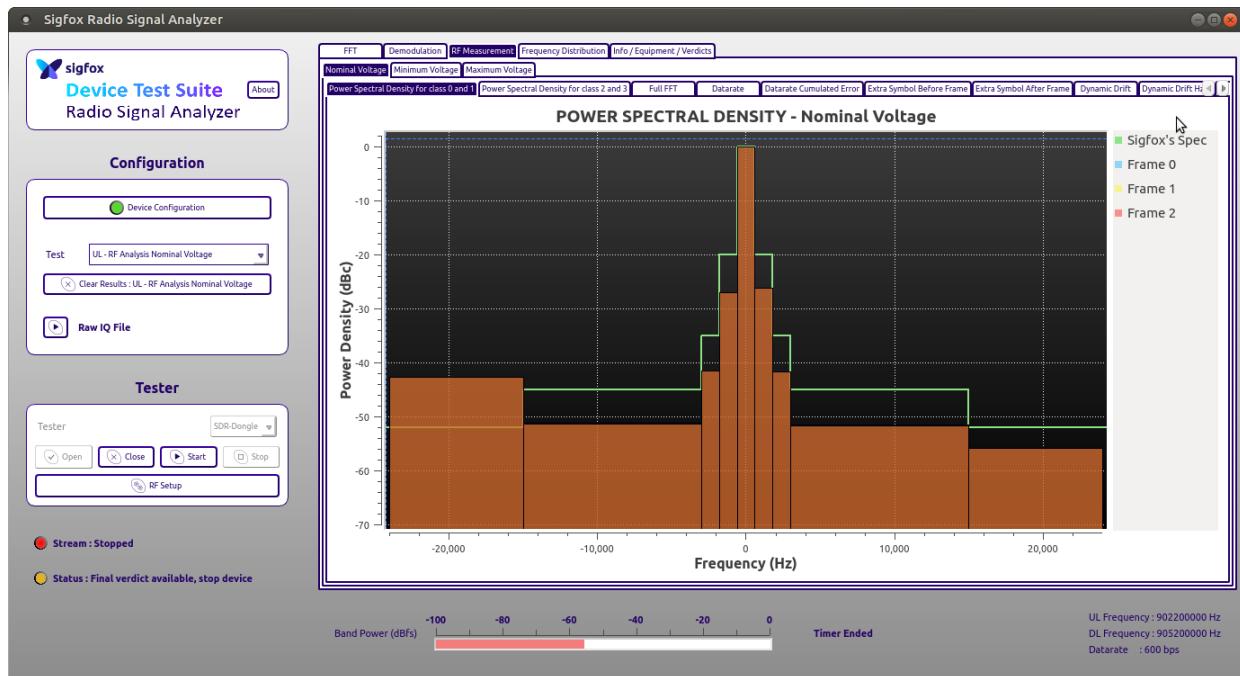


Figure 94: SX1262DVK1DAS RC2 Power Spectral Density for Class 0 & 1

6.7.5 Full FFT

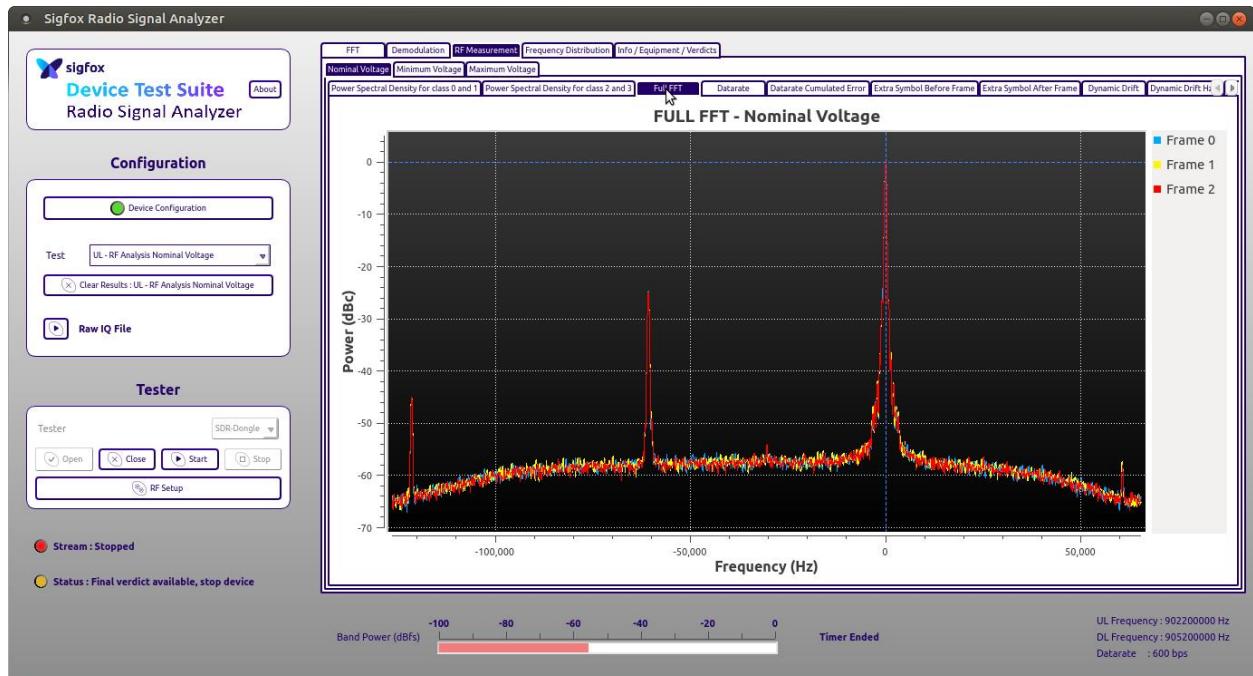


Figure 95: SX1262DVK1DAS RC2 Full FFT

6.7.6 Data Rate

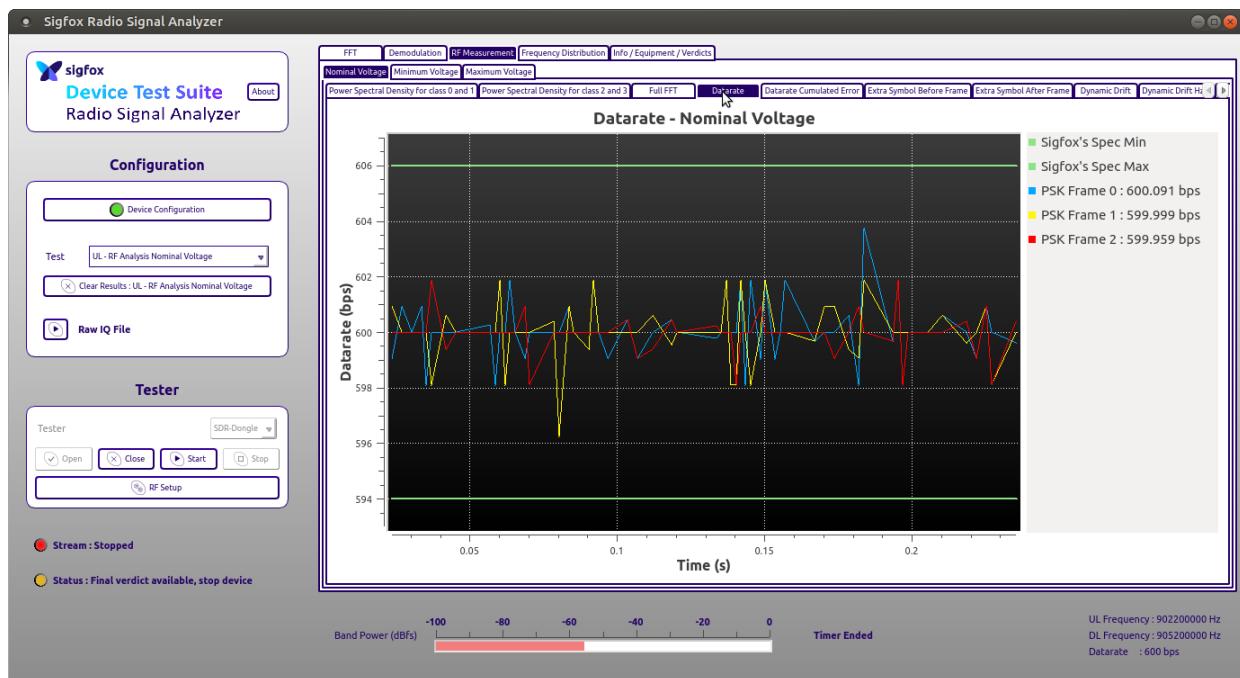


Figure 96: SX1262DVK1DAS RC2 Data Rate

6.7.7 Data Rate Cumulated Error

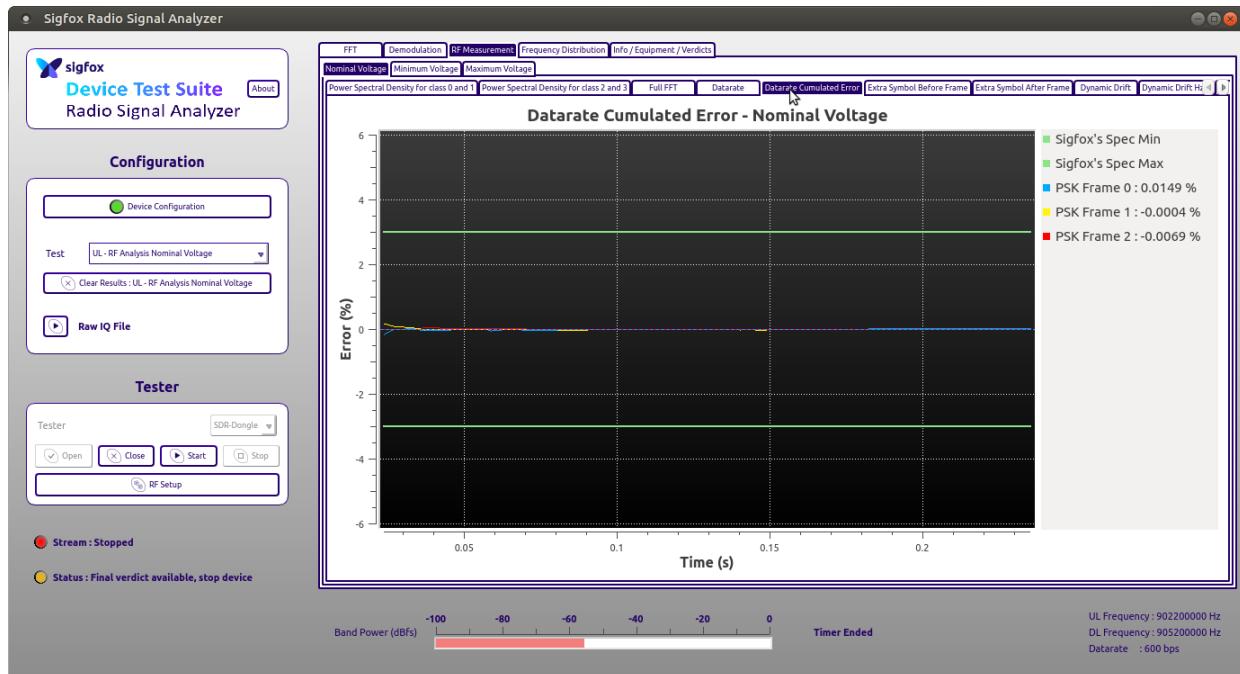


Figure 97: SX1262DVK1DAS RC2 Data Rate Cumulated Error

6.7.8 Extra Symbol Before Transmission

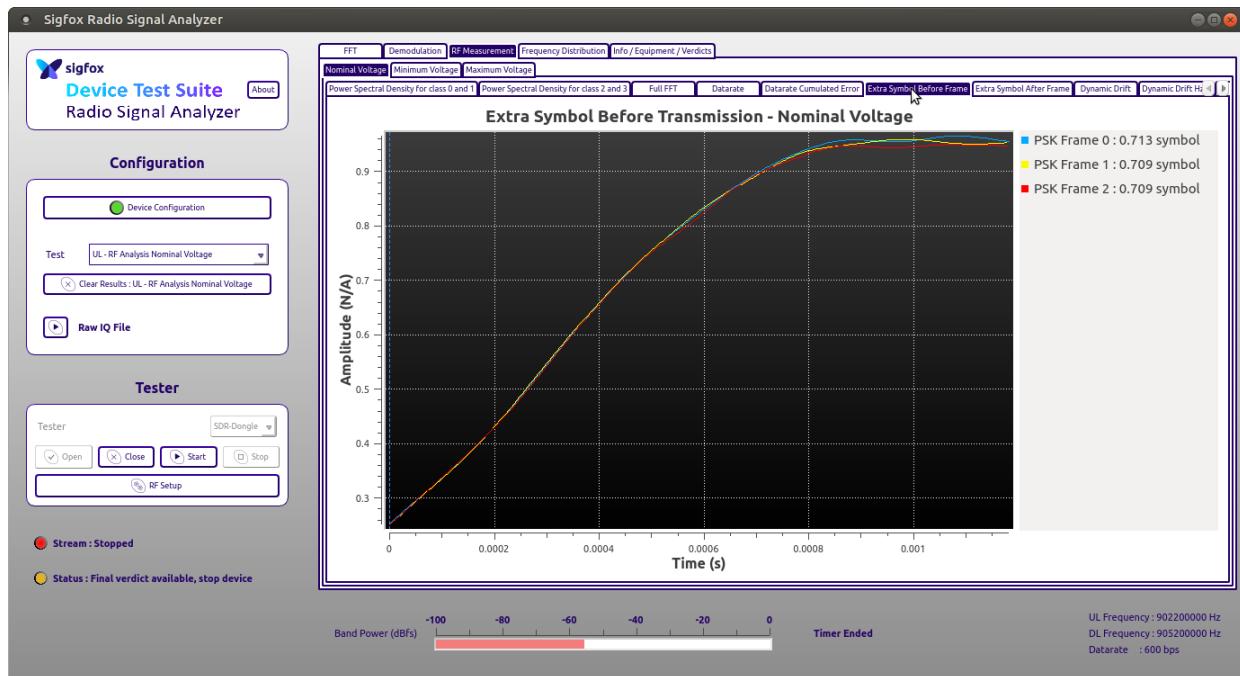


Figure 98: SX1262DVK1DAS RC2 Extra Symbol Before Transmission

6.7.9 Extra Symbol After Transmission

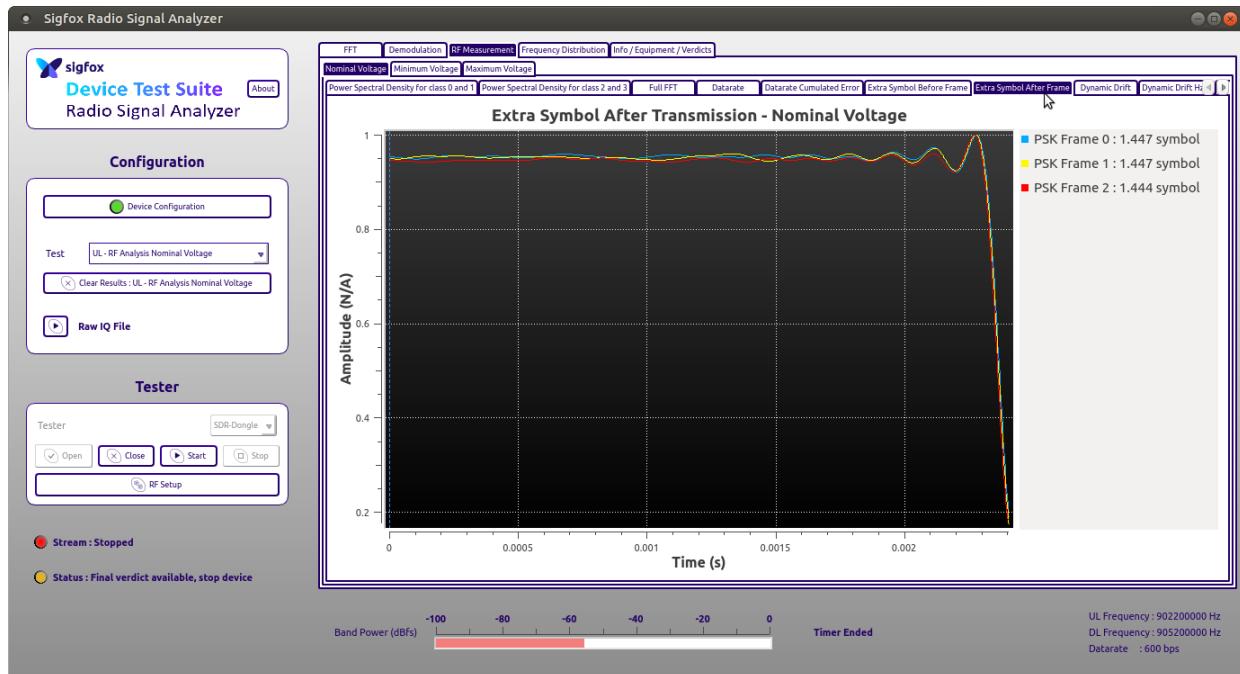


Figure 99: SX1262DVK1DAS RC2 Extra Symbol After Transmission

6.7.10 Dynamic Drift

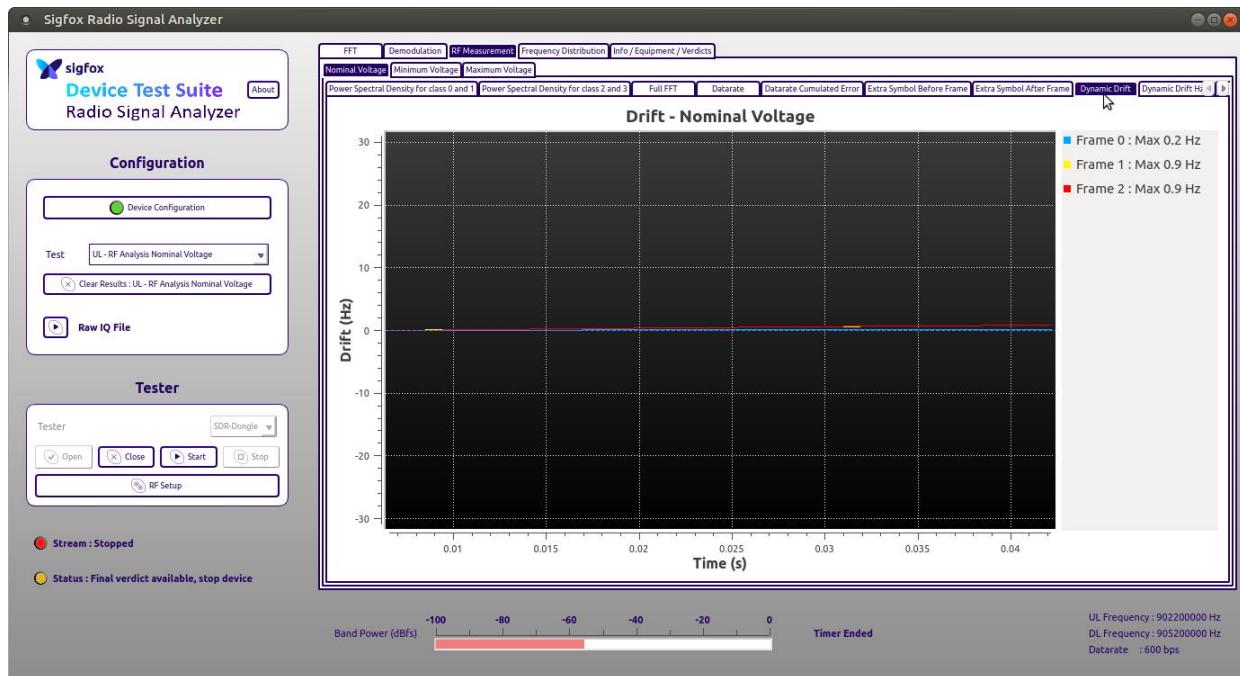


Figure 100: SX1262DVK1DAS RC2 Dynamic Drift

6.7.11 Dynamic Drift per Second

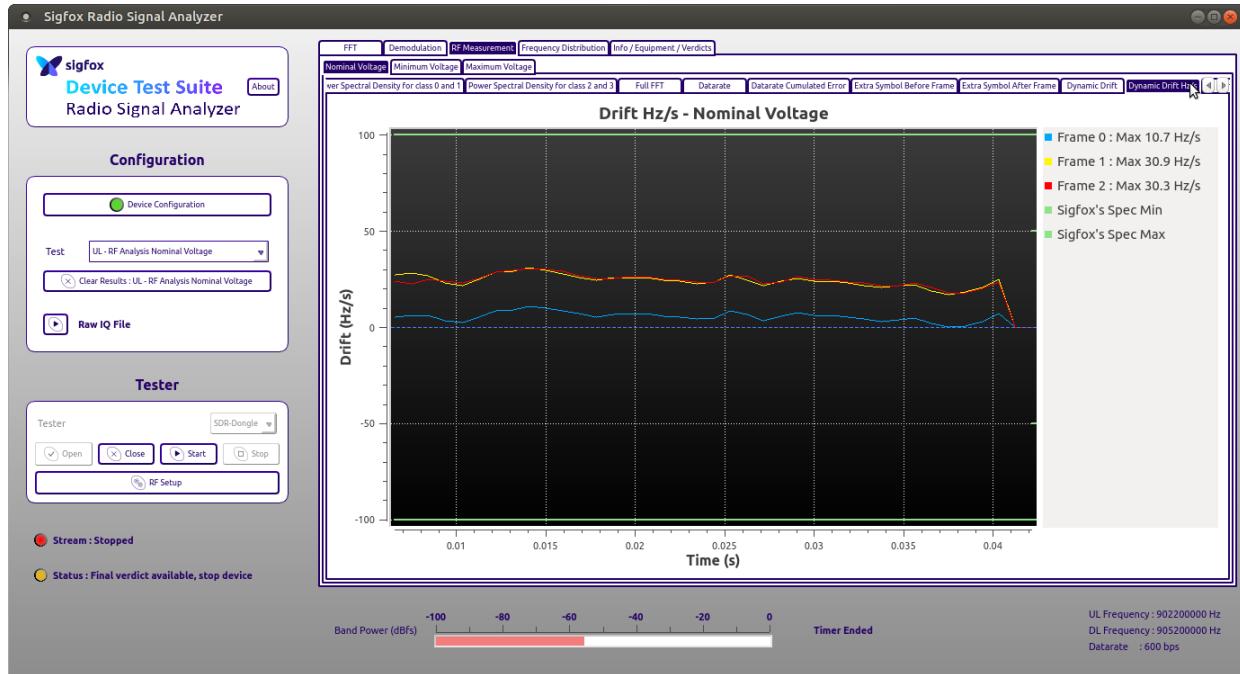


Figure 101: SX1262DVK1DAS RC2 Dynamic Drift per Second

6.7.12 Phase



Figure 102: SX1262DVK1DAS RC2 Phase

6.7.13 Envelope

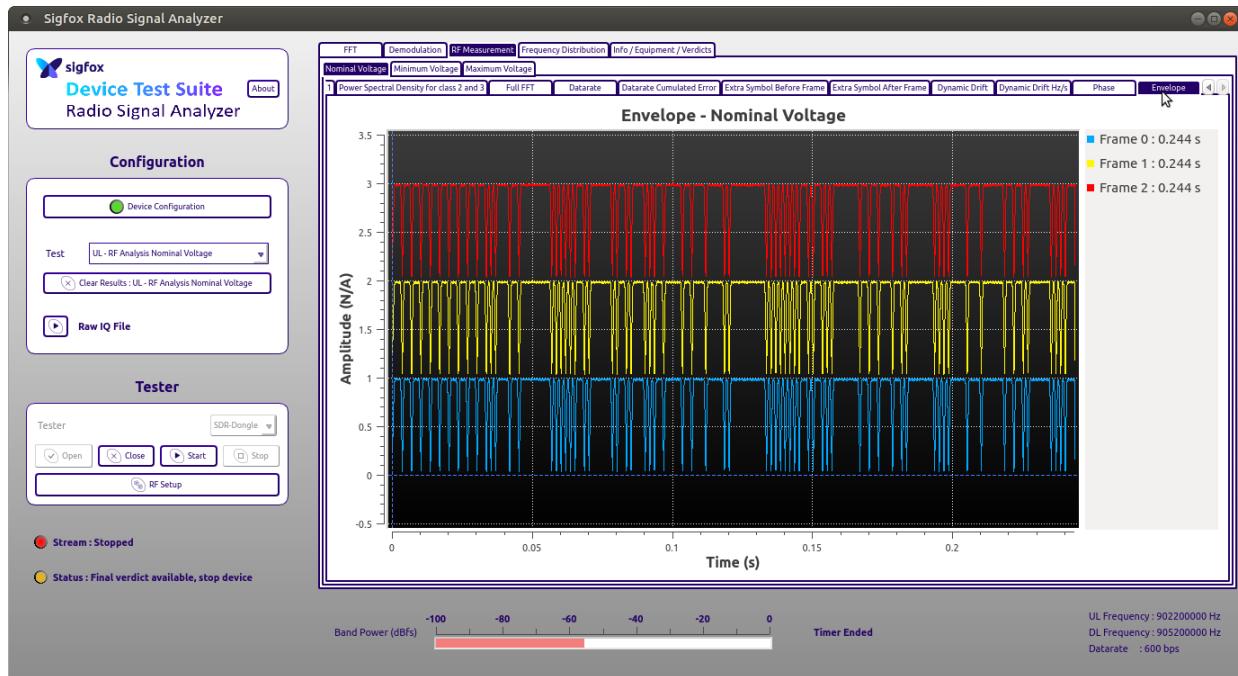


Figure 103: SX1262DVK1DAS RC2 Envelope

6.7.14 Verdict

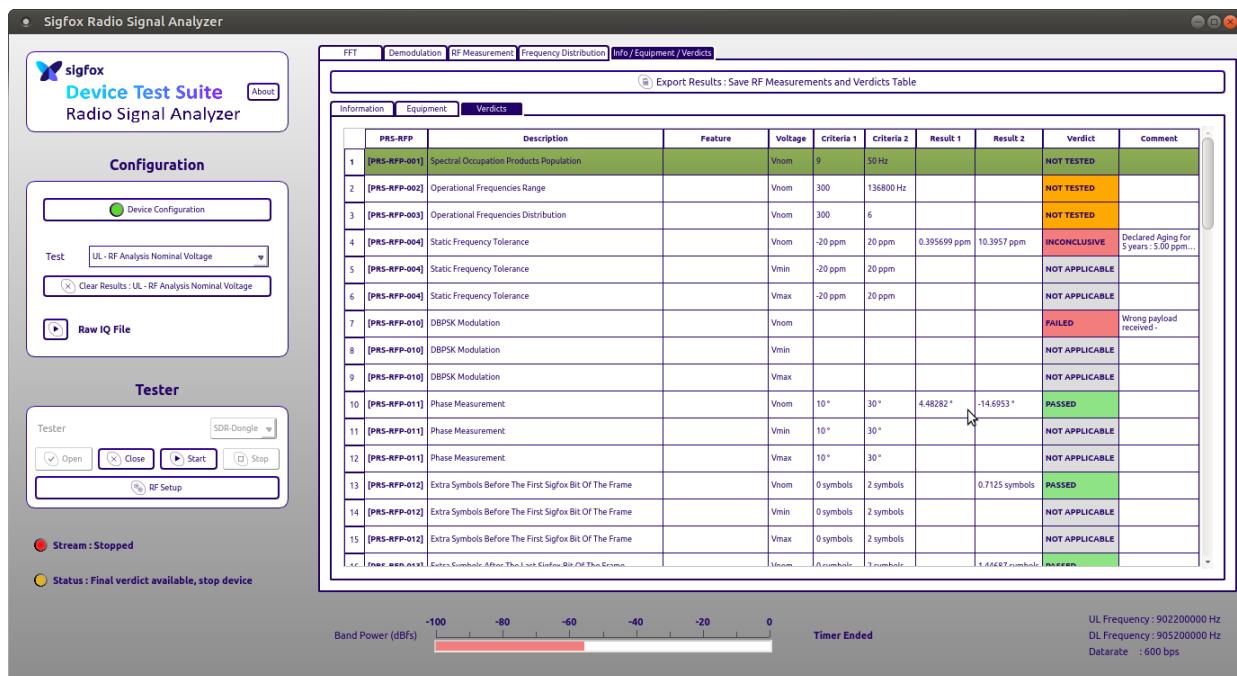


Figure 104: SX1262DVK1DAS RC2 Verdict Page 1

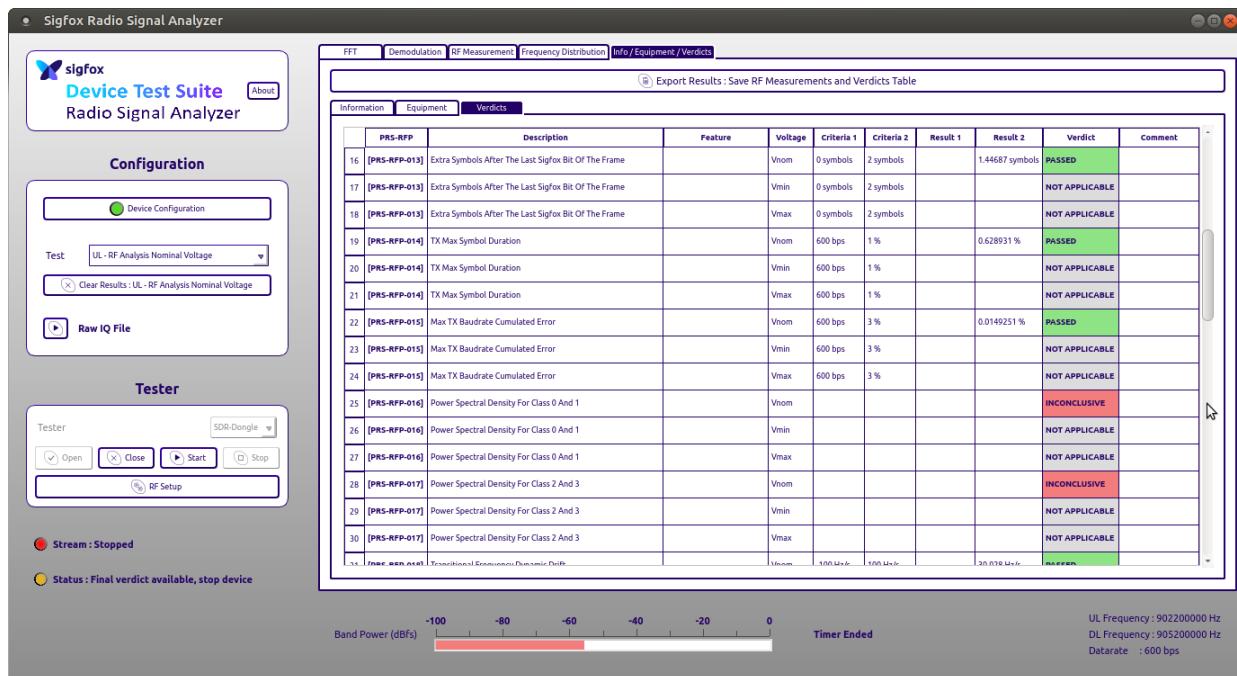


Figure 105: SX1262DVK1DAS RC2 Verdict Page 2

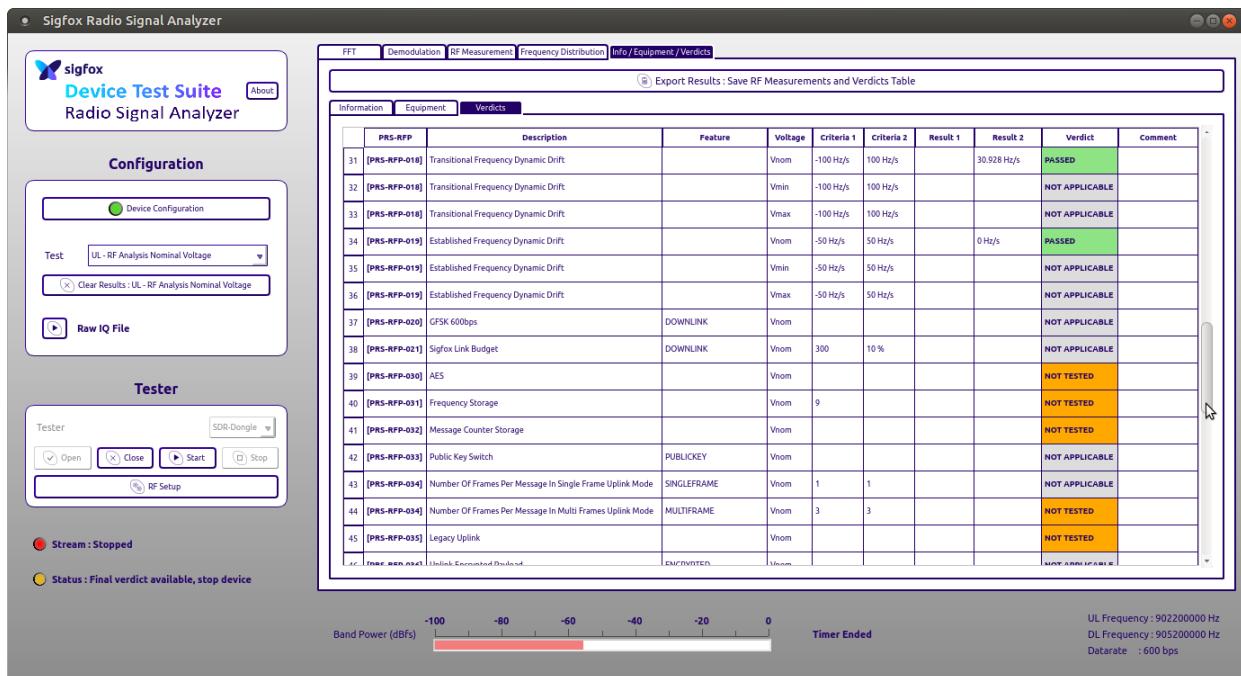


Figure 106: SX1262DVK1DAS RC2 Verdict Page 3

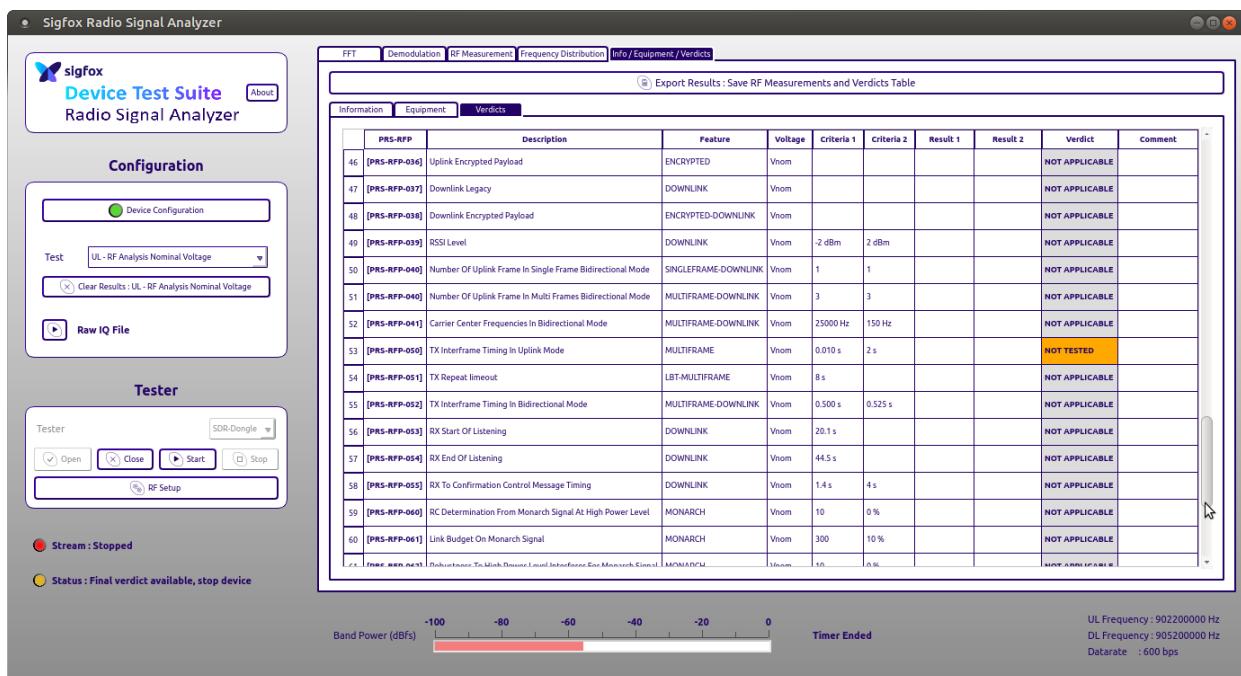


Figure 107: SX1262DVK1DAS RC2 Verdict Page 4

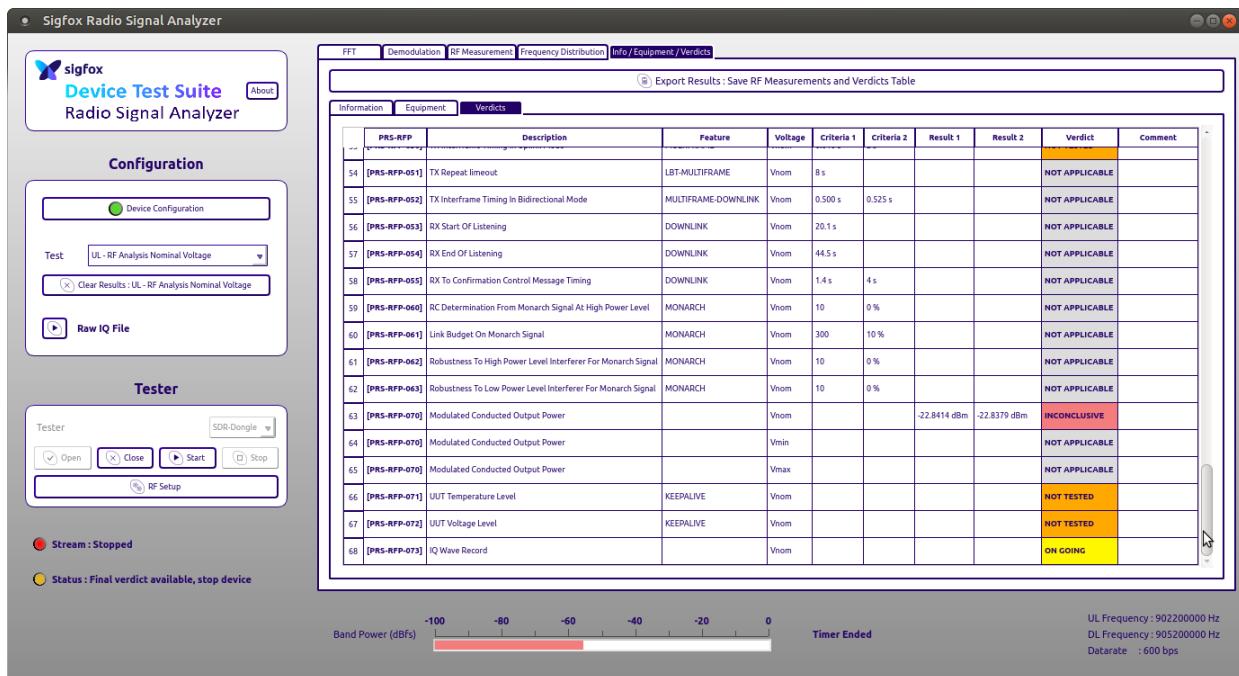


Figure 108: SX1262DVK1DAS RC2 Verdict Page 5

7 Conclusion

This application note has shown how to transmit BPSK packets using a Semtech SX1261/2 or LR11xx radio shield.

Compliance to the Sigfox specification has also been tested on Semtech SX26x and LR11xx shields. However, the user is responsible for performing Sigfox certification of his final hardware and software application.

8 Glossary

Term	Description
API	Application Programming Interface
BPSK	Binary Phase Shift Keying
DBPSK	Differential Binary Phase Shift Keying
GPIO	General Purpose Input Output
LoRa®	Long Range Communication The LoRa® Mark is a registered trademark of the Semtech Corporation
MCU	Micro-Controller Unit
SIGFOX®	SIGFOX is a registered trademark of SIGFOX
SPI	Serial Peripheral Interface
TCXO	Temperature Compensated Crystal Oscillator
UART	Universal Asynchronous Receiver/Transmitter

9 Revision History

Version	ECO	Date	Modifications
0.1	(Private)	July 2022	Initial version
1.0	068776	Dec 2023	First Public version



Important Notice

Information relating to this product and the application or design described herein is believed to be reliable, however such information is provided as a guide only and Semtech assumes no liability for any errors in this document, or for the application or design described herein.

Semtech reserves the right to make changes to the product or this document at any time without notice. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. Semtech warrants performance of its products to the specifications applicable at the time of sale, and all sales are made in accordance with Semtech's standard terms and conditions of sale.

SEMTECH PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS, OR IN NUCLEAR APPLICATIONS IN WHICH THE FAILURE COULD BE REASONABLY EXPECTED TO RESULT IN PERSONAL INJURY, LOSS OF LIFE OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. INCLUSION OF SEMTECH PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE UNDERTAKEN SOLELY AT THE CUSTOMER'S OWN RISK.

Should a customer purchase or use Semtech products for any such unauthorized application, the customer shall indemnify and hold Semtech and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs damages and attorney fees which could arise.

The Semtech name and logo are registered trademarks of the Semtech Corporation. All other trademarks and trade names mentioned may be marks and names of Semtech or their respective companies. Semtech reserves the right to make changes to, or discontinue any products described in this document without further notice. Semtech makes no warranty, representation or guarantee, express or implied, regarding the suitability of its products for any particular purpose. All rights reserved.

© Semtech 2023