

Bit Position of CAN Signals (Start Bit)

i

Example Signal 1: Intel (= Little Endian), message size = 8 bytes, start bit = 28, bit length = 16

| | | | | | | | | | |
|---------------|--|------------|----|----|----|----|----|----|-----|
| ===== | | | | | | | | | |
| CAN Bus | | Bit Number | | | | | | | |
| | | msb | | | | | | | lsb |
| -----+----- | | | | | | | | | |
| Message ID | | | | | | | | | |
| Control Field | | | | | | | | | |
| Data Byte 1 | | 07 | 06 | 05 | 04 | 03 | 02 | 01 | 00 |
| Data Byte 2 | | 15 | 14 | 13 | 12 | 11 | 10 | 09 | 08 |
| Data Byte 3 | | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 |
| Data Byte 4 | | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 |
| Data Byte 5 | | 39 | 38 | 37 | 36 | 35 | 34 | 33 | 32 |
| Data Byte 6 | | 47 | 46 | 45 | 44 | 43 | 42 | 41 | 40 |
| Data Byte 7 | | 55 | 54 | 53 | 52 | 51 | 50 | 49 | 48 |
| Data Byte 8 | | 63 | 62 | 61 | 60 | 59 | 58 | 57 | 56 |
| CRC Field | | | | | | | | | |
| Ack. Field | | | | | | | | | |
| End of Frame | | | | | | | | | |
| ===== | | | | | | | | | |

Example Signal 2: Motorola (= Big Endian), message size = 8 bytes, start bit = 28, bit length = 16

| ===== | |
|---------|------------|
| CAN Bus | Bit Number |
| 0 | 0 |
| 0 | 1 |
| 0 | 2 |
| 0 | 3 |
| 0 | 4 |
| 0 | 5 |
| 0 | 6 |
| 0 | 7 |
| 0 | 8 |
| 0 | 9 |
| 0 | 10 |
| 0 | 11 |
| 0 | 12 |
| 0 | 13 |
| 0 | 14 |
| 0 | 15 |
| 0 | 16 |
| 0 | 17 |
| 0 | 18 |
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| 0 | 30 |
| 0 | 31 |
| 0 | 32 |
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| 0 | 170 |
| 0 | 171 |
| 0 | 172 |
| 0 | 173 |
| 0 | 174 |
| 0 | 175 |
| 0 | 176 |
| 0 | 177 |
| 0 | 178 |
| 0 | 179 |
| 0 | |



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- TEMES
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