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3. As a condition to NTT's provision of the Software to User, NTT has required User to execute this Agreement.

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# 8. No Assignment or Sublicense. Neither this Agreement nor any right or license under this Agreement, nor the Software, may be sublicensed, assigned, or otherwise transferred by User without NTT's prior written consent.

# 9. General

# (a) If any provision, or part of a provision, of this Agreement is or becomes illegal, unenforceable, or invalidated, by operation of law or otherwise, that provision or part shall to that extent be deemed omitted, and the remainder of this Agreement shall remain in full force and effect.

## (b) This Agreement is the complete and exclusive statement of the agreement between the parties with respect to the subject matter hereof, and supersedes all written and oral contracts, proposals, and other communications between the parties relating to that subject matter.

## (c) Subject to Section 8, this Agreement shall be binding on, and shall inure to the benefit of, the respective successors and assigns of NTT and User.

## (d) If either party to this Agreement initiates a legal action or proceeding to enforce or interpret any part of this Agreement, the prevailing party in such action shall be entitled to recover, as an element of the costs of such action and not as damages, its attorneys' fees and other costs associated with such action or proceeding.

## (e) This Agreement shall be governed by and interpreted under the laws of Japan, without reference to conflicts of law principles. All disputes arising out of or in connection with this Agreement shall be finally settled by arbitration in Tokyo in accordance with the Commercial Arbitration Rules of the Japan Commercial Arbitration Association. The arbitration shall be conducted by three (3) arbitrators and in Japanese. The award rendered by the arbitrators shall be final and binding upon the parties. Judgment upon the award may be entered in any court having jurisdiction thereof.

(f)　　 NTT shall not be liable to the User or to any third party for any delay or failure to perform NTT’s obligation set forth under this Agreement due to any cause beyond NTT’s reasonable control.

**EXHIBIT A**

./README.md

./fig10\_q3de\_throughput/build.sh

./fig10\_q3de\_throughput/fig\_scalability\_instruction\_throughput\_micro.pdf

./fig10\_q3de\_throughput/micro\_spawn.py

./fig10\_q3de\_throughput/micro\_stat.py

./fig10\_q3de\_throughput/README.md

./fig10\_q3de\_throughput/src/main.cpp

./fig3\_8\_logical\_error\_rate\_with\_anomaly/build.sh

./fig3\_8\_logical\_error\_rate\_with\_anomaly/CMakeLists.txt

./fig3\_8\_logical\_error\_rate\_with\_anomaly/fig\_decoder\_below\_threshold\_micro.pdf

./fig3\_8\_logical\_error\_rate\_with\_anomaly/fig\_decoder\_below\_threshold\_norollback\_micro.pdf

./fig3\_8\_logical\_error\_rate\_with\_anomaly/plot\_fig3.py

./fig3\_8\_logical\_error\_rate\_with\_anomaly/plot\_fig8.py

./fig3\_8\_logical\_error\_rate\_with\_anomaly/plot\_validate.py

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./fig3\_8\_logical\_error\_rate\_with\_anomaly/spawn\_fig3.py

./fig3\_8\_logical\_error\_rate\_with\_anomaly/spawn\_fig8\_1.py

./fig3\_8\_logical\_error\_rate\_with\_anomaly/spawn\_fig8\_2.py

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./fig3\_8\_logical\_error\_rate\_with\_anomaly/src/common.hpp

./fig3\_8\_logical\_error\_rate\_with\_anomaly/src/correction\_uniform.cpp

./fig3\_8\_logical\_error\_rate\_with\_anomaly/src/correction\_weighted.cpp

./fig3\_8\_logical\_error\_rate\_with\_anomaly/src/main.cpp

./fig3\_8\_logical\_error\_rate\_with\_anomaly/src/make\_error.cpp

./fig3\_8\_logical\_error\_rate\_with\_anomaly/src/visualize.cpp

./fig7\_anomaly\_detection/README.md

./fig7\_anomaly\_detection/evaluator/proc0\_spawn\_allanomaly.py

./fig7\_anomaly\_detection/evaluator/proc1\_calculate\_window.py

./fig7\_anomaly\_detection/evaluator/proc2\_spawn\_latency.py

./fig7\_anomaly\_detection/evaluator/proc3\_calculate\_trajectory.py

./fig7\_anomaly\_detection/evaluator/proc4\_calculate\_detection\_latency.py

./fig7\_anomaly\_detection/evaluator/proc5\_calculate\_position\_error.py

./fig7\_anomaly\_detection/evaluator/proc6\_plot\_micro\_figure.py

./fig7\_anomaly\_detection/evaluator/proc7\_plot\_misc.py

./fig7\_anomaly\_detection/evaluator/proc\_all.sh

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./fig7\_anomaly\_detection/evaluator/\_util\_content.py

./fig7\_anomaly\_detection/evaluator/\_util\_count\_threshold.py

./fig7\_anomaly\_detection/evaluator/\_util\_fileproperty.py

./fig7\_anomaly\_detection/evaluator/\_util\_mumap.py

./fig7\_anomaly\_detection/evaluator/\_util\_mumap\_analysis.py

./fig7\_anomaly\_detection/evaluator/\_util\_plot\_histogram.py

./fig7\_anomaly\_detection/evaluator/figure/fig\_anomaly\_detection\_all\_micro.pdf

./fig7\_anomaly\_detection/evaluator/figure/fig\_anomaly\_detection\_latency.pdf

./fig7\_anomaly\_detection/evaluator/figure/fig\_anomaly\_detection\_latency\_plot.pdf

./fig7\_anomaly\_detection/generator/build.sh

./fig7\_anomaly\_detection/generator/CMakeLists.txt

./fig7\_anomaly\_detection/generator/src/CMakeLists.txt

./fig7\_anomaly\_detection/generator/src/decoder.hpp

./fig7\_anomaly\_detection/generator/src/error\_lattice.hpp

./fig7\_anomaly\_detection/generator/src/main\_anomaly.cpp

./fig7\_anomaly\_detection/generator/src/main\_anomaly\_long.cpp

./fig7\_anomaly\_detection/generator/src/position.hpp

./fig7\_anomaly\_detection/generator/src/random.hpp

./fig7\_anomaly\_detection/generator/src/syndrome\_lattice.hpp

./fig7\_anomaly\_detection/generator/src/syndrome\_lattice\_base.hpp

./fig9\_qubit\_reduction\_by\_q3de/build.sh

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./fig9\_qubit\_reduction\_by\_q3de/calc/fig\_applicability\_sweep.pdf

./fig9\_qubit\_reduction\_by\_q3de/calc/plot\_all.py

./fig9\_qubit\_reduction\_by\_q3de/calc/spawn\_freq.py

./fig9\_qubit\_reduction\_by\_q3de/calc/spawn\_lifetime.py

./fig9\_qubit\_reduction\_by\_q3de/calc/spawn\_size.py

./fig9\_qubit\_reduction\_by\_q3de/calc/\_spawn\_common.py

./fig9\_qubit\_reduction\_by\_q3de/src/main.cpp

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./tab4\_fpga\_implementation/40-BASE/benchmark/tbench\_visualize.cpp

./tab4\_fpga\_implementation/40-Q3DE/tbench.cpp

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./tab4\_fpga\_implementation/80-BASE/tbench.cpp

./tab4\_fpga\_implementation/80-BASE/benchmark/build\_graph.py

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./tab4\_fpga\_implementation/80-BASE/benchmark/main.cpp

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./tab4\_fpga\_implementation/80-BASE/benchmark/tbench\_match.cpp

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./tab4\_fpga\_implementation/automation/run\_with\_arg.py

./tab4\_fpga\_implementation/automation/vitis\_hls\_automation/\_\_init\_\_.py

./tab4\_fpga\_implementation/automation/vitis\_hls\_automation/config.py

./tab4\_fpga\_implementation/automation/vitis\_hls\_automation/execute\_script.py

./tab4\_fpga\_implementation/automation/vitis\_hls\_automation/fetch\_result.py