

ICD Final Report

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RTL Report

Testbench	Pass	(Pass/Fail)
Clock Cycle	20	(ns)
Total Time	667989	(ns)

Synthesis Report

Testbench	Pass	(Pass/Fail)
Clock Cycle	20	(ns)
Cell Area	20139.651059	(Please report Total cell area)
Total Time	667989	(ns)
Area*Time	13453065371.250351	(ns ² *um ²)

APR Report

Testbench	Pass	(Pass/Fail)
Clock Cycle	20	(ns)
Cell Area	40330.224 um ²	(Please report Total area of Core)
Total Time	667989	(ns)
Area*Time	26940145999.536	(ns*um ²)

How to report area in Innovus?

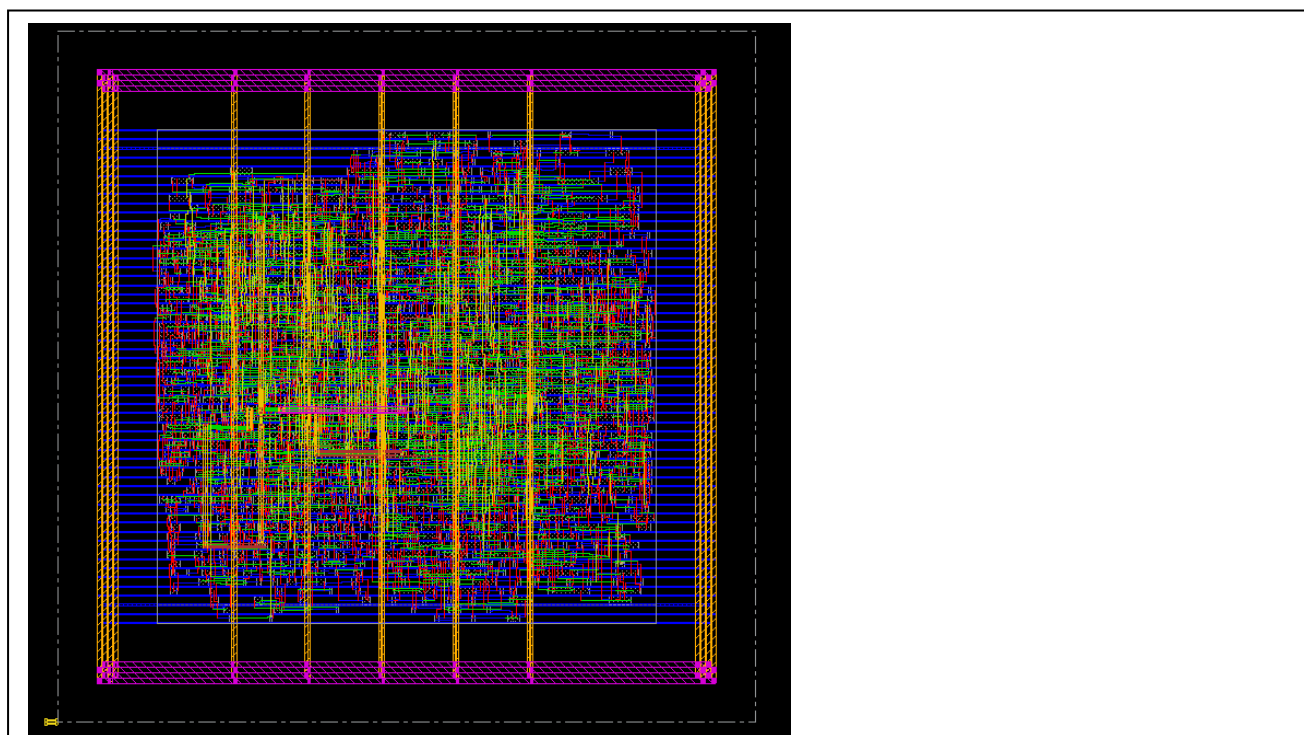
➔ File > Report > Summary > choose Text only, file name: **summaryReport.rpt**>OK, find **Total area of Core.**

Synthesis No latch

Register Name	Type	Width	Bus	MB	AR	AS	SR	SS	ST
h0_r_reg	Flip-flop	6	Y	N	Y	N	N	N	N
v0_r_reg	Flip-flop	6	Y	N	Y	N	N	N	N
state_r_reg	Flip-flop	3	Y	N	Y	N	N	N	N
valid_r_reg	Flip-flop	1	N	N	Y	N	N	N	N
addr_r_reg	Flip-flop	12	Y	N	Y	N	N	N	N
ren_r_reg	Flip-flop	1	N	N	N	Y	N	N	N
o_data_r_reg	Flip-flop	8	Y	N	Y	N	N	N	N
cycle_r_reg	Flip-flop	2	Y	N	Y	N	N	N	N
counter_r_reg	Flip-flop	9	Y	N	Y	N	N	N	N
data_ul_r_reg	Flip-flop	8	Y	N	Y	N	N	N	N
data_ur_r_reg	Flip-flop	8	Y	N	Y	N	N	N	N
data_ll_r_reg	Flip-flop	8	Y	N	Y	N	N	N	N
data_lr_r_reg	Flip-flop	8	Y	N	Y	N	N	N	N
width_r_reg	Flip-flop	4	Y	N	Y	N	N	N	N
height_r_reg	Flip-flop	4	Y	N	Y	N	N	N	N

Presto compilation completed successfully.

APR NanoRoute Innovus Result (截圖)



Report

If you didn't pass APR, the score for your report will be determined based on its completeness.

You may include details on the innovation in your design, or the reasons why you may not have passed the APR.