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(19) **United States**(12) **Patent Application Publication****Bonache Martinez et al.**(10) **Pub. No.: US 2022/0416781 A1**(43) **Pub. Date: Dec. 29, 2022**(54) **PRECHARGE BUFFER STAGE CIRCUIT  
AND METHOD****Publication Classification**(71) Applicant: **Analog Devices International  
Unlimited Company, Limerick (IE)**(51) **Int. Cl.**  
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(2013.01)(21) Appl. No.: **17/721,590**(22) Filed: **Apr. 15, 2022****Related U.S. Application Data**(60) Provisional application No. 63/216,367, filed on Jun.  
29, 2021.(57) **ABSTRACT**

A circuit may include or may be coupled to a precharge structure to reduce or minimize a net perturbation, caused by switching, in the input source. Apparatus and techniques shown herein may enable low input current operation in a signal chain of an analog circuit by such reduction or minimization of such perturbation.

