



(54) **METHOD, SYSTEM, AND DEVICE FOR SLEEP MODE RECOVERY**

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(57) **ABSTRACT**
A circuit including a device including a receiver having a receiver input, a receiver output, and a bias current input and a bias generator having a bias current output coupled to the bias current input. The device also includes a buffer having a buffer input, an enable input, and a buffer output, the buffer input coupled to the receiver output and a delay circuit having a delay output coupled to the enable input.

