



US 20240223208A1

(19) **United States**

(12) **Patent Application Publication**
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(10) **Pub. No.: US 2024/0223208 A1**

(43) **Pub. Date: Jul. 4, 2024**

(54) **TOP-DOWN RELATIVE DAC CALIBRATION**

(52) **U.S. Cl.**

CPC **H03M 1/825** (2013.01); **H03M 1/1009** (2013.01)

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(57)

ABSTRACT

A system and method for calibrating a digital-to-analog converter (DAC) device. The method includes tuning a second subset of one or more DAC segments to match a strength of a first subset of DAC segments wherein the first subset of DAC segments is of a strength nominally equal to that of the second subset of DAC segments. The process is iterative, and the second subset of DAC segments is associated with lesser significant bits than the bits associated with the first subset of DAC segments. The process is repeated to tune each of successive second subsets of DAC segments to corresponding successive first subsets of DAC segments in top-down order from a segment associated with a MSB input to a segment associated with a LSB input. In each case the first subset of DAC segments is of a strength nominally equal to that of the second subset of DAC segments.

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(21) Appl. No.: **18/091,513**

(22) Filed: **Dec. 30, 2022**

Publication Classification

(51) **Int. Cl.**

H03M 1/82 (2006.01)

H03M 1/10 (2006.01)

