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PARK et al.(10) **Pub. No.: US 2022/0360282 A1**(43) **Pub. Date: Nov. 10, 2022**(54) **LOW DENSITY PARITY CHECK ENCODER
HAVING LENGTH OF 64800 AND CODE
RATE OF 7/15, AND LOW DENSITY PARITY
CHECK ENCODING METHOD USING THE
SAME**continuation of application No. 14/496,700, filed on
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Nam-Ho HUR, Sejong (KR)(21) Appl. No.: **17/864,144**(22) Filed: **Jul. 13, 2022****Related U.S. Application Data**(63) Continuation of application No. 17/202,050, filed on
Mar. 15, 2021, now Pat. No. 11,424,763, which is a
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Aug. 16, 2019, now Pat. No. 10,979,073, which is a
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Jul. 3, 2017, now Pat. No. 10,432,220, which is a
continuation of application No. 15/234,531, filed on
Aug. 11, 2016, now Pat. No. 9,729,172, which is a(57) **ABSTRACT**

A low density parity check (LDPC) encoder, an LDPC decoder, and an LDPC encoding method are disclosed. The LDPC encoder includes first memory, second memory, and a processor. The first memory stores an LDPC codeword having a length of 64800 and a code rate of 7/15. The second memory is initialized to 0. The processor generates the LDPC codeword corresponding to information bits by performing accumulation with respect to the second memory using a sequence corresponding to a parity check matrix (PCM).

