

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2023/0231818 A1 Galles et al.

Jul. 20, 2023 (43) **Pub. Date:**

(54) METHODS AND SYSTEMS FOR LINE RATE PACKET CLASSIFIERS FOR PRESORTING NETWORK PACKETS ONTO INGRESS **OUEUES**

(71) Applicant: Pensando Systems Inc., Milpitas, CA

(72)Inventors: Michael Brian Galles, Los Altos, CA (US); Vipin Jain, San Jose, CA (US)

Appl. No.: 17/580,367 (21)

(22) Filed: Jan. 20, 2022

Publication Classification

(51) Int. Cl. H04L 49/00 (2006.01)H04L 41/5019 (2006.01)

H04L 47/32 (2006.01)H04L 69/22 (2006.01)

(52) U.S. Cl.

CPC H04L 49/3018 (2013.01); H04L 41/5019 (2013.01); H04L 47/32 (2013.01); H04L 69/22

(2013.01)

(57)**ABSTRACT**

A network appliance can have an input port that can receive network packets at line rate, two or more ingress queues, a line rate classification circuit that can place the network packets on the ingress queues at the line rate, a packet buffer that can store the network packets, and a sub line rate packet processing circuit that can process the network packets that are stored in the packet buffer. The line rate classification circuit can place a network packet on one of the ingress queues based on the network packet's packet contents. A buffer scheduler can select network packets for processing by a sub line rate packet processing circuit based on the priority levels of the ingress to queues.

