

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0215249 A1 KIM et al.

Jun. 27, 2024 (43) **Pub. Date:**

(54) VERTICAL NAND FLASH MEMORY DEVICE

(71) Applicant: Samsung Electronics Co., Ltd., Suwon-si (KR)

(72) Inventors: **Kyunghun KIM**, Suwon-si (KR); Sunho KIM, Suwon-si (KR); Seyun

KIM, Suwon-si (KR); Hyungyung KIM, Suwon-si (KR); Seungyeul YANG, Suwon-si (KR); Gukhyon YON, Hwaseong-si (KR); Minhyun LEE, Suwon-si (KR); Seokhoon CHOI, Suwon-si (KR); Hoseok HEO,

Suwon-si (KR)

(73) Assignee: Samsung Electronics Co., Ltd.,

Suwon-si (KR)

(21) Appl. No.: 18/322,365

(22) Filed: May 23, 2023

(30)Foreign Application Priority Data

Dec. 22, 2022 (KR) 10-2022-0182168

Publication Classification

(51) Int. Cl. (2006.01)H10B 43/35 H10B 43/27 (2006.01)

(52) U.S. Cl. CPC H10B 43/35 (2023.02); H10B 43/27 (2023.02)

(57)ABSTRACT

A vertical NAND flash memory device may include a plurality of cell arrays. Each of the plurality of cell arrays may include a channel layer, a charge trap layer on the channel layer, and a plurality of gate electrodes on the charge trap layer. The charge trap layer may include silicon oxynitride comprising a metal. The metal may include at least one of Ga or In.

