



US 20240244907A1

(19) **United States**

(12) **Patent Application Publication**
Kim et al.

(10) **Pub. No.: US 2024/0244907 A1**

(43) **Pub. Date: Jul. 18, 2024**

(54) **DISPLAY APPARATUS**

Publication Classification

(71) Applicant: **Samsung Display Co., Ltd.**, Yongin-si (KR)

(72) Inventors: **Sunghwan Kim**, Yongin-si (KR);
Hwansoo Jang, Yongin-si (KR);
Daehyun Kim, Yongin-si (KR); **Heyjin Shin**, Yongin-si (KR)

(73) Assignee: **Samsung Display Co., Ltd.**, Yongin-si (KR)

(21) Appl. No.: **18/492,936**

(22) Filed: **Oct. 24, 2023**

(30) **Foreign Application Priority Data**

Jan. 12, 2023 (KR) 10-2023-0004924

(51) **Int. Cl.**

H10K 59/131 (2006.01)

H10K 59/121 (2006.01)

H10K 59/122 (2006.01)

H10K 59/35 (2006.01)

(52) **U.S. Cl.**

CPC **H10K 59/131** (2023.02); **H10K 59/1213** (2023.02); **H10K 59/1216** (2023.02); **H10K 59/122** (2023.02); **H10K 59/353** (2023.02)

(57)

ABSTRACT

A display apparatus includes a driving voltage line disposed on a substrate and extending in a first direction, a data line extending in the first direction and apart from the driving voltage line, a first sub-pixel electrode in which a first emission area is defined, and a second sub-pixel electrode in which a second emission area is defined. The first emission area overlaps the data line in a plan view, and the second emission area is disposed inside an edge of the driving voltage line.

