



US 20240223162A1

(19) **United States**

(12) **Patent Application Publication**
HU et al.

(10) **Pub. No.: US 2024/0223162 A1**

(43) **Pub. Date: Jul. 4, 2024**

(54) **DRIVING OUTPUT CIRCUIT, CHIP, AND DRIVING OUTPUT METHOD**

Publication Classification

(51) **Int. Cl.**

H03K 3/012 (2006.01)

H03K 17/687 (2006.01)

(52) **U.S. Cl.**

CPC **H03K 3/012** (2013.01); **H03K 17/6872**

(2013.01); **H03K 2217/0063** (2013.01); **H03K**

2217/0072 (2013.01)

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(21) Appl. No.: **17/802,133**

(22) PCT Filed: **May 25, 2021**

(86) PCT No.: **PCT/CN2021/095737**

§ 371 (c)(1),

(2) Date: **Aug. 24, 2022**

(30) **Foreign Application Priority Data**

Dec. 24, 2020 (CN) 202011551720.9

(57)

ABSTRACT

A driving output circuit, a chip, and a driving output method are provided; the driving output circuit includes: a timer for outputting a timing signal; a bootstrap module for generating a first turn-on voltage based on an input signal; a charge pump for generating a second turn-on voltage based on the timing signal; a driving module including an upper driving MOSFET and a lower driving MOSFET connected to the upper driving MOSFET, and the upper driving MOSFET is connected to the lower driving MOSFET at a signal output; the first turn-on voltage and the second turn-on voltage are both used to turn on the upper driving MOSFET and/or the lower driving MOSFET to cause the signal output to output an output signal. The present disclosure provides a dynamic hybrid driving output circuit, which has improved gate oxide reliability and an enhanced anti-leakage function when not powered.

