

US 20240237431A9

(19) United States

(12) **Patent Application Publication Zheng et al.**

(10) Pub. No.: US 2024/0237431 A9

(48) **Pub. Date: Jul. 11, 2024 CORRECTED PUBLICATION**

(54) OLED DISPLAY PANEL AND MANUFACTURING METHOD THEREOF

(71) Applicant: Shenzhen China Star Optoelectronics

Semiconductor Display Technology Co., Ltd, Shenzhen, Guangdong (CN)

(72) Inventors: Shuai Zheng, Shenzhen, Guangdong

(CN); Zhaosong Liu, Shenzhen,

Guangdong (CN)

(73) Assignee: SHENZHEN CHINA STAR

OPTOELECTRONICS SEMICONDUCTOR DISPLAY TECHNOLOGY CO., LTD.,

Shenzhen, Guangdong (CN)

(21) Appl. No.: 17/755,149

(22) PCT Filed: Apr. 15, 2022

(86) PCT No.: **PCT/CN2022/087117**

§ 371 (c)(1),

(2) Date: **Apr. 21, 2022**

Prior Publication Data

(15) Correction of US 2024/0138209 A1 Apr. 25, 2024See (22) PCT Filed.See (86) PCT No.See (30) Foreign Application Priority Data.

(65) US 2024/0138209 A1 Apr. 25, 2024

(30) Foreign Application Priority Data

Mar. 24, 2022 (CN) 202210297933.6

Publication Classification

(51) Int. Cl.

H10K 59/131 (2006.01)

H10K 59/12 (2006.01)

H10K 59/123 (2006.01)

H10K 71/00 (2006.01)

H10K 71/60 (2006.01)

(52) U.S. Cl.

CPC *H10K 59/131* (2023.02); *H10K 59/1201* (2023.02); *H10K 59/123* (2023.02); *H10K 59/126* (2023.02); *H10K 71/60* (2023.02); *H10K 71/621* (2023.02)

(57) ABSTRACT

The present application discloses an OLED display panel and a manufacturing method thereof. The OLED display panel includes: a base layer, an active layer, a gate insulating layer, a first metal layer, and a second metal layer, wherein the active layer includes a channel portion, a source contact portion, a first electrode, and an anode; the second metal layer is arranged above the first metal layer and connected to the source contact portion; the insulating layer, the first metal layer, and the second metal layer are stacked, and an opening is defined in the insulating layer at the position corresponding to the anode.

