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(54) **METHODS AND APPARATUS TO  
CALIBRATE MULTIPHASE BUCK  
REGULATORS**

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(57) **ABSTRACT**

An example non-transitory machine-readable storage medium includes instructions that, when executed, configure processor circuitry to at least: determine a first delay corresponding to an amount of time for a first pulse to reach first phase circuitry; determine a second delay corresponding to an amount of time for a second pulse to reach second phase circuitry; determine a third delay corresponding to an amount of time for a third pulse to reach third phase circuitry, wherein one or more of the first phase circuitry, the second phase circuitry, and the third phase circuitry are located a non-uniform distance from the processor circuitry; and transmit, based on the delays, the pulses to the respective phase circuitry such that a first time period between the first pulse and the second pulse is equal to a second time period between the second pulse and the third pulse.

