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(57) **ABSTRACT**

An array substrate is provided. The array substrate includes a cathode layer extending across a plurality of subpixel regions and an inter-subpixel region. The cathode layer includes a plurality of first ring structures respectively in a plurality of first regions of the inter-subpixel region, a respective first region of the plurality of first regions surrounding a respective subpixel region of the plurality of subpixel regions. A respective first ring structure of the plurality of first ring structures protrudes away from a base substrate, forming a ridge.

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