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HSU et al.(10) **Pub. No.: US 2024/0222994 A1**(43) **Pub. Date: Jul. 4, 2024**(54) **CHARGING/DISCHARGING CONTROL  
CIRCUIT**(52) **U.S. Cl.**CPC ..... **H02J 7/00712** (2020.01); **H02J 7/0024**  
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**ABSTRACT**(72) Inventors: **Hao-Cheng HSU**, Hsinchu (TW);  
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A charging/discharging control circuit, comprising a first inverter circuit, a second inverter circuit and a driving circuit. The first inverter circuit is configured to generate a first control voltage on a first control node according to a threshold voltage of a first control transistor. The second inverter circuit is configured to generate a second control voltage on a second control node according to a threshold voltage of a second control transistor. The driving circuit is coupled to the first inverter circuit, the second inverter circuit and a load, and is configured to selectively charge or discharge the load according to the first control voltage or the second control voltage.

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