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ZHAO et al. (43) **Pub. Date: Dec. 8, 2022**(54) **INTEGRATED CIRCUIT, CONTROL METHOD, AND SYSTEM**(52) **U.S. Cl.**
CPC **H03K 17/56** (2013.01)(71) Applicant: **HUAWEI TECHNOLOGIES CO., LTD.**, Shenzhen (CN)(57) **ABSTRACT**(72) Inventors: **Pengfei ZHAO**, Shenzhen (CN);
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An integrated circuit, a control method, and a system are provided, to improve reliability of the integrated circuit. The integrated circuit mainly includes a power supply pin (P1), a configuration pin (P4, P5), a switchable pull-up resistor (KR1, KR2), and a control unit (2011). The integrated circuit can provide a control signal for a target chip (202) by using the configuration pin (P4, P5) of the integrated circuit. In the integrated circuit, a first end of the switchable pull-up resistor (KR1, KR2) is connected to the power supply pin (P1), a second end of the switchable pull-up resistor (KR1, KR2) is connected to the configuration pin (P4, P5), and a control end of the switchable pull-up resistor (KR1, KR2) is connected to the control unit (2011). The power supply pin (P1) can receive a power supply voltage of the integrated circuit.

