

US 20240235570A9

### (19) United States

# (12) **Patent Application Publication** CHAO

### (10) Pub. No.: US 2024/0235570 A9

# (48) **Pub. Date: Jul. 11, 2024 CORRECTED PUBLICATION**

### (54) APPARATUS AND METHOD OF REFERENCE-FREE SAR ANALOG-TO-DIGITAL CONVERSION

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- (21) Appl. No.: 17/971,801
- (22) Filed: Oct. 24, 2022

### **Prior Publication Data**

- (15) Correction of US 2024/0137036 A1 Apr. 25, 2024 See (22) Filed
- (65) US 2024/0137036 A1 Apr. 25, 2024

#### **Publication Classification**

- (51) **Int. Cl. H03M 1/12** (2006.01)
- (52) **U.S. Cl.** CPC ...... *H03M 1/1245* (2013.01)
- (57) ABSTRACT

A method of eliminating reference voltage of Analog-to-Digital Converter to enhance faster conversion rate, achieve compact size and decrease power consumption for Successive Approximation Register (SAR) Analog-to-Digital Converter (ADC).

