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SHIOMI et al.(10) **Pub. No.: US 2024/0237373 A1**(43) **Pub. Date: Jul. 11, 2024**(54) **SOLID-STATE IMAGING ELEMENT**(30) **Foreign Application Priority Data**(71) Applicants: **SONY GROUP CORPORATION**,
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SAITO, Kanagawa (JP)(57) **ABSTRACT**

A solid-state imaging element according to an embodiment of the present disclosure includes: a photoelectric conversion layer including first semiconductor nanoparticles; and a buffer layer including second semiconductor nanoparticles. A p-n junction surface is formed at an interface between the photoelectric conversion layer and the buffer layer. A product of a carrier concentration and a film thickness of the buffer layer is larger than a product of a carrier concentration of the photoelectric conversion layer and a diffusion length of a minority carrier, and a thickness of a depletion region formed in the photoelectric conversion layer is maximized.

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