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(54) DUAL LOOP VOLTAGE CLAMP

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## (57)**ABSTRACT**

A dual loop clamp circuit includes a clamp circuit and a low-side driver circuit. The clamp circuit includes a clamp enable output and a low-side clamp output. The low-side driver circuit includes a low-side control signal input and an output stage. The output stage includes a low-side drive output and an input. The low-side drive output is coupled to the low-side clamp output. The input of the output stage is coupled to the clamp enable output and the low-side control signal input.

