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(19) **United States**(12) **Patent Application Publication****Borse**(10) **Pub. No.: US 2022/0376656 A1**(43) **Pub. Date: Nov. 24, 2022**(54) **OSCICLAMP - AN ELECTRONIC CIRCUIT  
TO INCREASE LOW VOLTAGE LEVELS OF  
ELECTRICAL SOURCES**(71) Applicant: **Swaresh Borse**, Allen, TX (US)(72) Inventor: **Swaresh Borse**, Allen, TX (US)(21) Appl. No.: **17/746,142**(22) Filed: **May 17, 2022****Related U.S. Application Data**

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**H03B 5/12** (2006.01)(52) **U.S. Cl.**  
CPC ..... **H03B 5/1243** (2013.01)(57) **ABSTRACT**

The present invention is an electronic circuit, which can also be built into an integrated circuit to create a single electronic component, used to increase voltage levels of electrical signals from sources having low voltage levels for any required application in an electrical system. While the focus is to increase voltage levels, current levels can also be optimized per application requirements. It is built by electronically cascading a clamper circuit with an oscillator circuit. The oscillator circuit generates an AC signal. The basic functionality of a clamper circuit is to raise DC level of an AC signal. With an oscillator circuit feeding an AC signal to the clamper circuit, multiple applications can be achieved economically. Said invention can be used for driving LEDs at low voltage levels, charge capacitors in a circuit to voltage levels higher than applied voltages, low frequency signal amplifiers, low frequency signal generators, AM/FM modulators, etc.

