



US 20220385310A1

(19) **United States**(12) **Patent Application Publication**
CASTRILLON et al.(10) **Pub. No.: US 2022/0385310 A1**(43) **Pub. Date: Dec. 1, 2022**(54) **METHOD AND DEVICE FOR
ENERGY-EFFICIENT DECODERS****Publication Classification**(51) **Int. Cl.****H03M 13/37** (2006.01)**H03M 13/00** (2006.01)**H03M 13/01** (2006.01)**H03M 13/11** (2006.01)(52) **U.S. Cl.**CPC **H03M 13/3707** (2013.01); **H03M 13/616**
(2013.01); **H03M 13/015** (2013.01); **H03M**
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(AR)(21) Appl. No.: **17/882,136**(22) Filed: **Aug. 5, 2022****Related U.S. Application Data**(63) Continuation of application No. 16/778,918, filed on
Jan. 31, 2020, now Pat. No. 11,424,766.

(57)

ABSTRACT

A decoder circuit includes first and second decoders. The first decoder is a first type of decoder configured to receive data encoded with an error correction code and decode and eliminate errors from a first subset of codewords of the data. The second decoder is a second type of decoder configured receive the data encoded with the error correction code and decode and eliminate errors from a second subset of codewords of the data, different from the first subset of the codewords, without attempting to decode and eliminate errors from the first subset of the codewords.

