

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2022/0368331 A1 **Breynaert**

Nov. 17, 2022 (43) **Pub. Date:**

(54) PARASITIC PULSE CANCELATION **CIRCUIT**

(71) Applicant: INTEVA PRODUCTS, LLC, Troy, MI

Inventor: François Breynaert, Caen (FR)

Appl. No.: 17/715,223

Filed: (22)Apr. 7, 2022

(30)Foreign Application Priority Data

(FR) 21/03684 Apr. 9, 2021

Publication Classification

(51) Int. Cl. H03K 23/58 (2006.01)H02P 7/18 (2006.01)H02P 7/00 (2006.01)H02P 27/08 (2006.01) (52) U.S. Cl. CPC H03K 23/58 (2013.01); H02P 7/18 (2013.01); H02P 7/0094 (2013.01); H02P **27/08** (2013.01)

(57)ABSTRACT

A motor control system includes a DC motor and a ripple count circuit. The DC motor includes a rotor that rotates in response to a drive current. The rotation of the rotor generates a mechanical force that drives a component. The ripple count circuit includes an active filter circuit and a parasitic pulse cancellation circuit. The active filter circuit is configured to filter the drive current and to generate a pulsed signal. The parasitic pulse cancelation circuit is in signal communication with the ripple count circuit to receive the pulsed signal and generates a ripple count signal that excludes parasitic pulses included in the pulsed signal having a parasitic voltage level that exceeds a voltage level of a voltage threshold. The parasitic pulse cancelation circuit actively adjusts the voltage level of the voltage threshold based at least in part on a rotational direction of the rotor.

