



US 20230231563A1

(19) **United States**(12) **Patent Application Publication**
Lin(10) **Pub. No.: US 2023/0231563 A1**(43) **Pub. Date: Jul. 20, 2023**(54) **DUTY-CYCLE CORRECTOR PHASE SHIFT
CIRCUIT****Publication Classification**(71) Applicant: **Taiwan Semiconductor
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Manufacturing Company, Ltd.,
Hsinchu (TW)**(21) Appl. No.: **18/186,676**(22) Filed: **Mar. 20, 2023****Related U.S. Application Data**(63) Continuation of application No. 17/538,291, filed on
Nov. 30, 2021, now Pat. No. 11,611,335.(60) Provisional application No. 63/185,159, filed on May
6, 2021.(51) **Int. Cl.****H03L 7/081** (2006.01)**H03L 7/095** (2006.01)**H03K 5/156** (2006.01)(52) **U.S. Cl.**CPC **H03L 7/0812** (2013.01); **H03L 7/095**
(2013.01); **H03K 5/1565** (2013.01)

(57)

ABSTRACT

One embodiment of a duty-cycle corrector phase shift (DCCPS) circuit includes a voltage-controlled delay line circuit, a duty-cycle correct circuit, an error amplifier circuit, and DC sampler circuits. Another embodiment of a duty-cycle corrector phase shift circuit includes a digital-controlled delay line circuit, a duty-cycle correct circuit, DC sampler circuits, a comparator circuit, a counter circuit, a control circuit, and a lock detector circuit. In some instances, the DCCPS circuit provides a clock signal with a duty-cycle of approximately fifty percent (50%) and a given phase shift between an input clock signal and the output clock signal.

