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(54) **ENHANCEMENT MODE GALLIUM NITRIDE TRANSISTOR**

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(57) **ABSTRACT**

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Techniques to fabricate an enhancement mode HEMT device where the normally off characteristic is implemented through the backside of the device by epitaxially growing a semiconductor layer, such as aluminum nitride (AlN) or aluminum gallium nitride (AlGaN), to deplete a two-dimensional electron gas (2DEG) channel. This buried semiconductor layer, e.g., a buried AlN or AlGaN layer, advantageously maintains a high transconductance and is more amenable to gate scaling than other enhancement mode techniques.

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