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(54) **VIA LAYER IN TRANSMISSION AREA AND
DISPLAY DEVICE INCLUDING THE SAME**

(52) **U.S. Cl.**

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(57)

ABSTRACT

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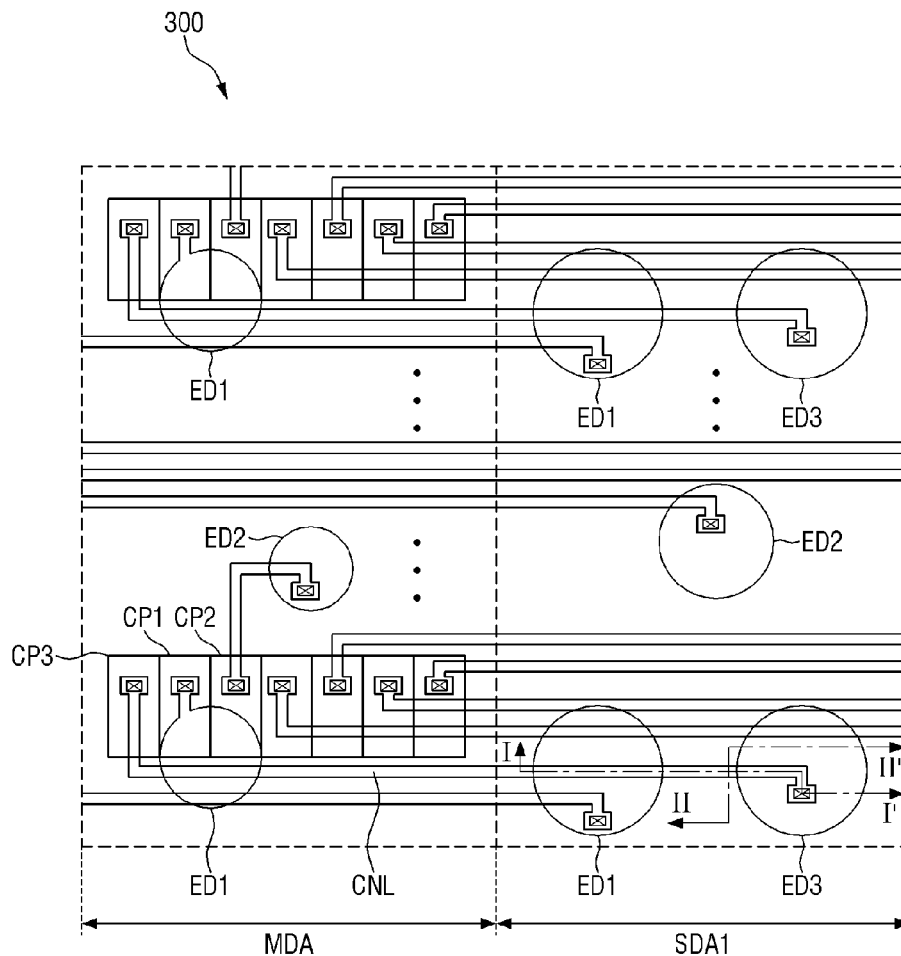
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A display device includes a main display area and a sub-display area adjacent to each other, a transistor in the main display area, a light emitting element which is in the sub-display area and electrically connected to the transistor, the light emitting element including a first electrode, a light emitting layer and a second electrode, a connection line which extends from the main display area to the sub-display area and connects the transistor to the light emitting element, and a via layer between the transistor and the light emitting element. The via layer has a first thickness overlapping the first electrode together with having a second thickness which overlaps the connection line and is smaller than the first thickness.



PX1: CP1, ED1
PX2: CP2, ED2
PX3: CP3, ED3
PX: PX1, PX2, PX3