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GERFERS et al.(10) **Pub. No.: US 2022/0407539 A1**(43) **Pub. Date: Dec. 22, 2022**(54) **SIGMA-DELTA ANALOGUE-TO-DIGITAL
CONVERTER WITH GMC-VDAC**(71) Applicant: **TECHNISCHE UNIVERSITÄT
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(2013.01)(57) **ABSTRACT**

The present invention relates to a sigma-delta analogue-to-digital converter. The sigma-delta analogue-to-digital converter comprises a transconductance stage having first, second and third terminals. A capacitor is connected in parallel at the third terminal. Further, the sigma-delta analogue-to-digital converter comprises a quantiser at the third terminal of the transconductance stage with feedback by a voltage digital-to-analogue converter for feeding back a feedback signal to one of the terminals of the transconductance stage.

