

(54) ARRAY SUBSTRATE, DISPLAY PANEL AND DISPLAY DEVICE

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(57) ABSTRACT

An array substrate has a display area and a non-display area including a first bonding region and a fan-out region located between the display area and the first bonding region. The array substrate includes: a plurality of pixel columns disposed in the display area, at least two first power supply input terminals disposed in the first bonding region, and at least two first fan-out structures disposed in the fan-out region. Each pixel column includes light-emitting units. Each first power supply input terminal is connected to multiple pixel columns of the plurality of pixel columns through one first fan-out structure, so as to provide a first power supply signal to the multiple pixel columns. Each first fan-out structure includes a plurality of first conductive units, and each first conductive unit is electrically connected to at least one of the plurality of pixel columns.

