

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0213963 A1 Ko et al.

Jun. 27, 2024 (43) **Pub. Date:**

(54) PULL-UP VOLTAGE DETECTION CIRCUIT AND PULL-UP VOLTAGE DETECTION **METHOD**

(71) Applicant: Richtek Technology Corporation, Zhubei City (TW)

(72) Inventors: Chien-Tzu Ko, Pingtung (TW); Jiun-Jang Lin, Taichung (TW); Yan-Jhen Huang, Chiayi (TW); Jhen-Yu Li, Kaohsiung (TW)

Appl. No.: 18/539,356

(22) Filed: Dec. 14, 2023

Related U.S. Application Data

Provisional application No. 63/476,987, filed on Dec. 23, 2022.

(30)Foreign Application Priority Data

Feb. 22, 2023 (TW) 112106425

Publication Classification

(51) **Int. Cl.** H03K 3/0233 (2006.01)H03K 5/08 (2006.01) H03K 5/24 (2006.01)(2006.01)H03K 19/17728

U.S. Cl.

CPC H03K 3/02332 (2013.01); H03K 5/082 (2013.01); H03K 5/249 (2013.01); H03K

19/17728 (2013.01)

(57)ABSTRACT

A pull-up voltage detection circuit is for use in a serial bus. The serial bus includes a communication signal. During a communication interval, the communication signal is toggled based on a pull-up voltage for communicating on the serial bus via open-drain scheme. The pull-up voltage detection circuit includes: at least one comparator circuit for comparing the communication signal or a divided voltage thereof with at least one reference voltage in a detection procedure, so as to generate at least one comparison result; and a selector circuit for selecting one of plural predetermined voltages according to the at least one comparison result. The selected predetermined voltage serves as a logic threshold voltage corresponding to the pull-up voltage. In the communication interval, the logic state of the communication signal is determined by comparing the communication signal and the logic threshold voltage for communicating on the serial bus.

