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YOSHIOKA(10) **Pub. No.: US 2023/0230773 A1**(43) **Pub. Date: Jul. 20, 2023**(54) **MULTILAYER CERAMIC CAPACITOR**(52) **U.S. Cl.**(71) Applicant: **Murata Manufacturing Co., Ltd.**,
Nagaokakyo-shi (JP)CPC **H01G 4/30** (2013.01); **H01G 4/1227**
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4/008 (2013.01)(72) Inventor: **Hiroyuki YOSHIOKA**,
Nagaokakyo-shi (JP)(57) **ABSTRACT**(21) Appl. No.: **18/097,630**

A multilayer ceramic capacitor includes a multilayer body including dielectric layers laminated on each other, inner electrode layers laminated on the dielectric layers, first and second main surfaces, first and second end surfaces, and first and second lateral surfaces, first and second external electrodes on the first and second end surfaces, an inner layer portion in which the inner electrode layers are opposed to each other, outer layer portions on first and second main surface sides and first and second lateral surface sides. Voids are provided in the outer layer portions on the first and second lateral surface sides. A ratio of a total area of the voids relative to an area of the outer layer portion on the first or second lateral surface side in a cross section of the multilayer body is greater than or equal to about 0.02% and less than or equal to about 0.2%.

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