



US 20220385297A1

(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2022/0385297 A1**
(43) **Pub. Date: Dec. 1, 2022**(54) **ANALOG-TO-DIGITAL CONVERSION
CIRCUIT AND RECEIVER INCLUDING
SAME**(71) Applicant: **SAMSUNG ELECTRONICS CO.,
LTD.**, Suwon-si (KR)(72) Inventors: **SUNGNO LEE**, HWASEONG-SI
(KR); **HEECHANG HWANG**, SEOUL
(KR); **YONGKI LEE**, SUWON-SI
(KR); **KYOUNGJUN MOON**,
HWASEONG-SI (KR); **HYOCHUL
SHIN**, SEOUL (KR); **MICHAEL
CHOI**, SEOUL (KR)(21) Appl. No.: **17/827,135**(22) Filed: **May 27, 2022**(30) **Foreign Application Priority Data**

May 31, 2021 (KR) 10-2021-0070108

Publication Classification(51) **Int. Cl.**
H03M 1/10 (2006.01)(52) **U.S. Cl.**
CPC **H03M 1/1014** (2013.01)(57) **ABSTRACT**

An analog-to-digital conversion circuit includes analog-to-digital converters (ADCs) including a target analog-to-digital converter (ADC) providing second data samples, a first adjacent ADC providing first data samples, and a second adjacent ADC providing third data samples. The ADCs perform an analog-to-digital conversion using a time-interleaving approach in response to clock signals having different phases and including a reference clock signal. A timing calibration circuit includes a relative time skew generator and an absolute time skew generator. A clock generator adjusts at least one phase of the clock signals based on the absolute time skew.

