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(54) SUBSTRATE, PACKAGED STRUCTURE, AND **ELECTRONIC DEVICE**

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(57)ABSTRACT

A substrate, a packaged structure, and an electronic device are provided. The substrate is configured to be electrically connected to a chip. The chip includes a power terminal and a signal terminal. The substrate includes a first substrate and a second substrate mounted on the first substrate. The first substrate includes a first layout, and the first layout is configured to be electrically connected to the power terminal. The second substrate includes a second layout, and the second layout is configured to be electrically connected to the signal terminal. A spacing between lines of the second layout is less than a spacing between lines of the first layout. The substrate provided in this application has a small size and high integration.

