

(19) **United States**

(12) **Patent Application Publication**
Lin et al.

(10) **Pub. No.: US 2022/0393693 A1**

(43) **Pub. Date: Dec. 8, 2022**

(54) **ANALOG-TO-DIGITAL CONVERTER
CAPABLE OF REDUCING NONLINEARITY
AND METHOD OF OPERATING THE SAME**

Publication Classification

(51) **Int. Cl.**
H03M 1/06 (2006.01)
(52) **U.S. Cl.**
CPC **H03M 1/0604** (2013.01)

(71) Applicant: **Realtek Semiconductor Corp.,**
HsinChu (TW)

(72) Inventors: **Kai-Yue Lin**, HsinChu (TW);
Wei-Jyun Wang, HsinChu (TW);
Shih-Hsiung Huang, HsinChu (TW);
Kai-Yin Liu, HsinChu (TW)

(73) Assignee: **Realtek Semiconductor Corp.,**
HsinChu (TW)

(21) Appl. No.: **17/551,104**

(22) Filed: **Dec. 14, 2021**

(30) **Foreign Application Priority Data**

Jun. 4, 2021 (TW) 110120507

(57) **ABSTRACT**

An analog-to-digital converter includes a switch circuit, a first capacitor array, a second capacitor array and a comparator. A method of operating the analog-to-digital converter includes switching a swap signal to a first level in a first sampling period for the switch circuit to couple the first capacitor array to a first input terminal of the comparator and a first signal source, and couple the second capacitor array to a second input terminal of the comparator and a second signal source, and switching the swap signal to a second level in a second sampling period for the switch circuit to couple the first capacitor array to the second input terminal of the comparator and the second signal source, and couple the second capacitor array to the first input terminal of the comparator and the first signal source.

