

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0179916 A1 Matsuno et al.

May 30, 2024 (43) **Pub. Date:**

(54) THREE-DIMENSIONAL MEMORY DEVICE WITH SELF-ALIGNED WORD LINE CONTACT VIA STRUCTURES AND METHOD OF MAKING THE SAME

(71) Applicant: SANDISK TECHNOLOGIES LLC, ADDISON, TX (US)

(72) Inventors: Koichi Matsuno, Fremont, CA (US); Kota Funayama, Yokkaichi (JP)

(21) Appl. No.: 18/221,711

(22) Filed: Jul. 13, 2023

Related U.S. Application Data

(60) Provisional application No. 63/385,089, filed on Nov. 28, 2022.

Publication Classification

(51) Int. Cl. H10B 43/35 (2006.01)H01L 23/522 (2006.01) H01L 23/528 (2006.01)H10B 43/10 (2006.01)H10B 43/27 (2006.01)

(52) U.S. Cl.

CPC H10B 43/35 (2023.02); H01L 23/5226 (2013.01); H01L 23/5283 (2013.01); H10B 43/10 (2023.02); H10B 43/27 (2023.02)

(57)ABSTRACT

A three-dimensional memory device includes an alternating stack of insulating layers and electrically conductive layers having stepped surfaces in a contact region, memory openings vertically extending through the alternating stack, memory opening fill structures located in the memory openings, at least one retro-stepped dielectric material portion overlying the alternating stack, finned dielectric pillar structures vertically extending through the alternating stack in the contact region, support pillar structures, and layer contact via structures vertically extending through the at least one retro-stepped dielectric material portion. Each of the layer contact via structures contacts a respective one of the electrically conductive layers and a respective one of the finned dielectric pillar structures.

