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(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2022/0385242 A1**  
(43) **Pub. Date: Dec. 1, 2022**(54) **LOW NOISE AMPLIFIER TOPOLOGY**(71) Applicant: **Analog Devices, Inc.**, Wilmington, MA (US)(72) Inventors: **Xudong WANG**, Colorado Springs, CO (US); **William B. BECKWITH**, Palmer Lake, CO (US)(73) Assignee: **Analog Devices, Inc.**, Wilmington, MA (US)(21) Appl. No.: **17/689,422**(22) Filed: **Mar. 8, 2022****Related U.S. Application Data**

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**H04B 1/04** (2006.01)(52) **U.S. Cl.**CPC ..... **H03F 1/32** (2013.01); **H03F 3/19** (2013.01); **H04B 1/04** (2013.01); **H03F 2200/294** (2013.01); **H03F 2200/451** (2013.01); **H04B 2001/045** (2013.01)(57) **ABSTRACT**

A low noise amplifier topology can achieve very low noise figure by applying multiple magnetic coupling between gate matching inductors and source degeneration inductor of a field effect transistor. The resulting low noise amplifier has smaller inductors, which can have lower thermal noise contribution, and can maintain good gain and linearity performance. For example, a low noise amplifier includes a first inductor to receive an input; a second inductor coupled to the first inductor in series; a first field effect transistor device whose gate receives a signal from the second inductor; and a third inductor coupled to a source of the first field effect transistor device, where the third inductor is magnetically positively coupled to the first inductor and the second inductor.

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