

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0179852 A1 CHEN et al.

(43) **Pub. Date:** May 30, 2024

- (54) PRINTED CIRCUIT BOARD FOR PHASED ARRAY ANTENNA TRANSCEIVER ASSEMBLY, TRANSCEIVER ASSEMBLY AND **RADAR**
- (71) Applicant: SHENNAN CIRCUITS CO.,LTD., SHENZHEN (CN)
- (72) Inventors: Li CHEN, SHENZHEN (CN); Shuping SHAN, SHENZHEN (CN); Guodong **GUO**, SHENZHEN (CN)
- Appl. No.: 18/433,761
- (22) Filed: Feb. 6, 2024

Related U.S. Application Data

- Continuation of application No. PCT/CN2021/ 142194, filed on Dec. 28, 2021.
- (30)Foreign Application Priority Data

Sep. 1, 2021 (CN) 202111022007.X

Publication Classification

(51) **Int. Cl.** (2006.01)H05K 3/46 G01S 13/02 (2006.01)

H05K 1/02 (2006.01)H05K 3/00 (2006.01)H05K 3/12 (2006.01)

(52) U.S. Cl.

CPC H05K 3/4602 (2013.01); G01S 13/02 (2013.01); H05K 1/0209 (2013.01); H05K 3/0088 (2013.01); H05K 3/1258 (2013.01); G01S 2013/0245 (2013.01); H05K 2201/10098 (2013.01)

(57)ABSTRACT

A printed circuit board (PCB) for a phased array antenna transceiver assembly, a transceiver assembly, and a radar are provided. The PCB includes a microwave core board layer, operating in a first frequency band, where a chip carrying area is arranged on a side of the microwave core board layer, and the chip carrying area is configured to carry a chip; a digital core board layer, operating in a second frequency band and stacked on another side of the microwave core board layer away from the chip carrying area; where an interlayer connecting path is arranged on the microwave core board layer and the digital core board layer, and the interlayer connecting path is connected to the chip carrying area; and a heat dissipation member, embedded in the digital core board layer and connected to the chip carrying area.

