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**WANG et al.**(10) **Pub. No.: US 2022/0352876 A1**(43) **Pub. Date: Nov. 3, 2022**(54) **OSCILLATOR AND CLOCK GENERATION  
CIRCUIT****Publication Classification**(71) Applicant: **CHANGXIN MEMORY  
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(57) **ABSTRACT**

Embodiments of the present application provide an oscillator and a clock generation circuit. The oscillator includes: a first ring topology, including a plurality of first inverters connected end to end, and configured to transmit an oscillation signal at a first transmission speed; and a second ring topology, including a plurality of second inverters connected end to end, and configured to transmit the oscillation signal at a second transmission speed, wherein the present application, the first ring topology is electrically connected to the second ring topology, and the second transmission speed is less than the first transmission speed.

