



US 20240223143A1

(19) **United States**

(12) **Patent Application Publication**
SHI et al.

(10) **Pub. No.: US 2024/0223143 A1**

(43) **Pub. Date: Jul. 4, 2024**

(54) **OFFSET VOLTAGE COMPENSATION
CIRCUIT AND ISOLATION AMPLIFIER
DEVICE INCLUDING THE SAME**

(52) **U.S. Cl.**
CPC *H03F 3/45475* (2013.01); *H03F 3/68*
(2013.01); *H03F 2200/165* (2013.01); *H03F*
2200/375 (2013.01); *H03F 2203/45151*
(2013.01)

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(21) Appl. No.: **18/527,342**

(22) Filed: **Dec. 3, 2023**

(30) **Foreign Application Priority Data**

Dec. 30, 2022 (SG) 10202260647U

Publication Classification

(51) **Int. Cl.**
H03F 3/45 (2006.01)
H03F 3/68 (2006.01)

(57) **ABSTRACT**

An offset voltage compensation circuit and an isolation amplifier device including the same are provided. The offset voltage compensation circuit includes a first amplifier, a second amplifier, a first current source, a second current source, a first resistor and a second resistor. The first amplifier has a first input connected to a first differential input node and a first output connected to a first differential output node. The second amplifier has a first input connected to a second differential input node and a second output connected to a second differential output node. The first current source is connected to the first amplifier for providing a first current. The second current source is connected to the second amplifier for providing a second current. The first current source is controlled according to the second current and a difference between a first initial output voltage and a second initial output voltage.

