

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2023/0230931 A1 Arulalan et al.

(43) Pub. Date:

Jul. 20, 2023

(54) METHOD AND SYSTEM FOR FABRICATING REGROWN FIDUCIALS FOR SEMICONDUCTOR DEVICES

(71) Applicant: **NEXGEN POWER SYSTEMS, INC.**,

Santa Clara, CA (US)

(72) Inventors: Karthik Suresh Arulalan, Santa Clara, CA (US); Jianfeng Wang, Santa Clara, CA (US); Sharlene Wilson, Santa Clara, CA (US); Mark Curtice, Santa Clara, CA (US); Subhash Srinivas Pidaparthi, Santa Clara, CA (US);

> Clifford Drowley, Santa Clara, CA (US)

(73) Assignee: NEXGEN POWER SYSTEMS, INC., Santa Clara, CA (US)

(21) Appl. No.: 18/097,683

(22) Filed: Jan. 17, 2023

Related U.S. Application Data

(60) Provisional application No. 63/300,572, filed on Jan. 18, 2022.

Publication Classification

(51) Int. Cl. H01L 23/544 (2006.01)H01L 21/02 (2006.01)H01L 27/088 (2006.01)

U.S. Cl. CPC H01L 23/544 (2013.01); H01L 21/02639 (2013.01); H01L 21/0262 (2013.01); H01L 21/0254 (2013.01); H01L 21/02642 (2013.01); H01L 27/0886 (2013.01); H01L 2223/54426 (2013.01); H01L 21/3083 (2013.01)

(57)ABSTRACT

A method of forming regrown fiducials includes providing a III-V compound substrate having a device region and an alignment mark region. The III-V compound substrate is characterized by a processing surface. The method also includes forming a hardmask layer having a first set of openings in the device region exposing a first surface portion of the processing surface of the III-V compound substrate and a second set of openings in the alignment mark region exposing a second surface portion of the processing surface and etching the first surface portion and the second surface portion of the III-V compound substrate using the hardmask layer as a mask to form a plurality of trenches. The method also includes epitaxially regrowing a semiconductor layer in the trenches to form the regrown fiducials extending to a predetermined height over the processing surface in the alignment mark region.

