

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2023/0231055 A1 Ramamoorthy et al.

Jul. 20, 2023 (43) **Pub. Date:**

(54) MANGANESE-DOPED PEROVSKITE LAYERS AND SEMICONDUCTOR DEVICE INCORPORATING SAME

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(21) Appl. No.: 18/181,426

(22) Filed: Mar. 9, 2023

Related U.S. Application Data

- (60) Continuation of application No. 17/819,601, filed on Aug. 12, 2022, which is a division of application No. 17/528,054, filed on Nov. 16, 2021, now Pat. No. 11,417,768, which is a continuation of application No. 17/305,301, filed on Jul. 2, 2021, now Pat. No. 11,349,031, which is a continuation of application No. 16/842,593, filed on Apr. 7, 2020, now Pat. No. 11,164,976.
- (60) Provisional application No. 62/831,044, filed on Apr. 8, 2019.

Publication Classification

(51) **Int. Cl.** H01L 29/786 (2006.01)H01L 29/20 (2006.01)

H01L 29/737	(2006.01)
H01L 29/66	(2006.01)
H01L 29/74	(2006.01)
H10B 53/30	(2006.01)

(52) U.S. Cl.

CPC H01L 29/78618 (2013.01); H01L 29/2003 (2013.01); H01L 29/7375 (2013.01); H01L 28/57 (2013.01); H01L 29/6684 (2013.01); H01L 29/7869 (2013.01); H01L 29/7408 (2013.01); H01L 28/65 (2013.01); H01L 28/56 (2013.01); H10B 53/30 (2023.02); H10B 12/36 (2023.02)

(57)**ABSTRACT**

The disclosed technology generally relates to ferroelectric materials and semiconductor devices, and more particularly to semiconductor memory devices incorporating doped polar materials. In one aspect, a semiconductor device comprises a capacitor which in turn comprises a polar layer comprising a base polar material doped with a dopant. The base polar material includes one or more metal elements and one or both of oxygen or nitrogen. The dopant comprises a metal element that is different from the one or more metal elements and is present at a concentration such that a ferroelectric switching voltage of the capacitor is different from that of the capacitor having the base polar material without being doped with the dopant by more than about 100 mV. The capacitor stack additionally comprises first and second crystalline conductive oxide electrodes on opposing sides of the polar layer. The capacitor stack further comprises first and second barrier metal layers on respective ones of the first and second crystalline conductive oxide electrodes on opposing sides of the polar layer.



