



(19) **United States**

(12) **Patent Application Publication**  
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(10) **Pub. No.: US 2024/0223204 A1**

(43) **Pub. Date:** **Jul. 4, 2024**

(54) **RAMP CIRCUIT**

## Publication Classification

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(51) **Int. Cl.**  
*H03M 1/12* (2006.01)  
*H03M 1/00* (2006.01)  
*H03M 1/06* (2006.01)

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(52) U.S. Cl.  
CPC ..... *H03M 1/1255* (2013.01); *H03M 1/002*  
(2013.01); *H03M 1/0668* (2013.01)

(21) Appl. No.: **18/557,619**

(22) PCT Filed: **Apr. 27, 2022**

(86) PCT No.: **PCT/EP2022/061253**

§ 371 (c)(1),

(2) Date: **Oct. 27, 2023**

(30) **Foreign Application Priority Data**

Apr. 30, 2021 (GB) ..... 2106220.3

(57) **ABSTRACT**

Disclosed herein is a ramp circuit for an analogue to digital converter, ADC. The ramp circuit includes a ramp unit configured to provide a ramp signal usable for sampling an analogue signal. The ramp circuit also includes a hold unit connected to the ramp unit, the hold unit is configured to hold a reference voltage for resetting the ramp signal between subsequent samplings of the analogue signal.

