



US 20220368334A1

(19) **United States**(12) **Patent Application Publication**
OSAWA(10) **Pub. No.: US 2022/0368334 A1**(43) **Pub. Date: Nov. 17, 2022**(54) **PHASE SYNCHRONIZATION CIRCUIT,
TRANSMISSION AND RECEPTION CIRCUIT,
AND SEMICONDUCTOR INTEGRATED
CIRCUIT**(52) **U.S. Cl.**CPC *H03L 7/0995* (2013.01); *H03L 7/093*
(2013.01); *H03L 7/18* (2013.01); *H03K 3/023*
(2013.01); *H03L 2207/06* (2013.01)(71) Applicant: **Socionext Inc.**, Kanagawa (JP)(72) Inventor: **Hiromitsu OSAWA**, Kanagawa (JP)(21) Appl. No.: **17/877,481**(22) Filed: **Jul. 29, 2022****Related U.S. Application Data**(63) Continuation of application No. PCT/JP2020/
006858, filed on Feb. 20, 2020.**Publication Classification**(51) **Int. Cl.***H03L 7/099* (2006.01)
H03L 7/093 (2006.01)
H03L 7/18 (2006.01)
H03K 3/023 (2006.01)(57) **ABSTRACT**

A phase synchronization circuit includes: an oscillation circuit that includes a variable current generation unit that generates a variable current of a current amount corresponding to a control voltage and a fixed current generation unit that generates a fixed current of a current amount corresponding to a correction code and generates an output clock signal having a frequency corresponding to the total current amount of the variable current and the fixed current; a feedback circuit that generates a feedback clock signal based on the output clock signal; a control voltage generation circuit that generates the control voltage to make a frequency of the output clock signal become a desired frequency in a normal operation mode; and a correction code generation circuit that generates the correction code in a calibration mode, in which in the calibration mode, the control voltage generation circuit outputs a fixed one of the control voltage.

