



US 20240178832A1

(19) **United States**  
(12) **Patent Application Publication** (10) **Pub. No.: US 2024/0178832 A1**  
Kowkutla et al. (43) **Pub. Date: May 30, 2024**

(54) **ADAPTIVE VOLTAGE SCALING SYSTEM  
FOR OUT OF CONTEXT FUNCTIONAL  
SAFETY SOC**

**Publication Classification**

(51) **Int. Cl.**  
*H03K 17/22* (2006.01)  
*G05B 19/042* (2006.01)  
*G06F 1/24* (2006.01)  
*G06F 1/3296* (2019.01)  
*H03K 17/30* (2006.01)  
(52) **U.S. Cl.**  
CPC ..... *H03K 17/22* (2013.01); *G05B 19/042*  
(2013.01); *G06F 1/24* (2013.01); *G06F*  
*1/3296* (2013.01); *G05B 2219/21119*  
(2013.01); *H03K 17/30* (2013.01)

(71) Applicant: **Texas Instruments Incorporated,**  
Dallas, TX (US)  
(72) Inventors: **Venkateswar Reddy Kowkutla,** Allen,  
TX (US); **Chunhua Hu,** Plano, TX  
(US); **Erkan Bilhan,** Dallas, TX (US);  
**Sumant Dinkar Kale,** Allen, TX (US)

(21) Appl. No.: **18/432,430**

(22) Filed: **Feb. 5, 2024**

**Related U.S. Application Data**

(63) Continuation of application No. 16/912,057, filed on  
Jun. 25, 2020, now Pat. No. 11,923,836, which is a  
continuation of application No. 15/393,513, filed on  
Dec. 29, 2016, now Pat. No. 10,734,993.

(57) **ABSTRACT**

Systems and methods are provided for voltage monitoring  
and reset sequencing. One such system includes a voltage  
detector including multiple voltage level detectors to output  
multiple power OK signals, respectively; a trim adjustment  
circuit to output multiple trim values to the multiple voltage  
level detectors, respectively; and a sequencer circuit coupled  
to the trim adjustment circuit and the voltage detector. In  
response to receiving the power OK signals from the mul-  
tiple voltage level detectors, the sequencer circuit controls  
output of a reset signal to a target voltage domain.

