

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0213990 A1 OCK et al.

Jun. 27, 2024 (43) Pub. Date:

(54) HIGH GAIN LOW POWER PHASE DETECTOR AND LOOP FILTER FOR PHASE LOCK LOOP (PLL)

(71) Applicant: QUALCOMM Incorporated, San Diego, CA (US)

(72) Inventors: Sungmin OCK, San Diego, CA (US); Marzio PEDRALI-NOY, San Diego,

CA (US)

(21) Appl. No.: 18/086,565

(22) Filed: Dec. 21, 2022

Publication Classification

(51) Int. Cl. H03L 7/093 (2006.01)H03L 7/099 (2006.01)

(52) U.S. Cl. CPC H03L 7/093 (2013.01); H03L 7/0992 (2013.01)

(57)ABSTRACT

An apparatus including: a phase lock loop (PLL), including: a phase detector and loop pass filter (PD/LF), including: a phase/frequency detector including a first input configured to receive a reference signal, and a second input configured to receive a feedback signal, and an output configured to produce an output signal based on the reference and feedback signals; a first capacitor; and a charge pump, including: a charging path configured to generate a charging current to charge the first capacitor based on the output signal; and a discharging path including a first resistor configured to discharge the first capacitor.

