



US 20240213140A1

(19) **United States**(12) **Patent Application Publication**  
**THEN et al.**(10) **Pub. No.: US 2024/0213140 A1**(43) **Pub. Date: Jun. 27, 2024**(54) **INTEGRATED CIRCUIT STRUCTURES  
HAVING BACKSIDE HIGH**(22) Filed: **Dec. 24, 2022****Publication Classification**(71) Applicant: **Intel Corporation**, Santa Clara, CA  
(US)(51) **Int. Cl.**  
**H01L 23/522** (2006.01)  
**H01L 23/48** (2006.01)(72) Inventors: **Han Wui THEN**, Portland, OR (US);  
**Marko RADOSAVLJEVIC**, Portland,  
OR (US); **Samuel James BADER**,  
Hillsboro, OR (US); **Ahmad ZUBAIR**,  
Hillsboro, OR (US); **Pratik**  
**KOIRALA**, Portland, OR (US);  
**Michael S. BEUMER**, Portland, OR  
(US); **Heli Chetanbhai VORA**,  
Hillsboro, OR (US); **Ibrahim BAN**,  
Beaverton, OR (US); **Nityan NAIR**,  
Portland, OR (US); **Thomas HOFF**,  
Hillsboro, OR (US)(52) **U.S. Cl.**  
CPC ..... **H01L 23/5223** (2013.01); **H01L 23/481**  
(2013.01); **H01L 23/5226** (2013.01)(57) **ABSTRACT**

Structures having backside high voltage capacitors for front side GaN-based devices are described. In an example, an integrated circuit structure includes a front side structure including a GaN-based device layer, and one or more metallization layers above the GaN-based device layer. A backside structure is below and coupled to the GaN-based layer, the backside structure including metal layers and one or more alternating laterally-recessed metal insulator metal capacitors.

(21) Appl. No.: **18/088,541**