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(54) ELECTROSTATIC DISCHARGE PROTECTION DEVICE

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(57) ABSTRACT

An electrostatic discharge protection device is provided. The electrostatic discharge protection device includes first and second N-type and P-type and third P-type doped regions in the first and second N-type well regions in a P-type semiconductor substrate. First and second P-type well regions are in the first N-type well region. Third N-type, fourth P-type and N-type and fifth P-type doped regions are in the first and second P-type well regions. Fifth N-type and sixth P-type doped regions are in the third P-type well region in the second N-type well region. The first and second N-type doped regions are directly electrically connected to the fifth and sixth P-type doped regions, respectively. The first P-type and fifth N-type, the second P-type and third N-type, and the third P-type doped region and the fourth N-type and P-type doped regions are electrically connected to input/output, power supply and ground terminals.

