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(54) **DISPLAY SUBSTRATE AND DISPLAY DEVICE**

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ABSTRACT

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A display substrate includes a substrate and a plurality of sub-pixels arranged in an array on the substrate. The sub-pixel includes: a data line pattern extending along a first direction; a power signal line pattern, the power signal line pattern including a portion extending along the first direction; and a sub-pixel driving circuit. The sub-pixel driving circuit includes two switching transistors, a driving transistor, and a storage capacitor; a first/second electrode plate of the storage capacitor is coupled to a gate electrode of the driving transistor/the power signal line pattern, second electrodes of the two switching transistors are both coupled to a first electrode of the driving transistor, and orthographic projection of a second electrode of at least one of the two switching transistors on the substrate at least partially overlaps orthographic projection of the power signal line pattern or the second electrode plate on the substrate.

