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Kim et al.(10) **Pub. No.: US 2023/0232660 A1**(43) **Pub. Date: Jul. 20, 2023**(54) **DISPLAY APPARATUS**(71) Applicant: **Samsung Display Co., Ltd.**, Yongin-si (KR)(72) Inventors: **Kyongjun Kim**, Yongin-si (KR); **Yongbin Kim**, Yongin-si (KR); **Sohyun Shin**, Yongin-si (KR); **Gunwoo Yang**, Yongin-si (KR); **Dongjin Lee**, Yongin-si (KR); **Heejin Jeon**, Yongin-si (KR); **Hyunyoung Choi**, Yongin-si (KR)(21) Appl. No.: **17/891,986**(22) Filed: **Aug. 19, 2022**(30) **Foreign Application Priority Data**

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ABSTRACT

A display apparatus includes a first pixel circuit and a second pixel circuit. The first pixel circuit includes a first driving transistor and a first storage capacitor having a first lower storage electrode connected to a gate of the first driving transistor and a first upper storage electrode overlapping the first lower storage electrode. The second pixel circuit includes a second driving transistor and a second storage capacitor including a second lower storage electrode connected to a gate of the second driving transistor and a second upper storage electrode overlapping the second lower storage electrode. A second overlapping area of the second lower storage electrode and the second upper storage electrode is about twice to about four times a first overlapping area of the first lower storage electrode and the first upper storage electrode.

