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(54) **MULTI-VOLTAGE INPUT OUTPUT DEVICE**

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(57) **ABSTRACT**

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Jun. 30, 2021, now Pat. No. 11,424,740, which is a
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Jun. 17, 2020, now Pat. No. 11,057,035, which is a
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May 1, 2018, now Pat. No. 10,511,304.

A device comprises, a first power source providing a first voltage, a second power source providing a second voltage less than the first voltage, a first bias voltage source providing a first bias voltage between the first voltage and the second voltage, a second bias voltage source providing a second bias voltage between the first voltage and the second voltage, the second bias voltage greater than or equal to the first bias voltage. The device also includes an output, a pull up network coupled in series between the first power source and the output pad including: a first gate coupled to the bias voltage source; and a second gate coupled to a signal that varies between first bias voltage and first power source. The device includes and a pull down network coupled between the output pad and second power source and including: a third gate coupled to the second bias voltage source; and a fourth gate coupled to a signal that varies between the second power source and the second bias voltage source.

Foreign Application Priority Data

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