



(10) Pub. No.: US 2024/0237401 A9
(48) Pub. Date: Jul. 11, 2024
CORRECTED PUBLICATION

Publication Classification

(51) **Int. Cl.**
H10K 59/121 (2006.01)
H01L 27/12 (2006.01)

(52) **U.S. Cl.**
CPC **H01L 27/3262** (2013.01); **H01L 27/1222**
(2013.01); **H01L 2227/323** (2013.01)

(57) **ABSTRACT**

A display panel includes a substrate, an active layer, a first metal layer and a second metal layer. The active layer is disposed on the substrate and includes a plurality of the driving active portions, and each driving active portion comprises a conductor sub-layer and a first semiconductor sub-layer. The first metal layer is disposed on a side of the active layer away from the substrate and includes a plurality of the driving gates, one of the plurality of the driving gates is correspondingly located on a side of the driving active portion away from the substrate. The second metal layer is disposed on a side of the first metal layer away from the active layer and includes a plurality of the driving source electrodes and a plurality of the driving drain electrodes. The conductor sub-layer includes a first conductor sub-portion and a second conductor sub-portion arranged at intervals.

(22) Filed: **Nov. 25, 2022**

Prior Publication Data

(65) US 2024/0138186 A1 Apr. 25, 2024

(30) **Foreign Application Priority Data**

Oct. 25, 2022 (CN) 202211310327.X

