



US 20240213940A1

(19) **United States**(12) **Patent Application Publication**
Manetakis(10) **Pub. No.: US 2024/0213940 A1**(43) **Pub. Date: Jun. 27, 2024**(54) **ELECTRIC CIRCUIT PROVIDING MIXING
AND GAINING FUNCTIONS FOR A RF
RECEIVER FRONT-END AND RF RECEIVER
FRONT-END**(52) **U.S. Cl.**CPC ... **H03F 3/45475** (2013.01); **H03F 2200/129**
(2013.01); **H03F 2200/171** (2013.01); **H03F**
2200/294 (2013.01); **H03F 2200/451** (2013.01)(71) Applicant: **CSEM Centre Suisse d'Electronique
et de Microtechnique SA Recherche
et Développement**, Neuchâtel (CH)(72) Inventor: **Konstantinos Manetakis**, Neuchâtel
(CH)(21) Appl. No.: **18/543,264**(22) Filed: **Dec. 18, 2023**(30) **Foreign Application Priority Data**

Dec. 23, 2022 (EP) 22216554.0

Publication Classification(51) **Int. Cl.**
H03F 3/45 (2006.01)

(57)

ABSTRACT

An electric circuit providing mixing and gaining functions for a RF receiver front-end, including an I path and a Q path. Each of the I path and Q path includes: a switch, an input capacitor between the switch and an input of an operational amplifier, and a feedback capacitor between the input and the output of the operational amplifier. The electric circuit includes: a local oscillator arranged to generate a carrier signal having a carrier frequency, a command module arranged to sequentially close each switch during a mixer's averaging window, so that an incoming signal having a frequency close to the carrier frequency results in a non-zero down-converted signal across the input capacitor. This down-converted signal is amplified by the operational amplifier so that an amplified down-converted signal appears at the output of the operational amplifier. A RF front-end is inductor-less, includes a linearity optimized LNA, and/or has reconfigurable topology.

