

# (19) United States

## (12) Patent Application Publication (10) Pub. No.: US 2024/0178835 A1 CURINA et al.

May 30, 2024 (43) Pub. Date:

#### (54) ISOLATED DRIVER DEVICE, CORRESPONDING ELECTRONIC SYSTEM AND METHOD OF TRANSMITTING A DATA SIGNAL ACROSS A GALVANIC ISOLATION **BARRIER**

- (71) Applicant: STMICROELECTRONICS S.r.l., Agrate Brianza (IT)
- Inventors: Carlo CURINA, Bareggio (IT); Valerio BENDOTTI, Vilminore di Scalve (IT)
- Assignee: STMICROELECTRONICS S.r.l., Agrate Brianza (IT)
- Appl. No.: 18/508,011

(30)

- Filed: Nov. 13, 2023 (22)
- Nov. 25, 2022 (IT) ...... 102022000024381

Foreign Application Priority Data

### **Publication Classification**

Int. Cl. (51)H03K 17/605 (2006.01)H03K 3/037 (2006.01)H03K 5/133 (2006.01)H03K 19/096 (2006.01)

U.S. Cl. H03K 17/605 (2013.01); H03K 3/037 CPC ..... (2013.01); H03K 5/133 (2013.01); H03K

**19/096** (2013.01)

#### (57)ABSTRACT

In an electronic device, a pulse generator receives an input signal and a clock signal and produces a transmission signal that includes a pulse following each edge of the input signal and of the clock signal. The pulse is low when the input signal is low and high when the input signal is high. A transmitter produces, at its two output nodes, a replica of the transmission signal and the complement of the transmission signal. A galvanic isolation barrier is coupled to the output nodes of the transmitter and produces a differential signal that includes a positive spike at each rising edge of the transmission signal and a negative spike at each falling edge of the transmission signal.

