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(54) **SEMICONDUCTOR DEVICE**

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(57)ABSTRACT

A semiconductor device includes a gate electrode structure including first to fourth gate electrodes, a first memory channel structure extending through the first to third gate electrodes, a second memory channel structure contacting an upper surface of the first memory channel structure and extending through the fourth gate electrode, and a first contact plug including a lower portion extending partially through the gate electrode structure and an upper portion on and contacting an upper surface of the lower portion. The lower portion of the first contact plug has a varying width, and the upper portion of the first contact plug has a width gradually increasing from a bottom toward a top thereof. The lower portion of the first contact plug extends through the first, second and third gate electrodes, and is electrically insulated from the first and second gate electrodes, and is electrically connected to the third gate electrode.

