



US 20230230905A1

(19) **United States**  
(12) **Patent Application Publication**  
**Luniewski et al.**

(10) **Pub. No.: US 2023/0230905 A1**  
(43) **Pub. Date: Jul. 20, 2023**

(54) **STACKED MODULE ARRANGEMENT****H01R 12/70**

(2006.01)

(71) Applicant: **Infineon Technologies Austria AG,**  
Villach (AT)(52) **U.S. Cl.**  
CPC ..... **H01L 23/49568** (2013.01); **H01L 23/055**  
(2013.01); **H01L 23/3114** (2013.01); **H01R**  
**12/585** (2013.01); **H01R 12/7064** (2013.01);  
**H05K 2201/1059** (2013.01)(72) Inventors: **Peter Luniewski**, Poing (DE); **Ivan Nikitin**, Regensburg (DE); **Bernd Schmoelzer**, Radentheim (AT)(21) Appl. No.: **17/577,879**(57) **ABSTRACT**(22) Filed: **Jan. 18, 2022****Publication Classification**(51) **Int. Cl.**  
**H01L 23/495** (2006.01)  
**H01L 23/055** (2006.01)  
**H01L 23/31** (2006.01)  
**H01R 12/58** (2006.01)

A stacked module arrangement includes: a first molded electronic module; a second molded electronic module; and an interface by which the first molded electronic module and the second molded electronic module are physically and electrically connected to one another in a stacked configuration. The first molded electronic module is a power electronic module having a maximum breakdown voltage of at least 40 V and a maximum DC current of at least 10 A.

