

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2022/0369452 A1 PRITCHARD et al.

Nov. 17, 2022 (43) **Pub. Date:**

(54) SLOTTED VIAS FOR CIRCUIT BOARDS

(71) Applicant: Dell Products L.P., Round Rock, TX

(72) Inventors: Jason PRITCHARD, Hopkinton, MA (US); Charles W. ZIEGLER, IV,

Framingham, MA (US); Qianwen WANG, Shanghai (CN); Lingyu KONG, Shanghai (CN)

(73) Assignee: Dell Products L.P., Round Rock, TX

(US)

(21) Appl. No.: 17/319,577

(22) Filed: May 13, 2021

Publication Classification

(51) Int. Cl. H05K 1/02

(2006.01)

U.S. Cl. (52)

> CPC . H05K 1/0222 (2013.01); H05K 2201/09636 (2013.01); H05K 2201/09618 (2013.01)

(57)**ABSTRACT**

A circuit board may include a traditional via electrically coupled to a first layer of the circuit board and coupled to a second layer of the circuit board and a slotted via formed within the circuit board proximate to the traditional via, the slotted via comprising an opening through a first surface and a second surface of the circuit board and a layer of conductive material formed on interior walls of the opening.

