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JINGU(10) **Pub. No.: US 2022/0416791 A1**(43) **Pub. Date: Dec. 29, 2022**(54) **LEVEL SHIFTER CIRCUIT**(52) **U.S. Cl.**(71) Applicant: **SONY SEMICONDUCTOR
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(JP)**(57) **ABSTRACT**(21) Appl. No.: **17/756,676**(22) PCT Filed: **Nov. 13, 2020**(86) PCT No.: **PCT/JP2020/042396**

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Provided is a level shifter circuit that changes a voltage of a high-frequency input signal to output.

Provided is a level shifter circuit provided with a first input terminal and a second input terminal to each of which an input signal having a level between a first potential level and a first reference potential level is input, a first output terminal and a second output terminal from each of which an output signal having a level between a second potential level higher than the first potential level and a second reference potential level is output, a second potential supply node that supplies a voltage at the second potential level, a reference potential supply node that supplies a voltage at the second reference potential level, first and second impedance elements, first to fourth transistors, and first and second nodes, in which each of the first impedance element and the second impedance element includes at least three terminals.

