



US 20240213270A1

(19) **United States**

(12) **Patent Application Publication**
GUO et al.

(10) **Pub. No.: US 2024/0213270 A1**

(43) **Pub. Date: Jun. 27, 2024**

(54) **ARRAY SUBSTRATE, DISPLAY PANEL AND
DISPLAY DEVICE**

Publication Classification

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(51) **Int. Cl.**
H01L 27/12 (2006.01)

(52) **U.S. Cl.**
CPC **H01L 27/124** (2013.01)

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(21) Appl. No.: **18/599,753**

(22) Filed: **Mar. 8, 2024**

Related U.S. Application Data

(63) Continuation of application No. PCT/CN2022/
115449, filed on Aug. 29, 2022.

(30) **Foreign Application Priority Data**

Jun. 28, 2022 (CN) 202210751702.8

(57) **ABSTRACT**

An array substrate includes a substrate, an active layer, multiple metal layers and pixel circuits. A pixel circuit of the multiple pixel circuits includes a drive transistor, a first initialization transistor, and a second initialization transistor. The multiple metal layers include a first metal wire extending in a first direction and a second metal wire extending in a second direction. The first metal wire and the second metal wire are located in different layers. The first initialization transistor is connected between the first metal wire and a gate of the drive transistor and is configured to transmit a first initialization voltage on the first metal wire to the gate of the drive transistor. The second initialization transistor is connected between the second metal wire and a light-emitting element and is configured to transmit a second initialization voltage on the second metal wire to the light-emitting element.

