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(11) **Aminaga et al.**(54) **AMPLIFYING CIRCUIT AND VOLTAGE  
GENERATING CIRCUIT**(71) Applicant: **ROHM CO., LTD.**, Kyoto (JP)(72) Inventors: **Hiroyuki Aminaga**, KYOTO (JP); **Koji  
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**1/468** (2013.01)(57) **ABSTRACT**

The present disclosure relates to an amplifying circuit and a voltage generating circuit. The amplifying circuit includes: an operational amplifier, including a first input terminal, a second input terminal and an output terminal, and configured to be capable of outputting an output voltage corresponding to an input voltage from the output terminal to the first input terminal; a voltage dividing circuit, including a series circuit of a plurality of voltage dividing resistors disposed between the output terminal and a predetermined potential terminal, wherein the series circuit includes a feedback node connected to the second input terminal and a correction node different from the feedback node; and a correction circuit, including a diode inserted between the correction node and the predetermined potential terminal.

