



(12) **Patent Application Publication**  
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(43) **Pub. Date:** **Jun. 27, 2024**

CPC ..... ***H03K 5/1252*** (2013.01)

(57)

## ABSTRACT

An electric circuit and a method for filtering glitches are described. The electric circuit includes a filter, an inverter circuit, and a reset circuit. The inverter circuit is electrically coupled to an output of the filter. The reset circuit is electrically coupled to the output of the filter. The reset circuit pulls the output of the filter high when an input signal to the electric circuit and the output of the inverter circuit are both low, pulls the output of the filter low when the input signal to the electric circuit and the output of the inverter circuit are both high, and passes the output of the filter when (i) the input signal to the electric circuit is high and the output of the inverter circuit is low or (ii) the input signal to the electric circuit is low and the output of the inverter circuit is high.

(22) Filed: **Dec. 22, 2022**

## Publication Classification

**H03K 5/1252**

(2006.01)

