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Applicant: Samsung Electronics Co., Ltd.,

Suwon-si (KR)

Inventors: Jihyun LEE, Suwon-si (KR);

Yongsung PARK, Suwon-si (KR)

(73)Assignee: Samsung Electronics Co., Ltd.,

Suwon-si, Gyeonggi-do (KR)

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ABSTRACT (57)

A semiconductor package includes a wiring substrate, a solder ball on a lower surface of the wiring substrate, a ball land between the lower surface of the wiring substrate and the solder ball and having an upper surface having a circular shape, and a mask layer covering the lower surface of the wiring substrate and including an opening through which a portion of the ball land is exposed. The ball land includes a first land region which is exposed via the opening and has an upper surface having a semicircular shape with a first radius and a second land region which is integrated with a flat side surface of the first land region and has an upper surface having a semicircular shape with a second radius that is less than the first radius.

