



US 20240213970A1

(19) **United States**(12) **Patent Application Publication**
YOSHIOKA et al.(10) **Pub. No.: US 2024/0213970 A1**(43) **Pub. Date: Jun. 27, 2024**(54) **APPARATUS FOR INPUTTING SIGNAL
FROM COMMUNICATION LINE AND
METHOD THEREOF****Publication Classification**

(51) **Int. Cl.**
H03K 5/24 (2006.01)
H03K 3/037 (2006.01)
(52) **U.S. Cl.**
CPC *H03K 5/24* (2013.01); *H03K 3/037*
(2013.01)

(71) Applicant: **Asahi Kasei Microdevices
Corporation, Tokyo (JP)**(72) Inventors: **Tomohiro YOSHIOKA, Tokyo (JP);
Yoshitaka OKUTSU, Tokyo (JP)**(21) Appl. No.: **18/391,720**(22) Filed: **Dec. 21, 2023**(30) **Foreign Application Priority Data**

Dec. 27, 2022 (JP) 2022-209279

(57) **ABSTRACT**

Provided is an apparatus including: a first input circuit which inputs signal from a first communication line; a second input circuit which inputs a signal from a second communication line; a reference voltage generation circuit which generates a reference voltage; and a comparison circuit which compares the reference voltage with a voltage of the first communication line. The first input circuit and the second input circuit adjust judgement thresholds to be used for judging a signal value according to the comparison result obtained by the comparison circuit.

