

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0237554 A1 Lin et al.

Jul. 11, 2024 (43) **Pub. Date:**

(54) SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

(71) Applicant: UNITED MICROELECTRONICS CORP., Hsin-Chu City (TW)

(72) Inventors: **Hung-Chan Lin**, Tainan City (TW); Yu-Ping Wang, Hsinchu City (TW); Chien-Ting Lin, Tainan City (TW)

(73) Assignee: UNITED MICROELECTRONICS CORP., Hsin-Chu City (TW)

(21) Appl. No.: 18/611,729

(22) Filed: Mar. 21, 2024

Related U.S. Application Data

(63) Continuation of application No. 17/500,971, filed on Oct. 14, 2021, now Pat. No. 11,968,910.

(30)Foreign Application Priority Data

Sep. 13, 2021 (CN) 202111067661.2

Publication Classification

(51) Int. Cl. H10N 52/01 (2006.01)H10B 61/00 (2006.01)H10N 52/00 (2006.01)H10N 52/80 (2006.01)

(52) U.S. Cl. CPC H10N 52/01 (2023.02); H10B 61/00 (2023.02); H10N 52/00 (2023.02); H10N 52/80 (2023.02)

(57)ABSTRACT

A semiconductor device includes a magnetic tunneling junction (MTJ) on a substrate, a first spin orbit torque (SOT) layer on the MTJ, a spacer adjacent to the MTJ and the first SOT layer, and a second SOT layer on the first SOT layer. Preferably, the first SOT layer and the second SOT layer are made of same material.

