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(54) DEADLOCK RECOVERY CIRCUIT AND DEADLOCK RECOVERY METHOD, AND PLL CIRCUIT INCLUDING THE SAME

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(57)ABSTRACT

The present disclosure relates to a deadlock recovery unit for applying a one-shot pulse signal to restart a Voltage Controlled Oscillator (VCO) when the output clock signal of the VCO remains in a high state or a low state. The deadlock recovery unit includes a VCO clock monitoring unit for monitoring a clock signal of a voltage control oscillator; a pulse signal generating unit for outputting a one-shot pulse signal to reset the VCO when a clock signal is not counted; and a control signal generating unit for generating a VCO counter enable signal applied to the VCO clock monitoring unit and a detector clock signal applied to the pulse signal generating unit by using an externally supplied reference clock signal.

