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(54) **AD CONVERTER**

(71) Applicant: **Asahi Kasei Microdevices Corporation**, Tokyo (JP)

(72) Inventors: **Eizo ICHIHARA**, Tokyo (JP); **Shintaro KAWAZOE**, Tokyo (JP)

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(57) **ABSTRACT**

Provided is an AD converter, including: an analog signal input circuit, configured to be input with an analog input signal, and output a first analog output signal based on the analog input signal and a second analog output signal based on the analog input signal at different timing; an integral circuit, configured to integrate the first analog output signal and the second analog output signal and output the first integral signal and the second integral signal; a predictive circuit, configured to predict an integral signal output after the output by the integral circuit based on the first integral signal and the second integral signal output by the integral circuit, and output a predictive integral signal; and a quantization circuit, configured to generate a digital signal with the predictive integral signal quantized.