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(54) **DEVICE AND METHOD FOR DETECTING A HARMONIC STATE**

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(57) **ABSTRACT**

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Detection of harmonic states in a delay-locked loop. A plurality of delayed clock signals within the delay-locked loop is evaluated and, on the basis thereof, in particular on the basis of the time points of the rising edges in the delayed clock signals, it is ascertained whether a harmonic state has arisen in the delay-locked loop.

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