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(54) **DISPLAY PANEL**

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A display panel includes a base layer including a display area and a non-display area adjacent to the display area, a lower electrode on the base layer and overlapping the display area, a driving voltage line on the base layer and overlapping the non-display area, a pixel defining layer on the base layer, covering a portion of the lower electrode, and defining a light emitting opening, a light emitting pattern within the light emitting opening and on the lower electrode, a partition wall on the pixel defining layer and the driving voltage line and defining an upper opening corresponding to the light emitting opening, and an upper electrode on the light emitting pattern and in contact with an inner surface of the partition wall defining the upper opening, wherein the driving voltage line is in contact with the partition wall.

