

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2022/0407513 A1 CRETU et al.

Dec. 22, 2022 (43) **Pub. Date:**

(54) SAMPLING SWITCH CIRCUITS

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Appl. No.: 17/834,653

Jun. 7, 2022 (22)Filed:

(30)Foreign Application Priority Data

Jun. 18, 2021 (EP) 21180456.2

Publication Classification

(51) Int. Cl. H03K 17/687 (2006.01) (52) U.S. Cl.

CPC H03K 17/6872 (2013.01)

ABSTRACT (57)

A sampling switch circuit, including an input node, which receives an input voltage signal to be sampled, a sampling transistor having gate, source and drain terminals, the source terminal connected to the input node, a capacitor, a current source configured to cause a defined current to flow therethrough and switching circuitry configured to alternate between a precharge configuration and an output configuration depending upon a clock signal. In the precharge configuration, the switching circuitry connects the capacitor into a current path between said current source and a first voltage reference node to form a potential difference across the capacitor which is dependent on the defined current. In the output configuration, the switching circuitry connects the capacitor between a second voltage reference node and the gate terminal of the sampling transistor so that a voltage level applied at the gate terminal of the sampling transistor is dependent on the defined current.





