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### (54) POWER CONVERSION APPARATUS AND SYNCHRONOUS RECTIFICATION CONTROLLER THEREOF

- (71) Applicant: Power Forest Technology Corporation, Hsinchu County (TW)
- Inventor: Chia-Hsien Liu, Chiayi County (TW)
- Assignee: Power Forest Technology Corporation, Hsinchu County (TW)
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#### (57)ABSTRACT

A power conversion apparatus and a synchronous rectification (SR) controller thereof are provided. An open-loop control circuit outputs a clamp voltage as a driving voltage when a drain voltage of a synchronous rectification transistor rises to a second voltage, so as to quickly pull down the driving voltage and maintain the driving voltage at a clamp voltage. Therefore, a second control circuit may quickly turn off the synchronous rectification transistor when the drain voltage is greater than a third voltage.

