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(57) **ABSTRACT**

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(63) Continuation of application No. 17/865,659, filed on Jul. 15, 2022, now Pat. No. 11,653,018, which is a continuation of application No. 17/130,298, filed on Dec. 22, 2020, now Pat. No. 11,425,409, which is a continuation of application No. 16/597,356, filed on Oct. 9, 2019, now Pat. No. 10,911,770, which is a continuation of application No. PCT/JP2018/014363, filed on Apr. 4, 2018.

(60) Provisional application No. 62/485,072, filed on Apr. 13, 2017.

A decoder that decodes a current block using a motion vector includes: a processor; and memory. Using the memory, the processor: derives a first candidate vector from one or more candidate vectors of one or more neighboring blocks that neighbor the current block; determines, in a first reference picture for the current block, a first adjacent region that includes a position indicated by the first candidate vector; calculates evaluation values of a plurality of candidate regions included in the first adjacent region; and determines a first motion vector of the current block, based on a first candidate region having a smallest evaluation value among the evaluation values. The first adjacent region is included in a first motion estimation region determined based on the position indicated by the first candidate vector.

