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**TAHARA et al.**(10) **Pub. No.: US 2022/0368284 A1**(43) **Pub. Date: Nov. 17, 2022**(54) **POWER AMPLIFICATION CIRCUIT,  
RADIO-FREQUENCY CIRCUIT, AND  
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(2013.01); **H03F 2200/451** (2013.01)(57) **ABSTRACT**

A current flowing through a transistor of a final-stage amplifier is suppressed. A power amplification circuit includes a driving-stage amplifier, a final-stage amplifier, a power supply terminal, a first voltage control circuit, and a second voltage control circuit. The driving-stage amplifier includes a first transistor having a first input terminal, a first output terminal, and a first ground terminal. The final-stage amplifier includes a second transistor having a second input terminal, a second output terminal, and a second ground terminal. The first voltage control circuit is connected between the power supply terminal and the first output terminal, and controls a first power supply voltage applied to the first transistor. The second voltage control circuit is connected between the power supply terminal and the second output terminal, and controls a second power supply voltage applied to the second transistor.

