

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0244849 A1

Jul. 18, 2024 (43) **Pub. Date:**

(54) SEMICONDUCTOR DEVICE AND A METHOD FOR MANUFACTURING THE

(71) Applicant: MACRONIX INTERNATIONAL CO., LTD., Hsinchu (TW)

Inventors: Yu-Yu LIN, New Taipei City (TW); Feng-Min LEE, Hsinchu City (TW)

Appl. No.: 18/153,376

(22) Filed: Jan. 12, 2023

Publication Classification

(51) **Int. Cl.** (2006.01)H10B 63/00 H10N 70/00 (2006.01)H10N 70/20 (2006.01) (52) U.S. Cl. CPC H10B 63/80 (2023.02); H10N 70/011 (2023.02); H10N 70/24 (2023.02); H10N 70/826 (2023.02)

(57)ABSTRACT

A semiconductor device includes a resistor. The resistor includes two bottom electrodes adjacent to each other, a resistive layer, a top electrode and a conductive sidewall. The resistive layer is disposed on the two bottom electrodes. The top electrode is disposed on the resistive layer. The conductive sidewall surrounds the top electrode and is electrically connected to the top electrode and a bottom electrode of the two bottom electrodes. The top electrode overlaps the two bottom electrodes in the first direction, and extends above the two bottom electrodes along a second direction different from the first direction.

