

US 20240235491A

## (19) United States

# (12) Patent Application Publication TAKEZONO et al.

# (10) **Pub. No.: US 2024/0235491 A9**

# (48) **Pub. Date: Jul. 11, 2024 CORRECTED PUBLICATION**

#### (54) POWER AMPLIFIER CIRCUIT

(71) Applicant: Murata Manufacturing Co., Ltd.,

Nagaokakyo-shi (JP)

(72) Inventors: Naofumi TAKEZONO,

Nagaokakyo-shi (JP); **Shunji YOSHIMI**, Nagaokakyo-shi (JP)

(73) Assignee: Murata Manufacturing Co., Ltd.,

Nagaokakyo-shi (JP)

(21) Appl. No.: 18/403,741

(22) Filed: Jan. 4, 2024

#### **Prior Publication Data**

- (15) Correction of US 2024/0136981 A1 Apr. 25, 2024 See (30) Foreign Application data
- (65) US 2024/0136981 A1 Apr. 25, 2024

### Related U.S. Application Data

(63) Continuation of application No. PCT/JP2022/ 032045, filed on Aug. 25, 2022.

## (30) Foreign Application Priority Data

Aug. 26, 2021 (JP) ...... 2021-138100

#### **Publication Classification**

(51) Int. Cl.

*H03F 1/26* (2006.01) *H03F 3/24* (2006.01)

(52) U.S. Cl.

### (57) ABSTRACT

A power amplifier circuit includes a first amplifier, a second amplifier, a third amplifier, and a harmonic suppression circuit. The first amplifier operates on power supplied through a first supply line, and amplifies a first transmit signal in a first frequency band. The second amplifier operates on power supplied through a second supply line connected to the first supply line, and amplifies a second transmit signal in a second frequency band different from the first frequency band. The third amplifier shares an antenna with the second amplifier, and amplifies a receive signal in the second frequency band received from the antenna. The harmonic suppression circuit generates, based on a harmonic of the first transmit signal, a suppression signal to suppress the harmonic to be transferred to the first supply line, and outputs the suppression signal to the first supply line or the second supply line.

