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(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2022/0416751 A1**
(43) **Pub. Date: Dec. 29, 2022**(54) **IMPEDANCE MATCHING CIRCUIT AND AN IMPEDANCE MATCHING ELEMENT**(57) **ABSTRACT**(71) Applicant: **Intel Corporation**, Santa Clara, CA (US)(72) Inventors: **Ritesh A. BHAT**, Hillsboro, OR (US);
Woorim SHIN, Portland, OR (US)(21) Appl. No.: **17/356,530**(22) Filed: **Jun. 24, 2021****Publication Classification**(51) **Int. Cl.****H03H 7/38** (2006.01)**H03H 7/42** (2006.01)(52) **U.S. Cl.**CPC **H03H 7/38** (2013.01); **H03H 7/42** (2013.01)

Impedance matching circuits, impedance matching elements, and radio communication circuits are provided in this disclosure. The impedance matching circuit may include a first impedance matching element which is configured to receive an unbalanced input signal from a first input, and couple the unbalanced input signal to a first output to match an impedance of the first output to a first impedance. It may further include a second impedance matching element coupled to the first input to receive the unbalanced input signal, the second impedance matching element configured to couple the unbalanced input signal to a second output to match an impedance of the second output to a second impedance. A terminal of the first output and a terminal of the second output may be coupled to provide a balanced output signal, and the coupling may match an output impedance of the impedance matching circuit based on the first impedance and the second impedance.

