

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2022/0354001 A1 HEALY

Nov. 3, 2022 (43) Pub. Date:

(54) SYSTEM AND METHOD FOR HIGH-TEMPERATURE LAMINATION OF PRINTED CIRCUIT BOARDS

(71) Applicant: Matrix Electronics Limited,

Mississauga (CA)

(72) Inventor: Kieran HEALY, Mississauga (CA)

Assignee: Matrix Electronics Limited,

Mississauga (CA)

Appl. No.: 17/868,896

(22) Filed: Jul. 20, 2022

Related U.S. Application Data

Continuation of application No. 17/177,733, filed on Feb. 17, 2021, now Pat. No. 11,445,619, which is a continuation-in-part of application No. 17/082,091, filed on Oct. 28, 2020.

Publication Classification

(51) Int. Cl.

H05K 3/46 (2006.01)B32B 37/26 (2006.01)B32B 37/12 (2006.01)

(52)U.S. Cl.

> H05K 3/4635 (2013.01); B32B 37/26 CPC (2013.01); B32B 37/12 (2013.01); B32B 2037/268 (2013.01); H05K 2203/068 (2013.01); B32B 38/162 (2013.01)

(57)ABSTRACT

A press pad for use in lamination of multi-layer circuit boards is disclosed. The press pad includes: a planar pad having a first surface and a second surface opposite to the first surface; and a sheet of release film coupled to the first surface, wherein the release film sheet is coupled to the first surface using an acrylic-based adhesive containing thermoplastic polyolefin and methyl acrylate, and wherein the release film sheet comprises polyimide-based film.

