



US 20220407507A1

(19) **United States**(12) **Patent Application Publication**
WEI et al.(10) **Pub. No.: US 2022/0407507 A1**(43) **Pub. Date: Dec. 22, 2022**(54) **CLOCK SIGNAL GENERATION CIRCUIT,
METHOD FOR GENERATING CLOCK
SIGNAL AND ELECTRONIC DEVICE**(52) **U.S. Cl.**CPC *H03K 5/249* (2013.01); *H03K 5/26*
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ABSTRACT(72) Inventors: **Xiangye WEI**, Beijing (CN); **Liming
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A clock signal generation circuit and method, and an electronic device are provided, relating to the field of communications technology. In the clock signal generation circuit, an initial clock providing circuit (10) can generate an initial clock signal having an initial frequency; a control word providing circuit (20) can determine a target frequency offset of the initial frequency based on a detected target parameter and generate a frequency control word based on the target frequency offset; a target clock generating circuit (30) can generate a target clock signal having a target output frequency based on the frequency control word and the initial clock signal, wherein the target output frequency is negatively correlated with the frequency control word and positively correlated with the initial frequency. It can be learned based on a relationship among the target output frequency and the initial frequency and the frequency control word that flexibly generating the frequency control word can reduce the impact of the target parameter on the frequency of the clock signal finally generated by the clock signal generation circuit.

(21) Appl. No.: **17/765,933**(22) PCT Filed: **Mar. 9, 2021**(86) PCT No.: **PCT/CN2021/079743**

§ 371 (c)(1),

(2) Date: **Apr. 1, 2022**(30) **Foreign Application Priority Data**

Apr. 3, 2020 (CN) 202010260304.7

Publication Classification(51) **Int. Cl.***H03K 5/24* (2006.01)*H03K 5/26* (2006.01)*H03K 3/011* (2006.01)