

# (19) United States

## (12) Patent Application Publication (10) Pub. No.: US 2024/0215344 A1 Feng et al.

(43) **Pub. Date:** 

Jun. 27, 2024

### (54) ARRAY SUBSTRATE AND DISPLAY **APPARATUS**

(71) Applicant: BOE Technology Group Co., Ltd.,

Beijing (CN)

(72) Inventors: Yu Feng, Beijing (CN); Libin Liu,

Beijing (CN); Jiangnan Lu, Beijing

(73) Assignee: BOE Technology Group Co., Ltd.,

Beijing (CN)

(21) Appl. No.: 17/788,605

(22) PCT Filed: Aug. 30, 2021

(86) PCT No.: PCT/CN2021/115249

§ 371 (c)(1),

(2) Date: Jun. 23, 2022

#### (30)Foreign Application Priority Data

Mar. 11, 2021 (WO) ...... PCT/CN2021/080326

### **Publication Classification**

(51) Int. Cl.

H10K 59/131 (2023.01)G09G 3/32 (2016.01)

(52) U.S. Cl.

(2013.01); G09G 2300/0426 (2013.01); G09G 2300/0819 (2013.01); G09G 2300/0842 (2013.01); G09G 2320/0233 (2013.01); G09G 2320/0247 (2013.01)

#### (57)ABSTRACT

An array substrate is provided. A respective pixel driving circuit of the array substrate includes a driving transistor, a storage capacitor, and a transistor having a gate electrode connected to a respective second gate line of a plurality of second gate lines, a first electrode connected to a first capacitor electrode of the storage capacitor, and a second electrode connected to a second electrode of a first reset transistor, the transistor being configured to receive a reset signal through the first reset transistor. An active layer of the driving transistor and an active layer of the transistor are spaced apart from each other by at least an insulating layer. The active layer of the driving transistor comprises a first semiconductor material. The active layer of the transistor comprises a second semiconductor material different from the first semiconductor material.

