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(54) ARRAY SUBSTRATE AND DISPLAY PANEL

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(57) ABSTRACT

An array substrate and a display panel are provided. A driving circuit layer of the array substrate includes a low temperature polysilicon thin film transistor and a low temperature polycrystalline oxide thin film transistor which are electrically connected with each other. A hydrogen blocking layer is formed on at least one of an upper side and a lower side of an oxide active layer of the low temperature polysilicon thin film transistor. The hydrogen blocking layer can block hydrogen ions in other film layers to invade the oxide active layer and avoid that device characteristics are drifted.

