



US 20220386471A1

(19) **United States**(12) **Patent Application Publication**
Ozawa(10) **Pub. No.: US 2022/0386471 A1**(43) **Pub. Date: Dec. 1, 2022**(54) **COMPUTER-READABLE RECORDING
MEDIUM STORING DESIGN PROGRAM,
DESIGN METHOD, AND PRINTED WIRING
BOARD****H05K 1/02** (2006.01)**G06F 30/394** (2006.01)(52) **U.S. CL.**CPC **H05K 3/0005** (2013.01); **H05K 1/115**
(2013.01); **H05K 1/0228** (2013.01); **G06F**
30/394 (2020.01); **G06F 2115/12** (2020.01)(71) Applicant: **FUJITSU LIMITED**, Kawasaki-shi
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(JP)(21) Appl. No.: **17/683,741**(22) Filed: **Mar. 1, 2022**(30) **Foreign Application Priority Data**

Jun. 1, 2021 (JP) 2021-092128

Publication Classification(51) **Int. Cl.****H05K 3/00** (2006.01)**H05K 1/11** (2006.01)(57) **ABSTRACT**

A design program for causing a computer to execute a process including: selecting, based on design data of a printed wiring board, a first transmission line and a second transmission line among transmission lines provided in the printed wiring board; adjusting a first wiring length between a first via in the first transmission line and a third via in the first transmission line, a second wiring length between a second via in the second transmission line and a fourth via in the second transmission line, a length of the first via, a length of the second via, a length of the third via, or a length of the fourth via such that a phase of first crosstalk noise generated between the first via and the second via is inverted between the third via and the fourth via; and outputting the design data corrected based on the adjustment in the board.

