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(19) **United States**(12) **Patent Application Publication****Ishizuya et al.**(10) **Pub. No.: US 2024/0251590 A1**(43) **Pub. Date: Jul. 25, 2024**(54) **LIGHT-EMITTING DEVICE, DISPLAY DEVICE, IMAGING DEVICE, ELECTRONIC DEVICE, AND METHOD FOR PRODUCING LIGHT-EMITTING DEVICE****H10K 59/88** (2006.01)**H10K 71/00** (2006.01)**H10K 102/00** (2006.01)(52) **U.S. Cl.****CPC** **H10K 50/856** (2023.02); **H10K 50/844**(2023.02); **H10K 50/852** (2023.02); **H10K****50/86** (2023.02); **H10K 50/865** (2023.02);**H10K 71/00** (2023.02); **H10K 50/858**(2023.02); **H10K 59/38** (2023.02); **H10K****59/88** (2023.02); **H10K 2102/3026** (2023.02);**H10K 2102/351** (2023.02)(71) Applicant: **CANON KABUSHIKI KAISHA,**
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Sano, Kanagawa (JP)(21) Appl. No.: **18/626,593**(22) Filed: **Apr. 4, 2024****Related U.S. Application Data**(63) Continuation of application No. 17/506,952, filed on
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(57)

ABSTRACT

The light-emitting device has: a display region; a peripheral region; a first light-emitting element having a light-emitting region in which a lower electrode, a light-emitting layer, and an upper electrode are laminated; a first laminated section, between the substrate and the lower electrode of the first light-emitting element, in which there are laminated a first high-reflection layer and a first low-reflection layer that has a lower reflectance than the first high-reflection layer; a peripheral laminated section, in the peripheral region, in which there are laminated on the substrate; a peripheral lamination layer in which a peripheral high-reflection layer and a peripheral low-reflection layer that has a lower reflectance than the peripheral high-reflection layer are laminated. For at least a portion of the first laminated section that overlaps in plan view with the light-emitting region, the first low-reflection layer has an opening such that the first high-reflection layer is exposed.

