



(12) **Patent Application Publication**
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(43) **Pub. Date:** **Jul. 11, 2024**

(22) Filed: **Mar. 27, 2024**

Nov. 25, 2020 (CN) 202011339928.4

(57) **ABSTRACT**

Provided are an array substrate and a manufacturing method of an array substrate and a display panel and device. The array substrate includes a pixel circuit. The pixel circuit includes a first transistor and a second transistor, the first transistor includes a first active layer, the second transistor includes a second active layer, and the first active layer and the second active layer both include silicon. The array substrate further includes a first-type inorganic layer and a second-type inorganic layer and a first via hole. The first via hole is located above the first active layer and at least penetrates through the second-type inorganic layer. Concentration of hydrogen ions in the first active layer is less than concentration of hydrogen ions in the second active layer.

