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SAPUTRA et al.(10) **Pub. No.: US 2022/0352899 A1**(43) **Pub. Date: Nov. 3, 2022**(54) **DATA-DEPENDENT CLOCK-GATING
SWITCH DRIVER FOR A
DIGITAL-TO-ANALOG CONVERTER (DAC)****Publication Classification**

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(US)(21) Appl. No.: **17/244,384**(22) Filed: **Apr. 29, 2021**(57) **ABSTRACT**

Certain aspects of the present disclosure provide a digital-to-analog conversion circuit. The digital-to-analog conversion circuit generally includes a detection circuit configured to detect digital transitions in a digital input signal. The digital-to-analog conversion circuit also includes a clock-gating circuit having an input coupled to an output of the detection circuit. The clock-gating circuit is configured to gate a clock signal for the digital-to-analog conversion circuit based on an output signal from the detection circuit.

