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(54) ARRAY SUBSTRATE AND DISPLAY DEVICE

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(57)ABSTRACT

The present disclosure is related to an array substrate and a display device. The array substrate has a display area and a peripheral area surrounding the display area. The array substrate includes a gate material layer and a source and drain material layer that are sequentially stacked on a base substrate. The array substrate further includes a driving signal line. The driving signal line includes a first sub-signal line and a second sub-signal line. The first sub-signal line is arranged in the gate material layer and extends from the peripheral area to the display area. The second sub-signal line is arranged in the source and drain material layer and located at least in the display area. The first sub-signal line and the second sub-signal line are electrically connected through a via hole.

