

US 20240237420A

(19) United States

(12) Patent Application Publication KIM et al.

(10) Pub. No.: US 2024/0237420 A9

(48) **Pub. Date: Jul. 11, 2024 CORRECTED PUBLICATION**

(54) DISPLAY PANEL AND MANUFACTURING METHOD OF THE SAME

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(21) Appl. No.: 18/490,814

(22) Filed: Oct. 20, 2023

Prior Publication Data

(15) Correction of US 2024/0138201 A1 Apr. 25, 2024
See (22) Filed
See (30) Foreign Application Data

(65) US 2024/0138201 A1 Apr. 25, 2024

(30) Foreign Application Priority Data

Oct. 21, 2022 (KR) 10-2022-0136818

Publication Classification

(51) Int. Cl. H10K 59/122 (2006.01) H10K 59/12 (2006.01) H10K 59/131 (2006.01) H10K 71/20 (2006.01)

(52) U.S. Cl.

CPC *H10K 59/122* (2023.02); *H10K 59/1201* (2023.02); *H10K 59/131* (2023.02); *H10K 71/233* (2023.02); *H10K 59/8792* (2023.02)

(57) ABSTRACT

A display panel includes a transistor, a first insulation layer disposed on the transistor, a pixel definition layer disposed on the first insulation layer and having a light emitting opening and an opening pattern surrounding the light emitting opening, a light emitting element disposed on the first insulation layer and including a first electrode at least a portion of which is exposed by the light emitting opening and a second electrode which is electrically connected to the transistor, and a connection line disposed on the transistor and electrically connecting the transistor and the second electrode. A groove overlapping the opening pattern is defined in the first insulation layer, and a portion of the pixel definition layer protrudes from an edge of the first insulation layer defining the groove toward an inside of the opening pattern.

