



US 20240235500A1

(19) **United States**

(12) **Patent Application Publication**

(10) **Pub. No.: US 2024/0235500 A1**

(43) **Pub. Date: Jul. 11, 2024**

(12) **Martin et al.**

(54) **DIGITAL COMPENSATION SYSTEM FOR A RADIO FREQUENCY POWER AMPLIFIER MODULE**

(71) Applicant: **Qorvo US, Inc.**, Greensboro, NC (US)

(72) Inventors: **Frederick L. Martin**, Plantation, FL (US); **Gangadhar Burra**, Fremont, CA (US); **Nikolaus Klemmer**, Dallas, TX (US); **Paul Edward Gorday**, West Palm Beach, FL (US); **Bror Peterson**, Fairview, TX (US)

(21) Appl. No.: **18/617,814**

(22) Filed: **Mar. 27, 2024**

**Related U.S. Application Data**

(63) Continuation of application No. 17/245,027, filed on Apr. 30, 2021, now Pat. No. 11,996,810.

(60) Provisional application No. 63/018,661, filed on May 1, 2020.

**Publication Classification**

(51) **Int. Cl.**  
*H03F 3/24* (2006.01)  
*H03F 1/02* (2006.01)  
*H03F 3/195* (2006.01)

(52) **U.S. Cl.**  
CPC ..... *H03F 3/245* (2013.01); *H03F 1/0227* (2013.01); *H03F 1/0288* (2013.01); *H03F 3/195* (2013.01); *H03F 2200/102* (2013.01); *H03F 2200/168* (2013.01); *H03F 2200/451* (2013.01)

(57) **ABSTRACT**

A digital compensation system for a radio frequency (RF) power amplifier module is disclosed. The digital compensation system includes an RF power amplifier having a first input, a first output, and a first bias input, wherein the RF power amplifier is configured to receive an RF signal at the first input and generate an amplified version of the RF signal at the first output. The digital compensation system also includes compensation circuitry coupled between the first input and the first output and a bias output coupled to the RF power amplifier, wherein the compensation circuitry is configured, in response to the RF signal, to generate or adjust a bias signal at the first bias input to correct dynamic bias errors caused by amplification variations that have time constants.

