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(54) **ARRAY SUBSTRATE AND DISPLAY APPARATUS**

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(57) **ABSTRACT**

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An array substrate is provided. The array substrate includes a first adjacent data line and a second adjacent data line extending along a first direction. The first adjacent data line and the second adjacent data line extend from a same inter-column region between a first column of pixel driving circuit and a second column of pixel driving circuit in a display area into a boundary area between the first column of pixel driving circuit and a peripheral area. In the boundary area, the first adjacent data line and the second adjacent data line are in a same layer.

