



US 20240215342A1

(19) **United States**(12) **Patent Application Publication**
HUANG et al.(10) **Pub. No.: US 2024/0215342 A1**(43) **Pub. Date: Jun. 27, 2024**(54) **DISPLAY DEVICE, AND DISPLAY PANEL
AND MANUFACTURING METHOD
THEREOF****Publication Classification**(51) **Int. Cl.****H10K 59/126** (2006.01)**H10K 59/12** (2006.01)**H10K 59/122** (2006.01)(52) **U.S. Cl.****CPC** **H10K 59/126** (2023.02); **H10K 59/1201**
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(CN)(21) Appl. No.: **17/915,522**(22) PCT Filed: **May 28, 2021**(86) PCT No.: **PCT/CN2021/096814**

§ 371 (c)(1),

(2) Date: **Sep. 29, 2022**

(57)

ABSTRACT

A display panel includes a substrate, a driving layer and a light-emitting control layer. The driving layer is provided with a plurality of driving transistors arranged into a plurality of transistor rows in a column direction. The light-emitting control layer includes a plurality of light-emitting devices arranged into a plurality of device rows in the column direction, the device rows is spaced apart by the transistor row in the column direction, and the transistor rows is spaced apart by the device row in the column direction. The pixel-defining layer is provided with a plurality of blocking grooves recessed toward the substrate, the plurality of blocking grooves are arranged in the column direction, at least one of the plurality of blocking grooves is arranged between the transistor row and the device row adjacent in the column direction, and a light-shielding layer is arranged in the blocking groove.

