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(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2022/0352884 A1**
(43) **Pub. Date:** **Nov. 3, 2022**(54) **GATE DRIVER OUTPUT PROTECTION CIRCUIT**(71) Applicant: **Skyworks Solutions, Inc.**, Irvine, CA (US)(72) Inventors: **Péter Onódy**, Budapest (HU); **Tamás Marozsák**, Budapest (HU); **Michael R. May**, Austin, TX (US); **Fernando Naim Lavalle Aviles**, Austin, TX (US); **Patrick De Bakker**, Hollis, NH (US)(21) Appl. No.: **17/242,909**(22) Filed: **Apr. 28, 2021****Publication Classification**(51) **Int. Cl.****H03K 17/082** (2006.01)**H03K 17/14** (2006.01)**H03K 17/284** (2006.01)(52) **U.S. Cl.**CPC **H03K 17/0828** (2013.01); **H03K 17/145**(2013.01); **H03K 17/284** (2013.01); **H03K****2017/0806** (2013.01)

(57)

ABSTRACT

A method for protecting a system including a driver integrated circuit includes receiving a driver input signal. The method includes driving an output signal externally to the driver integrated circuit. The output signal is driven based on the driver input signal and an indication of a delay between receipt of an edge of the driver input signal and arrival of a corresponding edge of the output signal at an output node coupled to a terminal of the driver integrated circuit.

