



US 20240178841A1

(19) **United States**

(12) **Patent Application Publication**
SAYGINER

(10) **Pub. No.: US 2024/0178841 A1**

(43) **Pub. Date: May 30, 2024**

(54) **ADDRESSING FOR INTEGRATED CIRCUITS**

(52) **U.S. Cl.**

CPC **H03K 19/018585** (2013.01); **H03K 19/09425** (2013.01)

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(21) Appl. No.: **18/507,524**

(22) Filed: **Nov. 13, 2023**

(30) **Foreign Application Priority Data**

Nov. 29, 2022 (FI) 20226058

Publication Classification

(51) **Int. Cl.**

H03K 19/0185 (2006.01)

H03K 19/094 (2006.01)

(57)

ABSTRACT

Disclosed is a method comprising determining that a signal is to be provided to a beamformer integrated circuit using a control interface associated with the beamformer integrated circuit, identifying, within, at least one memory, a unique address of the beamformer integrated circuit, wherein the beamformer integrated circuit is aware of own unique address based on a set state of an address pin of the beamformer integrated circuit, wherein the set state of the address pin is one of at least three available set states, the set state being provided as an input to three logical buffers electrically coupled to the beamformer integrated circuit, and outputs from the three logical buffers being combined using a logical encoder that generates the unique address, and providing the signal to the beamformer integrated circuit using the unique address.

