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(19) **United States**(12) **Patent Application Publication****Tsuboi et al.**(10) **Pub. No.: US 2022/0353995 A1**(43) **Pub. Date: Nov. 3, 2022**(54) **PRINTED CIRCUIT BOARD AND
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Tomohisa Ishigami, Kawasaki-shi (JP)(21) Appl. No.: **17/810,917**(22) Filed: **Jul. 6, 2022****Related U.S. Application Data**(62) Division of application No. 16/821,550, filed on Mar.
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ABSTRACT

A printed circuit board includes a first chip component, a second chip component, and a printed wiring board. The first chip component and the second chip component each has a length L_2 in the longitudinal direction. A relationship of $0.894 \leq L_2/L_1 \leq 1.120$ is satisfied, where L_1 represents a length of the first opening in the longitudinal direction. A relationship of $0.894 \leq L_2/4 \leq 1.120$ is satisfied, where length L_4 represents a length of the second opening in the longitudinal direction. A relationship of $0.183 \leq L_{OA}/L_{iA} \leq 50.309$ is satisfied, where L_{iA} represents a length of the first land in the longitudinal direction, and L_{OA} represents a thickness of solder on an end surface of the first electrode. A relationship of $0.183 \leq L_{OB}/L_{iB} \leq 0.309$ is satisfied, where L_{iB} represents a length of the second land in the longitudinal direction, and L_{OB} represents a thickness of solder on an end surface of the second electrode.

