

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0244831 A1 SONG et al.

Jul. 18, 2024 (43) **Pub. Date:**

(54) SEMICONDUCTOR MEMORY DEVICE AND MANUFACTURING METHOD THEREOF

(71) Applicant: SAMSUNG ELECTRONICS CO.,

LTD., Suwon-si (KR)

(72) Inventors: Younggeun SONG, Suwon-si (KR);

Sanghoon UHM, Suwon-si (KR); Yongjin LEE, Suwon-si (KR); Min Hee CHO, Suwon-si (KR)

- (21) Appl. No.: 18/239,268
- (22)Filed: Aug. 29, 2023
- (30)Foreign Application Priority Data

Jan. 17, 2023 (KR) 10-2023-0006956

Publication Classification

Int. Cl. (51)H10B 12/00

(2006.01)

U.S. Cl.

CPC H10B 12/482 (2023.02); H10B 12/02 (2023.02); H10B 12/315 (2023.02); H10B 12/488 (2023.02); H10B 12/50 (2023.02)

(57)ABSTRACT

A semiconductor device includes a bit line extending in a first direction on a substrate. A first insulating pattern is disposed on the bit line. A channel pattern is disposed on an upper side of the bit line and a lateral side of the first insulating pattern. The channel pattern includes an oxide semiconductor material. A gate insulating pattern is disposed on the channel pattern. Word lines are disposed on the gate insulating pattern. A second insulating pattern is disposed on the word lines. A landing pad is disposed on the channel pattern. An interlayer insulating layer disposed between the bit line and the channel pattern.

