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**Wang**(10) **Pub. No.: US 2024/0251601 A1**(43) **Pub. Date: Jul. 25, 2024**(54) **ARRAY SUBSTRATE AND DISPLAY APPARATUS****Publication Classification**(71) Applicant: **BOE Technology Group Co., Ltd.**,  
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Beijing (CN)(57) **ABSTRACT**(21) Appl. No.: **18/004,896**(22) PCT Filed: **Jan. 25, 2022**(86) PCT No.: **PCT/CN2022/073611**

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An array substrate is provided. The array substrate includes a pixel definition layer defining a plurality of subpixel regions. The pixel definition layer includes an undercut structure. The undercut structure includes a first insulating layer on a base substrate; and a second insulating layer on a side of the first insulating layer away from the base substrate. The first insulating layer is undercut relative to the second insulating layer.

