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#### (54) **SEMICONDUCTOR DEVICE**

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#### (57)ABSTRACT

A semiconductor device may include a substrate including a cell region and a peripheral circuit region, a first gate structure in the cell region of the substrate, the first gate structure extending in a first direction parallel to an upper surface of the substrate, bit line structures on the cell region of the substrate, the bit line structures extending in a second direction perpendicular to the first direction and parallel to the upper surface of the substrate, a second gate structure on the peripheral circuit region of the substrate, contact plug structures between the bit line structures, the contact plug structures contacting the substrate, first conductive structures on peripheral circuit region of the substrate, the first conductive structures being electrically connected to the peripheral circuit region of the substrate, a first upper insulation structure between the first conductive structures, the first upper insulation structure including a first upper insulation pattern and a hydrogen diffusing insulation pattern surrounding a bottom and sidewalls of the first upper insulation pattern, and a second upper insulation pattern between upper portions of the contact plug structures.

