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Osaka (JP)(57) **ABSTRACT**(21) Appl. No.: **17/732,687**(22) Filed: **Apr. 29, 2022**(30) **Foreign Application Priority Data**

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A Doherty amplifier includes a divider configured to divide an input signal into two signals, a first amplifier configured to amplify one of the two signals and output the amplified signal to a first node, a second amplifier configured to amplify the other of the two signals and output the amplified signal to a second node, a balun including lumped parameter elements and configured to output a signal obtained by combining the signal output from the first amplifier with the signal output from the second amplifier to a third node, and a path configured to DC-connect the first node to the second node, with the third node therebetween.

