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(54) DUTY CYCLE CORRECTION CIRCUIT AND DUTY CYCLE CORRECTING METHOD

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ABSTRACT (57)

A duty cycle correction circuit and a duty cycle correction method are disclosed. The disclosed duty cycle correction circuit can comprise at least one low pass filter configured to convert a clock signal to a voltage, and a voltage comparator configured to compare the voltage with a reference voltage to output a comparison signal. The disclosed duty cycle correction circuit can further comprise a controller configured to select, based on the comparing voltage, a duty cycle correction method from a first method and a second method different from the first method. The disclosed duty cycle correction circuit can further comprise a duty regulator configured to perform a duty tuning operation to adjust a duty of the clock signal using the selected duty cycle correction method.

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