



US 20230231476A1

(19) **United States**(12) **Patent Application Publication**
GIRANI et al.(10) **Pub. No.: US 2023/0231476 A1**(43) **Pub. Date: Jul. 20, 2023**(54) **DRIVER CIRCUIT FOR LOW VOLTAGE
DIFFERENTIAL SIGNALING, LVDS, LINE
DRIVER ARRANGEMENT FOR LVDS AND
METHOD FOR OPERATING AN LVDS
DRIVER CIRCUIT**(71) Applicant: **AMS AG**, Premstaetten (AT)(72) Inventors: **Elisa GIRANI**, PAVIA (IT); **Dominik
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(AT)(21) Appl. No.: **18/002,419**(22) PCT Filed: **May 17, 2021**(86) PCT No.: **PCT/EP2021/062997**

§ 371 (c)(1),

(2) Date: **Dec. 19, 2022**(30) **Foreign Application Priority Data**

Jun. 29, 2020 (EP) 20182953.8

Publication Classification(51) **Int. Cl.****H02M 3/156** (2006.01)**H03K 5/01** (2006.01)(52) **U.S. Cl.****CPC** **H02M 3/156** (2013.01); **H03K 5/01**
(2013.01); **H03K 19/20** (2013.01)

(57)

ABSTRACT

A driver circuit for low voltage differential signaling, LVDS, includes a phase alignment circuit including an input configured to receive an input signal, a first output configured to provide an internal signal as a function of the input signal, and a second output configured to provide an inverted internal signal, which is the inverted signal of the internal signal, and an output driver circuit coupled to the phase alignment circuit, the output driver circuit including a first input configured to receive the internal signal, a second input configured to receive the inverted internal signal, a first output configured to provide an output signal as a function of the internal signal and a second output configured to provide an inverted output signal which is the inverted signal of the output signal. Therein the phase alignment circuit is configured to provide the inverted internal signal with its phase being aligned to a phase of the internal signal.

