

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0213132 A1 DARMAWIKARTA et al.

Jun. 27, 2024 (43) **Pub. Date:**

(54) THIN FILM CAPACITORS (TFCS) IN ETCHED BACK DEEP VIA

(71) Applicant: Intel Corporation, Santa Clara, CA

(72) Inventors: Kristof DARMAWIKARTA, Chandler, AZ (US); Benjamin DUONG, Phoenix, AZ (US); Darko GRUJICIC,

Chandler, AZ (US); Shayan KAVIANI,

Phoenix, AZ (US); Mahdi

MOHAMMADIGHALENI, Phoenix, AZ (US); Suddhasattwa NAD, Chandler, AZ (US); Thomas L. SOUNART, Chandler, AZ (US); Marcel WALL, Phoenix, AZ (US); Ravindranath V. MAHAJAN, Chandler, AZ (US); Rahul N. MANEPALLI, Chandler, AZ (US)

(21) Appl. No.: 18/089,476

(22) Filed: Dec. 27, 2022

Publication Classification

(51) Int. Cl. H01L 23/498 (2006.01)H01L 27/01 (2006.01)

U.S. Cl.

CPC H01L 23/49838 (2013.01); H01L 27/016 (2013.01); H01L 28/86 (2013.01); H01L 28/90 (2013.01); H01L 23/49822 (2013.01); H01L 23/49894 (2013.01); H01L 24/16 (2013.01)

(57) **ABSTRACT**

Embodiments disclosed herein include an electronic package. In an embodiment, the electronic package comprises a package substrate, where the package substrate comprises a plurality of stacked dielectric layers. In an embodiment, the electronic package further comprises an opening into the package substrate, where the opening passes through at least two of the plurality of dielectric layers. In an embodiment, a first pad is at the bottom of the opening, a capacitor is disposed in the opening, and a second pad is over the capacitor.

