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In an embodiment of the present disclosure, an integrated circuit includes: a first interface suitable for receiving first to N^{th} data, where N is an even number equal to or greater than 2, and first to N^{th} multi-phase clocks; an interface conversion circuit suitable for generating serial data based on the first to N^{th} data that are received through the first interface, and generating a data strobe signal for strobing the serial data based on the first to N^{th} multi-phase clocks that are received through the first interface; and a second interface suitable for receiving the serial data and the data strobe signal.

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