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(54) **DISPLAY PANEL**

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(57) **ABSTRACT**

A display panel is provided, including: a substrate; a first planarization layer disposed on a side of the substrate, wherein a first via hole is defined in the first planarization layer; a first ITO layer disposed on a side of the first planarization layer away from the substrate; and an anode disposed on a side of the first ITO layer away from the first planarization layer. Wherein, a first groove is defined in the first ITO layer to make a part of the anode be disposed in the first groove, and a projection of the first groove on the substrate at least partially overlaps with a projection of the first via hole on the substrate.

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