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(54) TIMING CALIBRATIONTECHNIQUE FOR RADIO FREQUENCY DIGITAL-TO-ANALOG **CONVERTER**

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ABSTRACT (57)

A calibration system comprises an actuator circuit comprising a first delay circuit that receives a plurality of data pulses and a second delay circuit that receives the pulses, wherein one of the first and second delay circuits delays the data pulses independently of the other of the first and second delay circuits; a data switch that receives an output of the actuator circuit including delay data signals of the data pulses from the first and second delay circuits and switches and outputs a plurality of local oscillator (LO) signals for output as a controlled LO signal according to control signals of the delay data signals and applied to the data switch. At least one calibration switch receives the output of the actuator circuit and the plurality of LO+ and LO- signals, and outputs a second controlled LO signal output to a sense circuit.

