

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0213016 A1

Chang et al. (43) **Pub. Date:**

continuation of application No. 17/205,847, filed on

Jun. 27, 2024

(54) METHOD OF FORMING CONDUCTIVE FEATURE INCLUDING CLEANING STEP

(71) Applicant: Taiwan Semiconductor

Manufacturing Co., Ltd., Hsinchu

(TW)

(72) Inventors: Cheng-Wei Chang, Taipei City (TW); Min-Hsiu Hung, Tainan City (TW); Chun-I Tsai, Hsinchu (TW); Ken-Yu Chang, Hsinchu (TW); Yi-Ying Liu,

Hsinchu (TW)

(21) Appl. No.: 18/598,322

(22) Filed: Mar. 7, 2024

Related U.S. Application Data

(63) Continuation of application No. 18/309,298, filed on Apr. 28, 2023, now Pat. No. 11,955,329, which is a Mar. 18, 2021, now Pat. No. 11,670,499.

Publication Classification

(51) Int. Cl. H01L 21/02 (2006.01)H01L 21/768 (2006.01)

(52) U.S. Cl.

CPC .. H01L 21/02068 (2013.01); H01L 21/76871 (2013.01); H01L 21/76877 (2013.01)

(57)**ABSTRACT**

A method of forming a semiconductor device includes forming a first conductive feature on a bottom surface of an opening through a dielectric layer. The forming the first conductive feature leaves seeds on sidewalls of the opening. A treatment process is performed on the seeds to form treated seeds. The treated seeds are removed with a cleaning process. The cleaning process may include a rinse with deionized water. A second conductive feature is formed to fill the opening.

