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(71) Applicant: **Celtro GmbH**, Dresden (DE)

(72) Inventors: **Jarek BUDNY**, Dresden (DE); **Judith PIORKOWSKI**, Dresden (DE); **Gerd TEEPE**, Dresden (DE)

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ABSTRACT

The invention discloses a device, characterized in that the device comprises a multiple of microneedles and a chip comprising at least one comparator with adaptive level, sequence control circuit, at least one capacitor stack built by n capacitors and 2n switches, at least one buffer capacitor outside the at least one capacitor stack, at least two additional switches outside the at least one capacitor stack, a CMOS-Logic, wherein further, the device comprises an interposer layer comprising holes for the multiple of microneedles, at least one sensor and a lid.

