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Figure 1 is a cross-sectional view of a semiconductor device 100. The device includes a substrate 102 with a central layer 106. Above the substrate, there are several layers: a top layer 110, a layer 112, and a layer 114. A detailed view of a contact structure 140 is shown on the right, including layers 142, 144, 148, and 136, and a conductive pad 138.