



US 20240213083A1

(19) **United States**

(12) **Patent Application Publication**  
**SHEN**

(10) **Pub. No.: US 2024/0213083 A1**

(43) **Pub. Date: Jun. 27, 2024**

(54) **SEMICONDUCTOR DEVICE AND  
MANUFACTURING METHOD THEREOF**

(52) **U.S. Cl.**  
CPC .. *H01L 21/76232* (2013.01); *H01L 29/66621*  
(2013.01); *H01L 29/66636* (2013.01); *H01L*  
*29/7848* (2013.01)

(71) Applicant: **HON HAI PRECISION INDUSTRY  
CO., LTD.**, New Taipei City (TW)

(72) Inventor: **Yun-Hung SHEN**, Hsinchu City (TW)

(21) Appl. No.: **18/164,553**

(22) Filed: **Feb. 3, 2023**

(30) **Foreign Application Priority Data**

Dec. 26, 2022 (TW) ..... 111149961

**Publication Classification**

(51) **Int. Cl.**  
*H01L 21/762* (2006.01)  
*H01L 29/66* (2006.01)  
*H01L 29/78* (2006.01)

(57) **ABSTRACT**

A semiconductor device includes a substrate, a plurality of epitaxial structures and a plurality of gate structures. The substrate includes a plurality of recesses and a plurality of convex portions. Each of the convex portions is located between the two adjacent recesses. The epitaxial structures are located in the recesses of the substrate respectively. Each of the epitaxial structures includes a first transition layer, a second transition layer and an epitaxial layer. The first transition layer is located on a bottom surface and a sidewall of one of the recesses. The second transition layer is disposed along the first transition layer, in which a material of the second transition layer is different from a material of the first transition layer. The epitaxial layer is located on the second transition layer and fills one of the recesses. The gate structures are located on the convex portions of the substrate respectively.

100

