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**JUNNARKAR et al.**(10) **Pub. No.: US 2024/0213874 A1**(43) **Pub. Date: Jun. 27, 2024**(54) **GATE DRIVER CIRCUIT**(71) Applicant: **TEXAS INSTRUMENTS**  
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(2013.01)(57) **ABSTRACT**

A gate driver circuit includes a pull-up circuit, a pull-down circuit, a level shifter circuit, and a drive strength control circuit. The pull-up circuit includes a pull-up output, a first signal input, and a first enable input. The pull-up output is coupled to a gate drive output. The first signal input is coupled to a drive signal input. The pull-down circuit includes a pull-down output, a second signal input, and a second enable input. The pull-down output is coupled to the gate drive output. The second signal input is coupled to the drive signal input. The level shifter circuit includes a shifter output and a drive strength input. The shifter output is coupled to the first enable input and the second enable input. The drive strength control circuit includes a drive strength output coupled to the drive strength input.

