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Charbonneau et al.

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#### (54) BACKPLANE FOOTPRINT FOR HIGH SPEED, HIGH DENSITY ELECTRICAL **CONNECTORS**

(71) Applicant: Amphenol Corporation, Wallingford,

Inventors: Marc Robert Charbonneau, Bedford, NH (US); Jose Ricardo Paniagua,

Newmarket, NH (US)

Assignee: Amphenol Corporation, Wallingford,

CT (US)

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#### (57)**ABSTRACT**

A printed circuit board includes a plurality of layers including attachment layers and routing layers; and columns of via patterns formed in the plurality of layers, wherein via patterns in adjacent columns are offset in a direction of the columns, each of the via patterns comprising: first and second signal vias forming a differential signal pair, the first and second signal vias extending through at least the attachment layers; and at least one conductive shadow via located between the first and second signal vias of the differential pair. In some embodiments, at least one conductive shadow via is electrically connected to a conductive surface film.

