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Xu et al. (43) **Pub. Date: Dec. 1, 2022**(54) **MULTIPLE FEEDBACK FILTER**(71) Applicant: **TEXAS INSTRUMENTS**
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Rahman, Wylie, TX (US)(21) Appl. No.: **17/880,142**(22) Filed: **Aug. 3, 2022****Related U.S. Application Data**(62) Division of application No. 17/124,785, filed on Dec.
17, 2020, now Pat. No. 11,444,587.**Publication Classification**(51) **Int. Cl.**
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(2013.01); **H03F 2200/129** (2013.01)(57) **ABSTRACT**

A circuit having an input and an output, the circuit comprising: a first amplifier having a first input, a second input and an output coupled to the output of the circuit; a first capacitor having a first terminal coupled to the first input of the first amplifier and a second terminal coupled to the output of the first amplifier; a first resistor having a first terminal coupled to the first input of the first amplifier and a second terminal; a buffer having an output coupled to the second terminal of the first resistor and an input; a second resistor having a first terminal coupled to the output of the first amplifier and a second terminal coupled to the input of the buffer; a second capacitor coupled between the input of the buffer and ground; and a third resistor coupled between the input of the buffer and the input of the circuit.

