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**YANG et al.**(10) **Pub. No.: US 2024/0179951 A1**(43) **Pub. Date: May 30, 2024**(54) **DISPLAY SUBSTRATE AND ELECTRONIC APPARATUS**(52) **U.S. Cl.**CPC ..... **H10K 59/122** (2023.02); **H10K 59/80521** (2023.02)(71) Applicants: **YUNNAN INVENSIGHT OPTOELECTRONICS TECHNOLOGY CO., LTD.**, Kunming (CN); **BOE TECHNOLOGY GROUP CO., LTD.**, Beijing (CN)

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**ABSTRACT**(72) Inventors: **Zongshun YANG**, Beijing (CN); **Hongtao YU**, Beijing (CN); **Kuanta HUANG**, Beijing (CN)(21) Appl. No.: **17/793,790**(22) PCT Filed: **Aug. 11, 2021**(86) PCT No.: **PCT/CN2021/112067**

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A display substrate and an electronic apparatus are provided. The display substrate includes a base substrate, and a dielectric layer, a first electrode layer and a pixel defining layer thereon. The first electrode layer includes a first electrode and a second electrode spaced. The pixel defining layer includes a pixel defining portion between a first and a second opening region. The first electrode includes a first and a second surface away from the base substrate, and the second surface is closer to the base substrate than the first surface; the display substrate has a cross section perpendicular to the base substrate; within the cross section and along a first direction parallel to a plate surface of the base substrate, portions of the pixel defining portion on the first surface and the second surface of the first electrode respectively have lengths denoted by d1 and d2; d1 is less than d2.

