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(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2022/0407485 A1**
(43) **Pub. Date: Dec. 22, 2022**(54) **DIGITAL VARIABLE GAIN ADJUSTMENT
ON BASEBAND CHIP**(52) **U.S. Cl.**
CPC *H03G 3/3078* (2013.01); *H03G 3/3089*
(2013.01); *H03M 7/24* (2013.01)(71) Applicant: **ZEKU, INC.**, PALO ALTO, CA (US)(72) Inventors: **Jifeng GENG**, San Diego, CA (US);
Hong Kui YANG, San Diego, CA (US)(57) **ABSTRACT**(21) Appl. No.: **17/894,393**(22) Filed: **Aug. 24, 2022****Related U.S. Application Data**(63) Continuation of application No. PCT/US2021/
020956, filed on Mar. 4, 2021.(60) Provisional application No. 62/990,881, filed on Mar.
17, 2020.**Publication Classification**(51) **Int. Cl.**
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Embodiments of apparatus and method for digital variable gain adjustment (DVGA) are disclosed. In an example, a baseband chip includes an unpacking module, a symbol recording module operatively coupled to the unpacking module, and a first variable gain adjusting (VGA) module operatively coupled to the symbol recording module. The unpacking module is configured to unpack a plurality of symbols from a first representation of pseudo floating-point numbers to a second representation of fixed-point numbers. The symbol recording module is configured to obtain a symbol parameter based on the unpacking. The first VGA module is configured to dynamically adjust gains of the plurality of symbols having the second representation based on the symbol parameter.

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