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(54) **DISPLAY DEVICE**

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ABSTRACT (57)

On a chip-mounting portion, a plurality of chip-use terminals are provided in a single row, and a plurality of terminal lines are provided extending parallel to each other in such a manner as to correspond to the plurality of chip-use terminals and being electrically connected respectively to the plurality of chip-use terminals, and a chip support body is provided between the plurality of chip-use terminals.

