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HO et al. (43) **Pub. Date: Dec. 22, 2022**(54) **DIGITAL-TO-ANALOG CONVERSION
APPARATUS AND METHOD HAVING
SIGNAL CALIBRATION MECHANISM**(52) **U.S. Cl.**
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The present invention discloses a DAC method having signal calibration mechanism. A first conversion circuit generates a first analog signal according to an input digital signal. A second conversion circuit generates a second analog signal according to the input digital signal and a pseudo-noise digital signal. An echo transmission circuit processes a signal on an echo path to generate an echo signal. A first and a second calibration circuits generate a first and a second calibration signals. A calibration parameter calculation circuit performs calculation according to a difference between the echo signal and a sum of the first and the second calibration signals and related path information to generate a first and a second offsets. The first and the second calibration circuits converge first and second response coefficients and update a first and a second code-word offset tables according to the first and the second offsets.

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