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Figure 1 is a schematic diagram of a semiconductor device 100. The device includes a signal input section 11 and a signal output section 200. The signal input section 11 contains three SR flip-flops (SR1, SR2, SR3) with input and output terminals. The signal output section 200 is connected to the output terminals of the SR flip-flops. The device is also labeled with S, A, and Q'. The diagram shows a grid of access transistors and storage capacitors, with columns labeled D, VDD, D, VDD, D, and D, and rows labeled G, G, and G. The device is also labeled with Q' and VDD.