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(54) **DISPLAY SUBSTRATE, METHOD FOR  
MANUFACTURING DISPLAY SUBSTRATE,  
AND DISPLAY APPARATUS**

(52) **U.S. Cl.**  
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(57) **ABSTRACT**

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A display substrate is provided to include a base substrate and: a first transistor layer including a first active layer and a second transistor layer including a second active layer sequentially along a direction away from the base substrate; the first and second active layers are provided with at least one insulating layer therebetween, and are made of low-temperature polysilicon materials; the first active layer includes first channel region(s) and first source-drain doped region(s); the second active layer includes second channel region(s) and second source-drain doped region(s); orthographic projections of a second source-drain doped region and a first source-drain doped region on the base substrate overlap with each other; and a second source-drain doped region is connected to a first source-drain doped region through a connecting part filled in a corresponding first via in the insulating layer. A method for manufacturing a display substrate and a display apparatus are provided.

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