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(54) **DISPLAY DEVICE**

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(57) **ABSTRACT**

A transistor may include: first, second, and third sub-pixels adjacent to each other, each of the first, second, and third sub-pixels including a storage capacitor; a scan line selectively transferring a scan signal and a control signal to each of the first, second, and third sub-pixels, the scan line extending in a first direction; a data line transferring a data signal to each of the first, second, and third sub-pixels, the data line extending in a second direction intersecting the first direction; and a first power line electrically connected to each of the first, second, and third sub-pixels, and the first power line being supplied with a first driving power voltage. The first power line may be disposed between the storage capacitor and the data line.

