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### (54) SEMICONDUCTOR DEVICE WITH MAGNETIC TUNNEL JUNCTIONS

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#### (57)ABSTRACT

A semiconductor device includes a substrate; a memory array over the substrate, the memory array including first magnetic tunnel junctions (MTJs), where the first MTJs are in a first dielectric layer over the substrate; and a resistor circuit over the substrate, the resistor circuit including second MTJs, where the second MTJs are in the first dielectric layer.

