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(54) **NETWORK INTERFACE DEVICE WITH
FRAME SEQUENCE VALUE CHECKING**

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ABSTRACT

A network interface device comprises circuitry to add a frame check sequence value a data packet to be transmitted onto a network. The data packet with the frame check sequence value is stored in memory. Media access control layer circuitry reads the data packet from the memory and determines if the frame check sequence value is correct. When it is not correct, it is determined that the data in the data packet is corrupted.

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Receiving a data packet to be transmitted onto a network 500

Adding a frame check sequence value to the data packet 502

Storing in a memory the data packet with the frame check sequence value 504

Reading the data packet with the frame check sequence value from the
memory 506

Determining if the frame check sequence value is correct for data in the data
packet read from the memory 508

When the frame check sequence value is not correct for the data in the data
packet read from the memory, determining that the data in the data packet is
unexpected 510