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(54) A PROCESS OF FORMING AN ELECTRODE INTERCONNECTION IN AN INTEGRATED MULTILAYER THIN-FILM ELECTRONIC DEVICE

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(57) ABSTRACT

A process of forming an electrode interconnection between at least two adjacent unit devices in an integrated multilayer thin-film electronic device comprising: providing an intermediary device that comprises: a first electrode layer on a thin film substrate comprising a first patterned coating that includes at least two spaced apart first electrode sections of adjacent unit devices; a first functional layer comprising a substantially continuous coating over the first electrode layer; and a second functional layer comprising a second patterned coating on the first functional layer comprising at least two spaced apart functional sections, each functional section positioned on the first functional layer to overlay a portion of one of the first electrode sections so to define a gap portion between adjacent functional sections that includes a portion of that first electrode section and the first functional layer; and applying a second electrode layer over the second functional layer as a third patterned coating that includes at least two spaced apart second electrode sections of adjacent unit devices, each second electrode section being positioned to overlay at least one functional section of the second functional layer and a portion of an adjoining gap portion that includes at least one portion of the first electrode section of an adjacent unit device, the third patterned coating being formed using a solution including a conductive species and at least a first solvent, wherein the first functional layer is soluble in the first solvent and the second functional layer has a low to zero solubility in the first solvent, such that application of the second electrode layer to the gap portion forms at least one electrically conductive path through the first functional layer between the first electrode and the second electrode of adjacent unit devices.

