



US 20220407464A1

(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2022/0407464 A1**
(43) **Pub. Date: Dec. 22, 2022**(54) **ENVELOPE TRACKING VOLTAGE CORRECTION IN A TRANSMISSION CIRCUIT**(71) Applicant: **Qorvo US, Inc.**, Greensboro, NC (US)(72) Inventors: **Nadim Khat**, Cugnaux (FR); **James M. Retz**, Cedar Rapids, IA (US)(21) Appl. No.: **17/700,700**(22) Filed: **Mar. 22, 2022****Related U.S. Application Data**

(60) Provisional application No. 63/212,418, filed on Jun. 18, 2021, provisional application No. 63/245,145, filed on Sep. 16, 2021.

Publication Classification(51) **Int. Cl.**
H03F 1/02 (2006.01)
H03F 3/24 (2006.01)
H04B 1/40 (2006.01)(52) **U.S. Cl.**CPC **H03F 1/0233** (2013.01); **H03F 3/245** (2013.01); **H04B 1/40** (2013.01); **H03F 2200/105** (2013.01); **H03F 2200/451** (2013.01)

(57)

ABSTRACT

Envelope tracking (ET) voltage correction in a transmission circuit is provided. The transmission circuit includes a transceiver circuit and a power amplifier circuit(s). The transceiver circuit generates a radio frequency (RF) signal(s) from a time-variant modulation vector and the power amplifier circuit(s) amplifies the RF signal(s) based on a modulated voltage and provides the amplified RF signal(s) to a coupled RF front-end circuit. Herein, the transceiver circuit is configured to apply a complex filter(s) to the time-variant modulation vector and/or the RF signal(s) to compensate for a voltage distortion filter created across a modulation bandwidth of the RF signal(s) by coupling the power amplifier circuit with the RF front-end circuit. As a result, it is possible to reduce undesired instantaneous excessive compression and/or spectrum regrowth resulting from the voltage distortion filter to thereby improve efficiency and linearity of the power amplifier circuit(s) across the modulation bandwidth of the RF signal(s).

