

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2023/0231547 A1 McQuilkin et al.

Jul. 20, 2023 (43) Pub. Date:

(54) COMPARATOR WITH CONFIGURABLE **OPERATING MODES**

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- (21) Appl. No.: 18/186,855
- (22) Filed: Mar. 20, 2023

Related U.S. Application Data

(62) Division of application No. 17/461,634, filed on Aug. 30, 2021, now Pat. No. 11,637,551, which is a division of application No. 16/827,168, filed on Mar. 23, 2020, now Pat. No. 11,128,287.

Publication Classification

(51) Int. Cl. H03K 5/24 (2006.01)H03K 5/01 (2006.01) G01R 19/00 (2006.01)G11C 7/06 (2006.01)H03K 5/02 (2006.01)

U.S. Cl. CPC H03K 5/24 (2013.01); H03K 5/01 (2013.01); H03K 5/2481 (2013.01); G01R 19/0038 (2013.01); G11C 7/062 (2013.01); H03K 5/023 (2013.01)

(57)ABSTRACT

A multiple operating-mode comparator system can be useful for high bandwidth and low power automated testing. The system can include a gain stage configured to drive a high impedance input of a comparator output stage, wherein the gain stage includes a differential switching stage coupled to an adjustable impedance circuit, and an impedance magnitude characteristic of the adjustable impedance circuit corresponds to a bandwidth characteristic of the gain stage. The comparator output stage can include a buffer circuit coupled to a low impedance comparator output node. The buffer circuit can provide a reference voltage for a switched output signal at the output node in a higher speed mode, and the buffer circuit can provide the switched output signal at the output node in a lower power mode.

