



US 20240213191A1

(19) **United States**

(12) **Patent Application Publication**
Theil et al.

(10) **Pub. No.: US 2024/0213191 A1**

(43) **Pub. Date: Jun. 27, 2024**

(54) **CONTROLLED GRAIN GROWTH FOR
BONDING AND BONDED STRUCTURE
WITH CONTROLLED GRAIN GROWTH**

Publication Classification

(51) **Int. Cl.**
H01L 23/00 (2006.01)

(52) **U.S. Cl.**
CPC **H01L 24/05** (2013.01); **H01L 24/03**
(2013.01); **H01L 24/08** (2013.01); **H01L 24/80**
(2013.01); **H01L 2224/03462** (2013.01); **H01L**
2224/03616 (2013.01); **H01L 2224/05147**
(2013.01); **H01L 2224/08145** (2013.01); **H01L**
2224/80895 (2013.01); **H01L 2224/80896**
(2013.01); **H01L 2924/3512** (2013.01)

(71) Applicant: **ADELA SEMICONDUCTOR
BONDING TECHNOLOGIES INC.**,
San Jose, CA (US)

(72) Inventors: **Jeremy Alfred Theil**, Mountain View,
CA (US); **Cyprian Emeka Uzoh**, San
Jose, CA (US); **Guilian Gao**, Campbell,
CA (US)

(21) Appl. No.: **18/069,910**

(22) Filed: **Dec. 21, 2022**

Related U.S. Application Data

(60) Provisional application No. 63/293,300, filed on Dec.
23, 2021.

(57) **ABSTRACT**

Disclosed is an element including a conductive feature at a
contact surface of the element and a nonconductive region at
the contact surface in which the conductive feature is at least
partially embedded. The contact feature includes a conduc-
tive material and an amount of impurities at a grain bound-
ary of the conductive material. The impurities have a non-
alloying material that does not form an alloy with the
conductive material at a bonding temperature.

