



US 20240213932A1

(19) **United States**

(12) **Patent Application Publication**
Jiang et al.

(10) **Pub. No.: US 2024/0213932 A1**

(43) **Pub. Date: Jun. 27, 2024**

(54) **MULTI-INPUT LNA WITH PASSIVE BYPASS GAIN MODES**

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(21) Appl. No.: **18/087,476**

(22) Filed: **Dec. 22, 2022**

Publication Classification

(51) **Int. Cl.**
H03F 3/193 (2006.01)
H03F 1/56 (2006.01)
H03H 7/38 (2006.01)

(52) **U.S. Cl.**

CPC **H03F 3/193** (2013.01); **H03F 1/565** (2013.01); **H03H 7/38** (2013.01); **H04B 1/406** (2013.01)

(57)

ABSTRACT

New multi-input LNA architectures with improved passive mode negative gain performance that reconfigure the bypass path routes to achieve wide-band bypass matching and make bypass matching for lower frequency bands possible to achieve desired gain specifications. In a first embodiment, improved wide-band performance is provided by a bypass path that optionally does not pass through an impedance matching network and thus has a dedicated path to RF_{OUT}. In a second embodiment, improved wide-band performance is provided by a bypass path that does not pass through an input inductor. In a third embodiment, improved wide-band performance is provided by a bypass path that has a first portion that optionally does not pass through an impedance matching network, and a second portion that does not pass through an input inductor. In a fourth embodiment, improved wide-band performance is provided by selectively disabling a load inductor in some modes of operation.

