



US 20240237409A9

(19) **United States**  
(12) **Patent Application Publication**  
**LI et al.**

(10) **Pub. No.: US 2024/0237409 A9**  
(48) **Pub. Date: Jul. 11, 2024**  
**CORRECTED PUBLICATION**

(54) **DISPLAY PANEL AND DISPLAY APPARATUS**

**Publication Classification**

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(21) Appl. No.: **18/402,044**

(22) Filed: **Jan. 2, 2024**

**Prior Publication Data**

(15) Correction of US 2024/0138192 A1 Apr. 25, 2024  
See (22) Filed.  
See (30) Foreign Application Priority Data.

(65) US 2024/0138192 A1 Apr. 25, 2024

**Related U.S. Application Data**

(63) Continuation of application No. 18/150,953, filed on Jan. 6, 2023, now Pat. No. 11,903,257, which is a continuation of application No. 17/147,309, filed on Jan. 12, 2021, now Pat. No. 11,574,978.

**Foreign Application Priority Data**

Oct. 23, 2020 (CN) ..... 202011150331.5

(51) **Int. Cl.**

**H10K 59/121** (2006.01)

**G09G 3/3225** (2006.01)

**G09G 3/3266** (2006.01)

**G09G 3/3275** (2006.01)

(52) **U.S. Cl.**

CPC ..... **H10K 59/1216** (2023.02); **G09G 3/3225**

(2013.01); **G09G 3/3266** (2013.01); **G09G**

**3/3275** (2013.01); **H10K 59/1213** (2023.02);

**G09G 2320/0626** (2013.01); **H01L 27/1225**

(2013.01)

(57)

**ABSTRACT**

Provided are display panels and display apparatus. The display panel includes a driving array layer including a pixel circuit and a driving circuit configured to provide a control signal to the pixel circuit. The pixel circuit includes first and third transistors. The driving circuit includes second and fourth transistors. The first to fourth transistors include an active layer including silicon, an active layer including oxide semiconductor, an active layer including oxide semiconductor, and an active layer including silicon. The first and the third transistors are switching transistors of the pixel circuit,  $|W1/L1-W4/L4| < |W2/L2-W3/L3|$ ; or the first and the third transistors are a driving transistor and a switching transistor of the pixel circuit, respectively,  $|W1/L1-W4/L4| > 5*|W2/L2-W3/L3|$ ; or the first and third transistors are a switching transistor and a driving transistor of the pixel circuit, respectively,  $5*|W1/L1-W4/L4| < |W2/L2-W3/L3|$ .

