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(54) THREE-DIMENSIONAL MEMORY DEVICE CONTAINING ENGINEERED CHARGE STORAGE ELEMENTS AND METHODS FOR FORMING THE SAME

(71) Applicant: SANDISK TECHNOLOGIES LLC, ADDISON, TX (US)

(72) Inventors: Rahul SHARANGPANI, Fremont, CA (US); Raghuveer S. MAKALA, Campbell, CA (US); Adarsh RAJASHEKHAR, Santa Clara, CA (US); Fei ZHOU, San Jose, CA (US)

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(57)**ABSTRACT**

A semiconductor structure includes an alternating stack of insulating layers and electrically conductive layers, a memory opening vertically extending through the alternating stack, and a memory opening fill structure located in the memory opening and containing a vertical semiconductor channel and a memory film. The memory film includes a tunneling dielectric layer, a continuous charge storage material layer vertically extending through a plurality of the electrically conductive layers, a vertical stack of discrete charge storage elements located at levels of the electrically conductive layers and contacting a respective surface segment of an outer sidewall of the continuous charge storage material layer, and a vertical stack of discrete blocking dielectric material portions containing silicon atoms and oxygen atoms and located at the levels of the electrically conductive layers and vertically spaced apart from each other.

