



(54) **DIGITAL-TO-ANALOG CONVERTER AND METHOD FOR AMPLITUDE AND SKEW ERROR CORRECTION IN THE DIGITAL-TO-ANALOG CONVERTER**

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(57) **ABSTRACT**

A digital-to-analog converter (DAC) and a method for correcting amplitude and/or skew error in a DAC. The DAC includes a main DAC, cell error determination circuit, a correction DAC, and a combiner. The main DAC includes a plurality of DAC cells. The cell error determination circuit is configured to determine an amplitude error and/or a skew error of each of the plurality of DAC cells and generate error data of the DAC based on the input data to the DAC cells. The correction DAC is configured to generate an error signal based on the error data. The combiner is configured to combine the error signal with an output of the main DAC.

