



US 20240213182A1

(19) **United States**

(12) **Patent Application Publication**
Kozicki

(10) **Pub. No.: US 2024/0213182 A1**

(43) **Pub. Date: Jun. 27, 2024**

(54) **PHYSICAL UNCLONABLE FUNCTIONS
WITH SILICON-RICH DIELECTRIC
DEVICES**

(52) **U.S. Cl.**
CPC *H01L 23/573* (2013.01); *H01L 23/482*
(2013.01); *H04L 9/3278* (2013.01)

(71) Applicant: **Michael Kozicki**, Phoenix, AZ (US)

(72) Inventor: **Michael Kozicki**, Phoenix, AZ (US)

(21) Appl. No.: **18/431,219**

(22) Filed: **Feb. 2, 2024**

Related U.S. Application Data

(62) Division of application No. 17/112,668, filed on Dec. 4, 2020, now Pat. No. 11,935,843.

(60) Provisional application No. 62/945,683, filed on Dec. 9, 2019.

Publication Classification

(51) **Int. Cl.**
H01L 23/00 (2006.01)
H01L 23/482 (2006.01)
H04L 9/32 (2006.01)

(57) **ABSTRACT**

Systems for physical unclonable function (“PUF”) generation, PUF devices, and methods for manufacturing PUF devices. In one implementation, the system includes a plurality of PUF devices and an electronic controller. Each of the plurality of PUF devices include a first electrochemically-inactive electrode, a second electrochemically-inactive electrode, and a layer of silicon suboxide. The layer of silicon suboxide is positioned directly between the first electrochemically-inactive electrode and the second electrochemically-inactive electrode. The electronic controller is communicably coupled to the plurality of PUF devices. The electronic controller is configured to read binary values associated with the plurality of PUF devices.

