



US 20230230837A1

(19) **United States**
(12) **Patent Application Publication**
CHIANG

(10) **Pub. No.: US 2023/0230837 A1**
(43) **Pub. Date: Jul. 20, 2023**

(54) **SEMICONDUCTOR STRUCTURE AND METHOD OF FORMING THE SAME**

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(21) Appl. No.: **17/567,307**

(22) Filed: **Jan. 3, 2022**

Publication Classification

(51) **Int. Cl.**
H01L 21/033 (2006.01)
H01L 23/544 (2006.01)

(52) **U.S. Cl.**

CPC **H01L 21/0338** (2013.01); **H01L 21/0332** (2013.01); **H01L 21/0335** (2013.01); **H01L 21/0337** (2013.01); **H01L 23/544** (2013.01); **H01L 27/108** (2013.01)

(57)

ABSTRACT

A semiconductor structure includes: a plurality of calibration reference features disposed on a substrate and spaced apart from each other in a first direction; and a plurality of columns of first active features and a plurality of columns of second active features respectively disposed on opposite sides of the calibration reference features, wherein each of the columns of first active features is spaced apart from each other in a second direction, each of the columns of second active features is spaced apart from each other in the second direction, and the calibration reference features, the first active features, and the second active features are disposed on the same layer and are a portion of the substrate.

