



US 20240237427A9

(19) **United States**
(12) **Patent Application Publication**
Park et al.

(10) **Pub. No.: US 2024/0237427 A9**
(48) **Pub. Date: Jul. 11, 2024**
CORRECTED PUBLICATION

(54) **DISPLAY APPARATUS**

(30) **Foreign Application Priority Data**

(71) Applicant: **Samsung Display Co., Ltd.**, Yongin-si (KR)

Oct. 21, 2022 (KR) 10-2022-0136828

(72) Inventors: **Jeongju Park**, Yongin-si (KR);
Donghan Kang, Yongin-si (KR);
Sungwon Moon, Yongin-si (KR);
Seungsok Son, Yongin-si (KR);
Shinhyuk Yang, Yongin-si (KR);
Kapsoo Yoon, Yongin-si (KR);
Keumhee Lee, Yongin-si (KR);
Woogeun Lee, Yongin-si (KR)

Publication Classification

(51) **Int. Cl.**
H10K 59/124 (2006.01)
(52) **U.S. Cl.**
CPC **H10K 59/124** (2023.02)

(21) Appl. No.: **18/215,832**

(57) **ABSTRACT**

(22) Filed: **Jun. 29, 2023**

Prior Publication Data

(15) Correction of US 2024/0138207 A1 Apr. 25, 2024
See (22) Filed.
See (30) Foreign Application Priority Data.

(65) US 2024/0138207 A1 Apr. 25, 2024

A display apparatus is disclosed that includes a thin-film transistor including a semiconductor layer, an organic light-emitting element, and a passivation layer including a first contact hole connecting the thin-film transistor and the organic light-emitting element, wherein the passivation layer includes a first passivation layer, a second passivation layer, and a third passivation layer, which are sequentially stacked, and the first passivation layer includes SiON.

