



US 20220377909A1

(19) **United States**(12) **Patent Application Publication****KAO et al.**(10) **Pub. No.: US 2022/0377909 A1**(43) **Pub. Date: Nov. 24, 2022**(54) **ELECTRONIC-COMPONENT CARRIER BOARD AND A WIRING METHOD FOR THE SAME****B23K 1/00** (2006.01)**H05K 1/02** (2006.01)(71) Applicant: **GLOBAL MASTER TECH. CO., LTD.**, Kaohsiung (TW)(72) Inventors: **Yao-Hua KAO**, Kaohsiung (TW);
Chieh-Chien CHEN, New Taipei City (TW)(73) Assignee: **GLOBAL MASTER TECH. CO., LTD.**, Kaohsiung (TW)(21) Appl. No.: **17/449,032**(22) Filed: **Sep. 27, 2021**(30) **Foreign Application Priority Data**

May 24, 2021 (TW) 110118665

Publication Classification(51) **Int. Cl.****H05K 3/34** (2006.01)**H05K 1/11** (2006.01)(52) **U.S. Cl.**CPC **H05K 3/3447** (2013.01); **H05K 1/115** (2013.01); **B23K 1/0016** (2013.01); **H05K 1/0298** (2013.01); **H05K 2201/09545** (2013.01); **H05K 2201/096** (2013.01); **B23K 2101/42** (2018.08)

(57)

ABSTRACT

An electronic-component carrier board includes carrier plates formed in a stack, and insulating layers each disposed between two adjacent ones of the carrier plates. Multiple conductive pins extend through the insulating layers and the carrier plates. Multiple conductive wires equal in length and width are provided. Each conductive wire is connected to one of the conductive pins, covered by one of the insulating layers, disposed between two adjacent ones of the carrier plates, and extends outwardly from the stack of the carrier plates. A wiring method for the electronic-component carrier board is also disclosed.

