

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0213160 A1 PARK et al.

Jun. 27, 2024 (43) **Pub. Date:**

(54) SEMICONDUCTOR DEVICE HAVING A REINFORCING INSULATING LAYER CORRESPONDING TO A VIA

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(21) Appl. No.: 18/535,143

Filed: Dec. 11, 2023 (22)

(30)Foreign Application Priority Data

Dec. 12, 2022 (KR) 10-2022-0173047

Publication Classification

(51) Int. Cl.

H01L 23/532 H01L 23/522 (2006.01)(2006.01)

(52)U.S. Cl.

CPC H01L 23/5329 (2013.01); H01L 23/5226

 $(2013.01); \dot{H}01L\ 23/49816\ (2013.01)$

(57)**ABSTRACT**

A semiconductor device includes a first wiring level layer including a lower wiring layer, a second wiring level layer on the first wiring level layer and including an upper wiring layer, a via level layer positioned between the first wiring level layer and the second wiring level layer and including a via connecting the lower wiring layer to the upper wiring layer, and a reinforcing insulating layer positioned between the lower wiring layer and the upper wiring layer in the via level layer.

