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YADAV et al.(10) **Pub. No.: US 2024/0178662 A1**(43) **Pub. Date: May 30, 2024**(54) **FULLY DIFFERENTIAL ESD CIRCUIT FOR
HIGH-FREQUENCY APPLICATIONS****Publication Classification**(51) **Int. Cl.**
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Diego, CA (US); **Khaled Mahmoud
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Diego, CA (US)(57) **ABSTRACT**

A differential ESD circuit is provided for protecting a pair of differential terminals of an integrated circuit from electrostatic shock. A first diode couples between a first terminal in the pair of differential terminals and a first resistor that couples to a voltage node of the integrated circuit. Similarly, a second diode couples between a second terminal in the pair of differential terminals and a second resistor that couples to the voltage node of the integrated circuit. The first and second resistors isolate the first and second terminals from a capacitive loading that would otherwise exist from the first and second diodes.

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