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(54) **DISPLAY PANEL AND DISPLAY DEVICE**

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(57) **ABSTRACT**

A display panel and a display device are disclosed. The display panel includes at least a part of plurality of first connection parts arranged at intervals along a first direction. Each pixel electrode is connected to one driving transistor through the first connection part. The first connection parts are further configured to connect signal lines. An area of each of the first connection parts disposed sequentially increases along the first direction, or the area of each of the first connection parts is the same. The area is an orthographic projection area of the first connection part on a base substrate.

