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(11) **CHANG et al.**(54) **MICROCONTROLLER, OPERATION  
SYSTEM AND CONTROL METHOD  
THEREOF**(52) **U.S. CL.**  
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*H03K 21/10* (2006.01)(57) **ABSTRACT**

A microcontroller is coupled to a detection circuit which generates a detection signal. The microcontroller includes a processing circuit and an input-output circuit. The processing circuit generates an output signal according to the detection signal. In response to the output signal being at a specific level, the processing circuit enables a reset signal. The input-output circuit includes a latch circuit and a counter circuit. The latch circuit latches the output signal to generate a latched signal. The counter circuit starts adjusting the count value in response to the reset signal being enabled. The counter circuit changes the level of the latched signal in response to the count value being equal to a predetermined value.

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