

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2023/0231504 A1

Kaliyannan Eswaran et al.

Jul. 20, 2023 (43) **Pub. Date:**

(54) GATE DRIVE GROUNDING SCHEME IN MOTOR DRIVE SYSTEMS FOR WIDE INPUT DC LINK VOLTAGE

(71) Applicant: Hamilton Sundstrand Corporation,

Charlotte, NC (US)

(72) Inventors: Pravinsharma Kaliyannan Eswaran,

Bangalore (IN); Nageswara Rao Kalluri, Bangalore (IN); David Frederick Brookes, Birmingham (GB); Sridhar Katakam, Bangalore (IN); Surendra Somasekhar Valleru,

Bangalore (IN)

- Appl. No.: 17/994,688
- Nov. 28, 2022 (22)Filed:
- (30)Foreign Application Priority Data (IN) 202211003318

Publication Classification

- (51) Int. Cl. (2006.01)H02P 27/08
- (52) U.S. Cl. CPC *H02P 27/08* (2013.01); H02P 2201/03 (2013.01)

(57)**ABSTRACT**

A motor drive system includes a direct current (DC) bus that provides a DC link voltage across a DC link capacitor, and a split DC link mid-point circuit connected in parallel with the DC link capacitor. The split DC link mid-point circuit establishes a mid-point reference based on the DC link voltage. A power inverter is in signal communication with the DC bus. The power inverter includes one or more gate driver units configured to drive one or more corresponding switches. Each gate driver unit includes a mid-point ground connection that is connected to the mid-point reference. The split DC link mid-point circuit can define a voltage divide that establishes the mid-point reference and can be used to monitor the DC link voltage.

