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ABSTRACT

A motor control system includes a direct current (DC) motor and a ripple count circuit. The DC motor includes a rotor induced to rotate in response to a drive current generated by a supply voltage. The rotation of the rotor generates a mechanical force that drives a component. The ripple count circuit includes an active filter circuit and a parasitic pulse cancellation circuit. The active filter circuit is configured to filter the drive current and to generate a pulsed signal containing at least one parasitic pulse. The parasitic pulse cancellation circuit is in signal communication with the ripple count circuit to receive the pulsed signal and to output a ripple count signal based on the pulsed signal. The ripple count signal excludes the at least one parasitic pulse.

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