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(54) **MICROPHONE ARRAY**

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**ABSTRACT**

A microphone array includes a four-channel serial peripheral interface, a core logic unit, a data receiving unit and a voice recognition unit. The four-channel serial peripheral interface includes a bit clock signal line, a frame clock signal line, and four data signal lines, the core logic unit includes a frequency divider module for converting the control signal and the clock signal to provide a bit clock signal and a frame clock signal. The data receiving unit includes a shift register and a buffer, the shift register is connected to four data signal lines and receives input data of the four digital microphones, and the buffer is connected to the shift register. The voice recognition unit is connected to the data receiving unit and receives microphone signals of the four digital microphones to perform voice recognition.

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