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(54) **DISPLAY DEVICE**

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(57) **ABSTRACT**

The invention provides a display device, comprising: a first substrate having a peripheral region and a display region, a plurality of pixel units disposed in the display region; a second metal layer electrically connected to the plurality of pixel units; a first planarization layer including a plurality of bump pad groups and a plurality of opening regions; a third metal layer having a position corresponding to the plurality of opening regions; a second substrate; a light shielding layer, a first color resist layer and a second planarization layer disposed on the second substrate; a second color resist layer disposed on the second substrate; wherein in the display region, the second color resist layer is adjacent to the first color resist layer, and in the peripheral region, the second color resist layer and the first color resist layer are stacked, and a position of the second color resist layer corresponds to the plurality of opening regions.

The diagram illustrates a cross-sectional view of a display device structure. The layers are labeled as follows from top to bottom: 101, M2, PL, BP, PS1, OC, 103, BM, and 102. Two dimensions are indicated with arrows: T1 is the thickness of the OC layer, and T3 is the thickness of the BP layer.