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(54) ACTIVE DEVICE SUBSTRATE

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(57)ABSTRACT

An active device substrate including a substrate, a transfer wire, a first insulating layer, a second insulating layer, a first semiconductor pattern, a second semiconductor pattern, a first connection structure, a second connection structure, a first gate, a second gate, a third insulating layer, a first source/drain, and a second source/drain is provided. The first insulating layer is located above the transfer wire. The second insulating layer is located above the first insulating layer. The first semiconductor pattern and the second semiconductor pattern are located between the first insulating layer and the second insulating layer. The first connection structure and the second connection structure are located above the second insulating layer and electrically connected to the transfer wire. The first gate and the second gate respectively overlap the first semiconductor pattern and the second semiconductor pattern.

$$260 \begin{cases} 262 \\ 264 \end{cases} 270 \begin{cases} 272 \\ 274 \end{cases}$$

