



US 20220376698A1

(19) **United States**(12) **Patent Application Publication**
CHO et al.(10) **Pub. No.: US 2022/0376698 A1**(43) **Pub. Date: Nov. 24, 2022**(54) **ANALOG-DIGITAL CONVERTER AND
OPERATING METHOD THEREOF****Publication Classification**(51) **Int. Cl.****H03M 1/46** (2006.01)**H03M 1/12** (2006.01)(52) **U.S. Cl.****CPC** **H03M 1/466** (2013.01); **H03M 1/1245**
(2013.01)(71) Applicant: **Samsung Electronics Co., Ltd.**,
Suwon-si (KR)(72) Inventors: **Youngsea CHO**, Seongnam-si (KR);
Wan KIM, Hwaseong-si (KR); **Jiseon
PAEK**, Suwon-si (KR); **Seunghyun
OH**, Seoul (KR)(73) Assignee: **Samsung Electronics Co., Ltd.**,
Suwon-si (KR)(21) Appl. No.: **17/560,400**(22) Filed: **Dec. 23, 2021**(30) **Foreign Application Priority Data**

May 24, 2021 (KR) 10-2021-0066338

(57)

ABSTRACT

Provided are an analog-to-digital converter and/or an operating method thereof. The analog-to-digital converter includes a sample/hold circuit, a digital-to-analog converter, a comparing circuit, and a control logic circuit, wherein the digital-to-analog converter includes a first capacitor connected to a first comparison node and a first filtering node, a first reference voltage switch connected to the first filtering node and connected to a first delivery node or a first transmission node, a first pre-charge switch connected to the first filtering node or the first delivery node, and a first pre-charge capacitor connected to the first pre-charge switch and a ground voltage.

