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(54) SEMICONDUCTOR DEVICES

(71) Applicant: Samsung Electronics Co., Ltd.,

Suwon-si (KR)

(72) Inventors: Chanwoo Shin, Seoul (KR); Hyuckjin

KANG, Seoul (KR); Donghwan LEE, Daejeon (KR); Jeonil LEE, Suwon-si (KR); Minwu KIM, Hwaseong-si (KR); Jungwoo SONG, Hwaseong-si (KR)

(73) Assignee: Samsung Electronics Co., Ltd.,

Suwon-si (KR)

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(57) **ABSTRACT**

A semiconductor device includes a bit line structure on a substrate, a lower contact plug on a portion of the substrate adjacent to the bit line structure, an upper contact plug including a first metal pattern on the lower contact plug and a second metal pattern contacting an upper surface and an upper sidewall of the first metal pattern, and a capacitor on the upper contact plug. The upper surface of the first metal pattern is above an upper surface of the bit line structure with respect to an upper surface of the substrate.

