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(54) **CLOCK GENERATION CIRCUIT AND VOLTAGE GENERATION CIRCUIT INCLUDING THE CLOCK GENERATION CIRCUIT**

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(57) **ABSTRACT**

A clock generation circuit includes a control clock generation circuit and first and second clock synchronization circuits. The control clock generation circuit compares a reference voltage with first and second feedback clock signals to generate first and second control clock signals. The first clock synchronization circuit makes the first and second feedback clock signals transit in synchronization with the first and second control clock signals. The second clock synchronization circuit generates first and second phase clock signals in synchronization with the first feedback clock signal and the second feedback clock signal.

