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#### (54) REVERSE RECOVERY CHARGE REDUCTION CIRCUIT

(71) Applicant: Ross Teggatz, The Colony, TX (US)

(72) Inventor: Ross Teggatz, The Colony, TX (US)

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#### **ABSTRACT** (57)

A method for reducing or eliminating current and voltage transients in a device including: providing the device, wherein the device comprises a transistor with a gate; and controlling the gate, wherein the gate is turned off to a non-zero value in a presence of the positive current flow and wherein the gate is turned on in a presence of the negative current flow; reducing the negative current flow; and reducing or eliminating current and voltage transients in the device.

