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LAM(10) **Pub. No.: US 2024/0223190 A1**(43) **Pub. Date: Jul. 4, 2024**(54) **TRANSIENT INSENSITIVE LEVEL SHIFTER**(52) **U.S. Cl.**(71) Applicant: **TEXAS INSTRUMENTS**
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(57)

ABSTRACT

In an example, a level shifter includes a low side having first and second inputs. The level shifter also includes a high side, where the high side has a latch having first and second terminals. The high side also includes a current comparator including first and second current mirrors. The high side includes isolation circuitry including first and second transistors, in which the first transistor has a first control terminal, the second transistor has a second control terminal, the first transistor is coupled between the first terminal and the first current mirror, the second transistor is coupled between the second terminal and the second current mirror, the first control terminal is coupled to the second current mirror, and the second control terminal is coupled to the first current mirror.

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