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Inoue et al.(10) **Pub. No.: US 2022/0352862 A1**(43) **Pub. Date: Nov. 3, 2022**(54) **METHODS FOR FABRICATION OF BONDED
WAFERS AND SURFACE ACOUSTIC WAVE
DEVICES USING SAME***H03H 9/17* (2006.01)*H03H 9/25* (2006.01)*H03H 9/54* (2006.01)*H03H 9/64* (2006.01)(71) Applicant: **Qorvo US, Inc.**, Greensboro, NC (US)(52) **U.S. Cl.**CPC *H03H 3/02* (2013.01); *H03H 9/02574*(2013.01); *H03H 9/02834* (2013.01); *H03H**9/02952* (2013.01); *H03H 3/10* (2013.01);*H03H 9/145* (2013.01); *H03H 9/17* (2013.01);*H03H 9/25* (2013.01); *H03H 9/54* (2013.01);*H03H 9/6406* (2013.01); *Y10T 29/42*

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(57)

ABSTRACT

A method of fabricating a bonded wafer with low carrier lifetime in silicon comprises providing a silicon substrate having opposing top and bottom surfaces, modifying a top portion of the silicon substrate to reduce carrier lifetime in the top portion relative to the carrier lifetime in portions of the silicon substrate other than the top portion, bonding a piezoelectric layer having opposing top and bottom surfaces separated by a distance T over the top surface of the silicon substrate, and providing a pair of electrodes having fingers that are inter-digitally dispersed on a top surface of the piezoelectric layer, the electrodes comprising a portion of a Surface Acoustic Wave (SAW) device. The modifying and bonding steps may be performed in any order. The modified top portion of the silicon substrate prevents the creation of a parasitic conductance within that portion during operation of the SAW device.

