

(19) **United States**(12) **Patent Application Publication****Tzadok et al.**(10) **Pub. No.: US 2024/0223195 A1**(43) **Pub. Date:****Jul. 4, 2024**(54) **PHASE ESTIMATION FOR HIGH
FREQUENCY SIGNALS**(52) **U.S. Cl.**CPC *H03L 7/191* (2013.01); *G06F 17/156*
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ABSTRACT(72) Inventors: **Asaf Tzadok**, New Castle, NY (US);
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A first 1:N frequency divider has an input configured to be coupled to one of two signals and a second 1:N frequency divider has an input configured to be coupled to another of the two signals. A mixer includes two inputs, where each input is coupled to an output of one of the first and second 1:N frequency dividers. A low-pass filter has an input coupled to an output of the mixer and an analog-to-digital converter (ADC) has an input coupled to an output of the low-pass filter. A data collection and analysis block repeatedly changes a phase of an output of the first 1:N divider, collects a set of digitized data generated by the ADC, and estimates the phase difference between the two signals based on the set of digitized data.

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