

(19) United States

(12) Patent Application Publication (10) Pub. No.: US 2024/0178817 A1 Andre et al.

(43) **Pub. Date:**

May 30, 2024

(54) FILTERING DEVICE AND METHOD

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Appl. No.: 18/520,167

Filed: (22)Nov. 27, 2023

Foreign Application Priority Data (30)

Nov. 28, 2022 (FR) 2212378

Publication Classification

(51) Int. Cl. H03H 11/04 (2006.01)H03K 3/037 (2006.01)H03K 17/687 (2006.01)H04B 1/04 (2006.01) (52) U.S. Cl.

CPC H03H 11/04 (2013.01); H03K 3/037 (2013.01); H03K 17/6871 (2013.01); H04B *1/04* (2013.01)

(57)**ABSTRACT**

In embodiments, a radio frequency transmitter comprising at least one filtering circuit is provided. The filtering circuit includes a series/parallel shift register comprising a binary input and N binary outputs, with N being an integer greater than or equal to OSR, OSR being an integer greater than or equal to 2. The binary outputs ranging from 0 to N-1, the register receiving a binary data signal at a data frequency on its input and implementing shifts on the N binary outputs at a frequency equal to a multiplier of the data frequency and OSR. The filtering circuit further comprising a first circuit defined by N coefficients C_i. For each non-zero coefficient C_i , a signal determined by the coefficient C_i and by the corresponding one of the binary outputs. The filtering circuit further comprising and an adder circuit delivering an output equal to the sum of analog signals.

