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(54) **DRIVING CIRCUIT AND DISPLAY PANEL**

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(57) **ABSTRACT**

A driving circuit and a display panel are provided. The driving circuit at least includes first fan-out lines, second fan-out lines, and data lines. The first fan-out lines are located in the display region and electrically connected to the driving chip. The second fan-out lines are located in the display region and connected to the first fan-out lines. The data lines are located in the display region and connected to the second fan-out lines. Wherein, the first fan-out lines, the second fan-out lines, and the data lines are located in different layers, and the second fan-out lines are located between the first fan-out lines and the data lines.

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