



Software Safety Requirements and Architecture Lane Assistance

Document Version: 1.0



Document history

Date	Version	Editor	Description
3/25/2018	1.0	Taketo Kobayashi	First attempt

Table of Contents

Document history

Table of Contents

Purpose

Inputs to the Software Requirements and Architecture Document

Technical safety requirements

Refined Architecture Diagram from the Technical Safety Concept

Software Requirements

Refined Architecture Diagram

Purpose

The purpose of the software safety requirments and architecture document is to identify new detailed requirements and allocate these software requirements to component level diagrams for the lane assistance functional safety project as pertain to the potential malfunctions of the electrical and electronic systems as defined by ISO 26262 standard, tailored.

Inputs to the Software Requirements and Architecture Document

Technical safety requirements

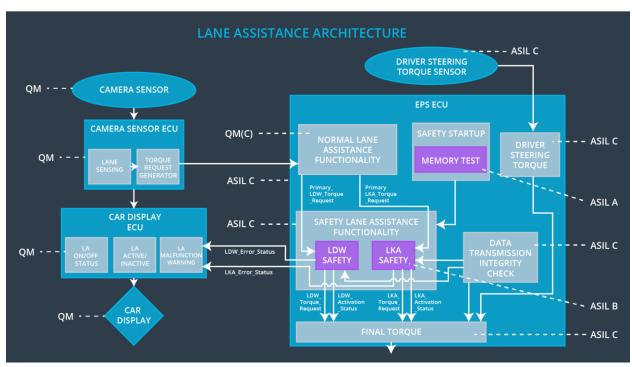
Technical Safety Requirements related to Functional Safety Requirement 01-01 are:

ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Architecture Allocation	Safe State
Technical Safety Requirement 01-01-01	The LDW safety component shall ensure that the amplitude of the 'LDW_Torque_Request' sent to the 'Final electronic power steering Torque' component is below 'Max_Torque_Amplitude.	O	50 ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 01-01-02	As soon as the LDW function deactivates the LDW feature, the 'LDW Safety' software block shall send a signal to the car display ECU to turn on a warning light.	С	50 ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 01-01-03	As soon as a failure is detected by the LDW function, it shall deactivate the LDW feature and the	С	50 ms	LDW Safety block	Set lane departure warning torque to zero

	'LDW_Torque_Request' shall be set to zero.				
Technical Safety Requirement 01-01-04	The validity and integrity of the data transmission for 'LDW_Torque_Request' signal shall be ensured.	С	50 ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 01-01-05	Memory test shall be conducted at start up of the EPS ECU to check for any faults in memory.	А	Ignition cycle	Data Transmission Integrity Check	Set lane departure warning torque to zero
Technical Safety Requirement 01-02-01	The LDW safety component shall ensure that the fequency of the 'LDW_Torque_Request' sent to the 'Final electronic power steering Torque' component is below 'Max_Torque_Fequency.	С	50 ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 02-01-01	The LKA safety component shall ensure that the duration of the lane keeping assistance torque applied is less than Max_Duration.	С	500 ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 02-01-02	As soon as the LKA function deactivates the LKA feature, the 'LKA Safety' software block shall send a signal to the car display ECU to turn on a warning light.	С	500 ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 02-01-03	As soon as a failure is detected by the LKA function, it shall deactivate the LKA feature and the 'LKA_Torque_Request' shall be set to zero.	С	500 ms	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement	The validity and integrity of the data transmission for 'LKA_Torque_Request'	С	500 ms	LDW Safety block	Set lane departure warning

02-01-04	signal shall be ensured.				torque to zero
Technical Safety Requirement 02-01-05	Memory test shall be conducted at start up of the EPS ECU to check for any faults in memory.	А	Igniton cycle	LDW Safety block	Set lane departure warning torque to zero
Technical Safety Requirement 02-02-01	The LKA safety component shall ensure that the loss of camera sensor torque request transmission will deactivate the LKA feature and the 'LKA_Torque_Request' shall be set to zero.	С	500 ms	LDW Safety block	Set lane departure warning torque to zero

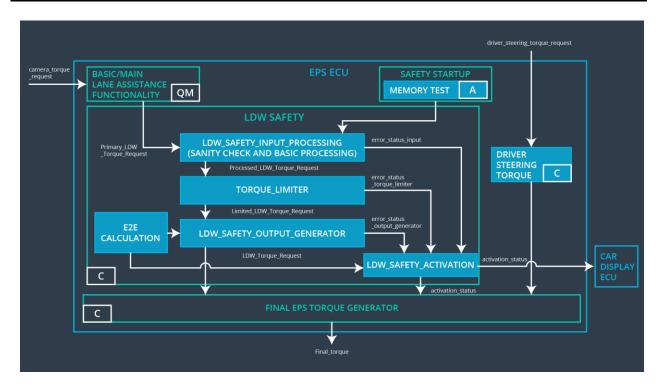
Refined Architecture Diagram from the Technical Safety Concept



Software Requirements

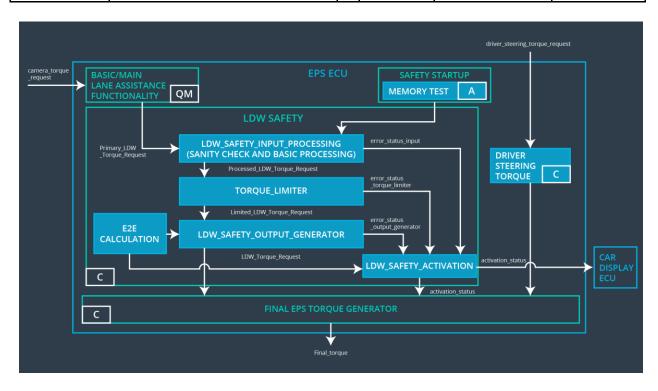
Lane Departure Warning (LDW) Amplitude Malfunction Software Requirements:

ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 01	The LDW safety component shall ensure that the amplitude of the LDW_Torque_Request sent to the Final Electronic Power Steering Torque component is below Max_Torque_Amplitude	С	50 ms	LDW Safety block	Set lane departure warning torque to zero



ID	Software Safety Requirement	AS-L	Allocation Software Elements	Safe State
Software Safety Requirement 01-01-01-01	The input signal "Primary_LDW_Torq_Req" shall be read and pre-processed to determine the torque request coming from the "Basic/Main LAFunctionality" SW Component. Signal "processed_LDW_Torq_R eq"shall be generated at the end of the processing.	С	LDW_SAFETY_INPUT_P ROCESSING	N/A
Software Safety Requirement 01-01-02	In case the "processed_LDW_Torq_Req" signal has a value greater than "Max_Torque_Ampltide_LD W"(maximum allowed safe torque), the torque signal "limited_LDW_Torq_Req" shall be set to 0, else "limited_LDW_Torq_Req" shall take the value of "processed_LDW_Torq_Req".	O	TORQUE_LIMITER	"limited_LDW_ T orq_Req" = 0(Nm=Newtonm eter)
Software Safety Requirement 01-01-03	The "limited_LDW_Torq_Req"shall be transformed into a signal "LDW_Torq_Req" whichis suitable to be transmittedoutside of the LDW Safetycomponent ("LDW Safety") to the "Final EPS Torque"component. Also see SofSafReq 01-02-01-03	С	LDW_SAFETY_OUTPUT _GENERATOR	LDW_Torq_Req = 0 (Nm)

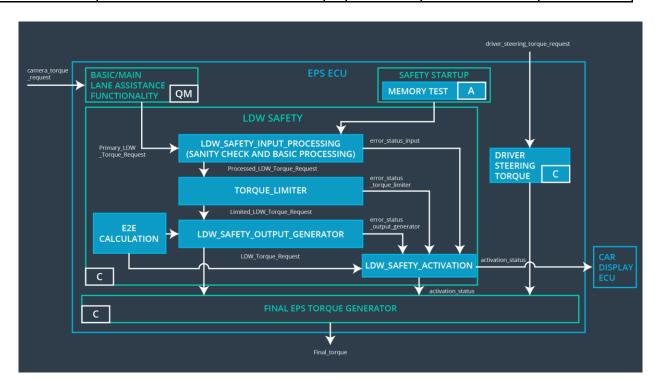
ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 01-01-02	As soon as a failure is detected by the LDW function, it shall deactivate the LDW feature and the LDW_Torque_Request shall be set to zero	С	50 ms	LDW Safety block	Set lane departure warning torque to zero



ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 01-01-02-01	Each of the SW elements shall output a signal to indicate any error which is detected by the element. Error signal = error_status_input(LDW_SAFET Y_INPUT_PROCESSING), error_status_torque_limiter(TOR QUE_LIMITER),	С	All	N/A

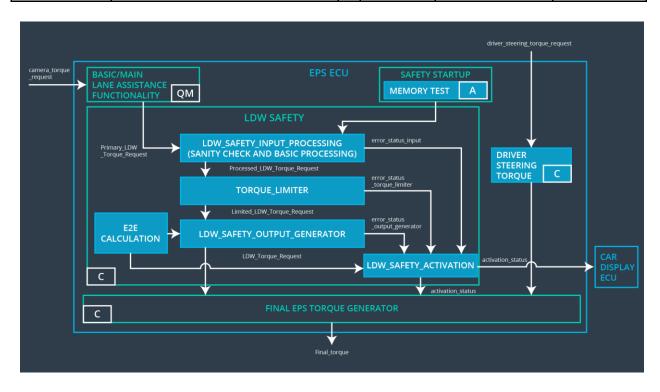
	error_status_output_gen(LDW_ SAFETY_OUTPUT_GENERAT OR)			
Software Safety Requirement 01-01-02-02	A software element shall evaluate the error status of all the other software elements and in case any 1 of them indicates an error, it shall deactivate theLDW feature("activation_status"=0)	С	LDW_SAFETY _ACTIVATION	Activation_statu s = 0 (LDW function deactivated)
Software Safety Requirement 01-01-02-03	In case of no errors from the software elements, the status of the LDW feature shall be set to activated ("activation_status"=1)	С	LDW_SAFETY _ACTIVATION	N/A
Software Safety Requirement 01-01-02-04	In case an error is detected by any of the software elements, it shall set the value of its corresponding torque to 0 so that "LDW_Torq_Req" is set to 0	С	All	LDW_Torq_Re q= 0 (Nm)
Software Safety Requirement 01-01-02-05	Once the LDW functionality has been deactivated, it shall stay deactivated till the time the ignition is switched from off to on again.	С	LDW_SAFETY _ACTIVATION	Activation_statu s = 0 (LDW function deactivated)

ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 01-01-03	As soon as a failure is detected by the LDW function, it shall deactivate the LDW feature and the LDW_Torque_Request shall be set to zero	С	50 ms	LDW Safety block	Set lane departure warning torque to zero



ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 01-01-03-01	When the LDW function is deactivated (activation_status set to 0), the activation_status shall be sent to the car displayECU.	С	LDW_SAFET Y_ACTIVATIO N, CarDisplay ECU	N/A

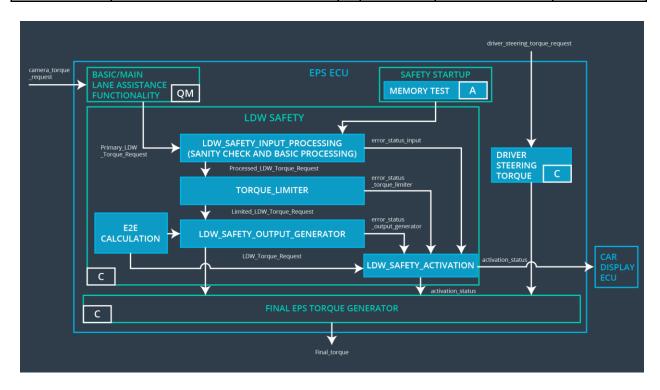
ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 01-01-04	The validity and integrity of the data transmission for LDW_Torque_Request signal shall be ensured	С	50 ms	Data Transmission Integrity Check	N/A



ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 01-01-04-01	Any data to be transmitted outside of the LDW Safetycomponent ("LDW Safety")including "LDW_Torque_Req"and "activation_status" (seeSofSafReq 01-01-02-02) shall beprotected by an End2End(E2E)protection mechanism	С	E2ECalc	LDW_Torq_Req = 0 (Nm)

Software Safety Requirement 01-01-04-02	The E2E protection protocol shall contain and attach the control data: alive counter (SQC) and CRC to the data to be transmitted.	С	E2ECalc	LDW_Torq_Req = 0 (Nm)
--------------------------------------------------	-----------------------------------------------------------------------------------------------------------------------------------	---	---------	--------------------------

ID	Technical Safety Requirement	A S I L	Fault Tolerant Time Interval	Allocation to Architecture	Safe State
Technical Safety Requirement 01-01-05	Memory test shall be conducted at start up of the EPS ECU to check for any faults in memory	A	Ignition cycle	Data Transmission Integrity Check	Set lane departure warning torque to zero



ID	Software Safety Requirement	A S I L	Allocation Software Elements	Safe State
Software Safety Requirement 01-01-05-01	A CRC verification check over the software code in the Flash memory shall be done every time the ignition is switched from off to on to check for any corruption of content.	A	MEMORYTES T	Activation_status = 0
Software Safety Requirement	Standard RAM tests to check the data bus, address bus and device integrity shall be done	Α	MEMORYTES T	Activation_status = 0

01-01-05-02	every time the ignition is switched from off to on (E.g.walking 1s test, RAM pattern test. Refer RAM and processor vendor recommendations)			
Software Safety Requirement 05-03	The test result of the RAM or Flash memory shall be indicated to the LDW_Safety component via the "test_status" signal	Α	MEMORYTES T	Activation_status = 0
Software Safety Requirement 05-04	In case any fault is indicated via the "test_status" signal the INPUT_LDW_PROCESSING shall set an error on error_status_input (=1) so that the LDW functionality is deactivated and the LDWTorque is set to 0	Α	LDW_SAFET Y_INPUT_PR OCESSING	Activation_status = 0

Refined Architecture Diagram

