Computer Organization & Architecture (23CS304) Assignment - 3 Imp Questions.

- 1. Define Data types & Explain about Fixed-Point Representation?
- 2. Define Data types & Explain about Floating -Point Representation?
- 3. What is the Hardware implementation for signed-magnitude addition and subtraction? And explain the flowchart for the hardware algorithm?
- 4. What is the Multiplication Algorithms? And how to implement for signed-magnitude data and draw the flowchart for multiplication operation?
- 5. Explain about Booth's algorithm for multiplication of signed-2's complement number and draw the flowchart for Booth's algorithm for multiplication?
- 6. Explain about division of two-fixed point binary numbers in signed-magnitude data and draw the flowchart for divide operation?
- 7. What are the registers for floating-point Arithmetic operations?
- 8. What is the multiplication of two floating-point numbers?
- 9. What is the floating-point numbers for division?
- 10. Design a Decimal Arithmetic Unit to perform BCD- Addition and BCD-Subtraction?