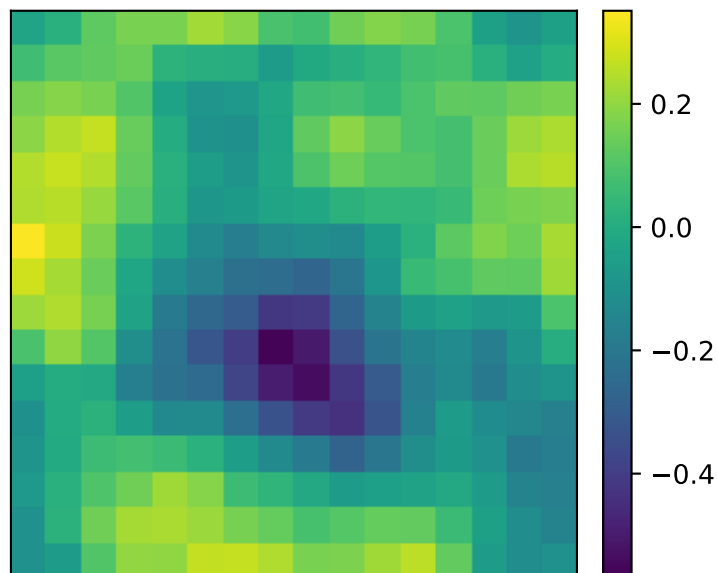
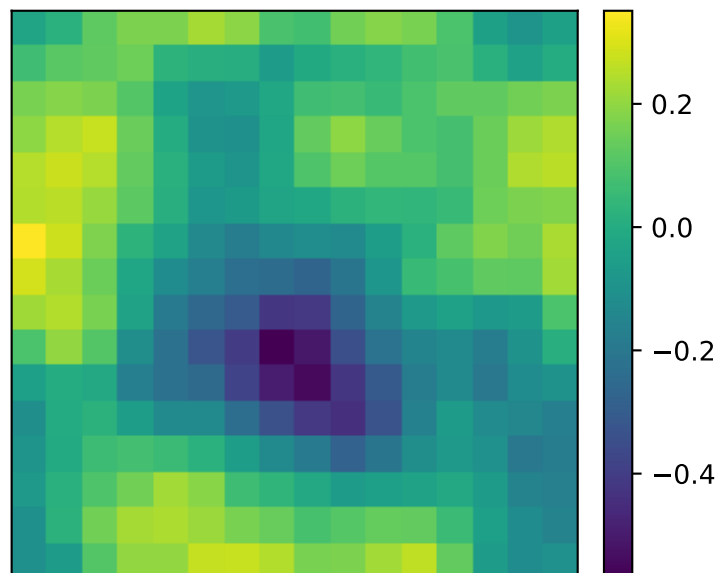


sample 1 full relL2=1.04

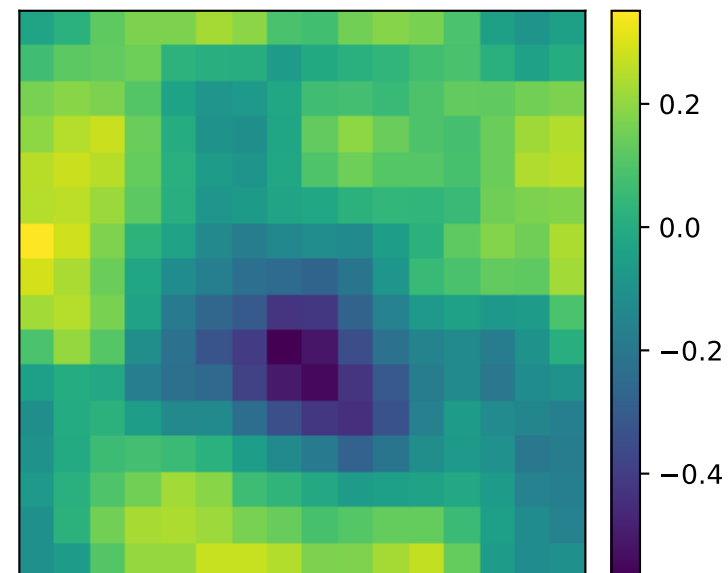
GT t=0



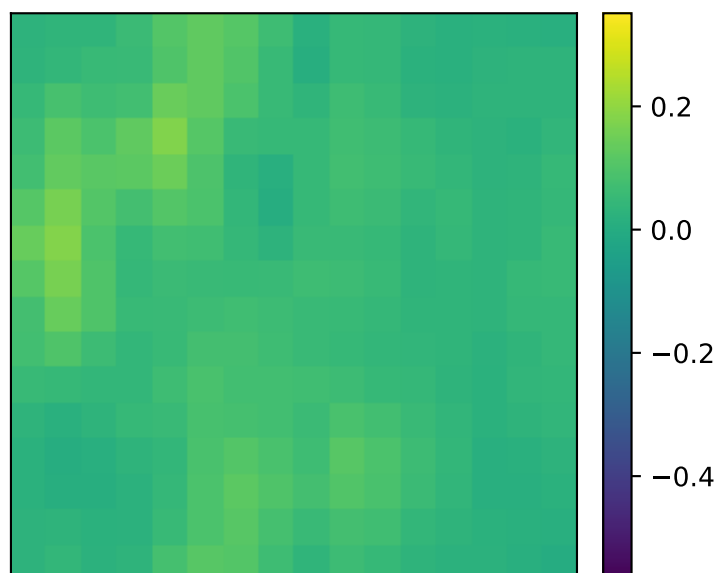
GT t=3



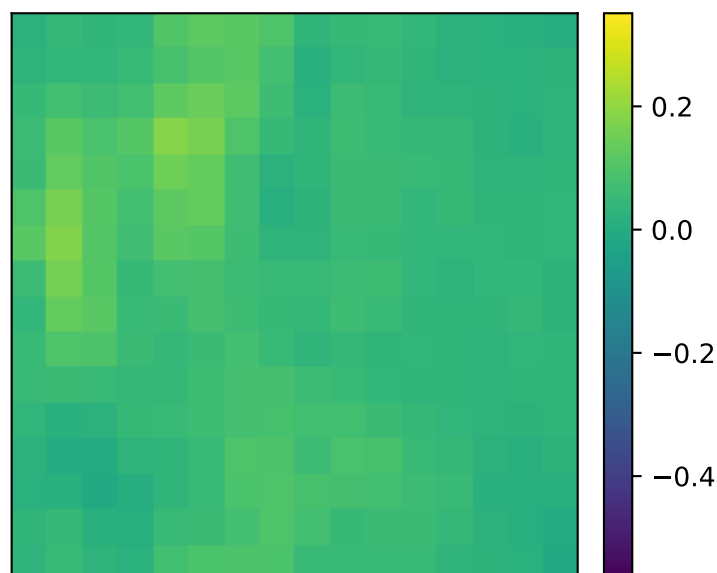
GT t=5



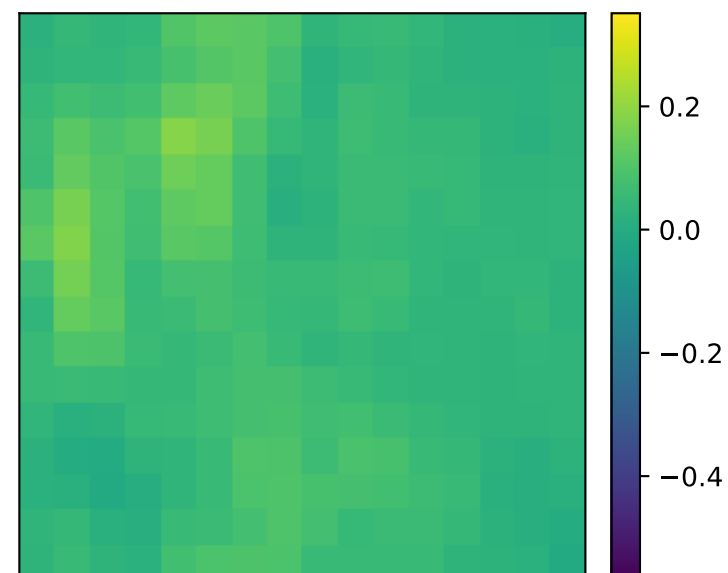
Pred t=0



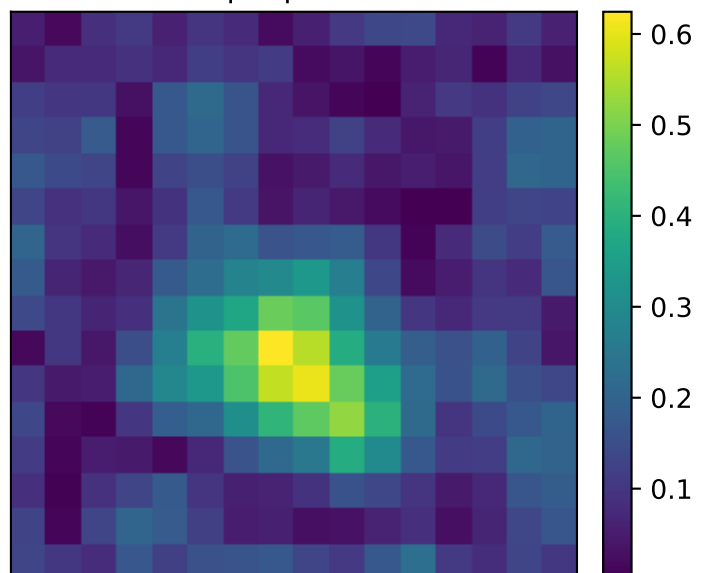
Pred t=3



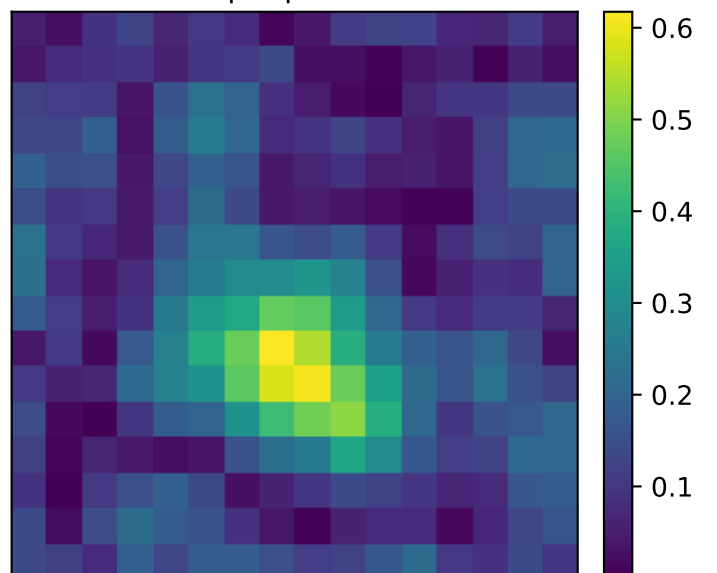
Pred t=5



|Err| t=0



|Err| t=3



|Err| t=5

