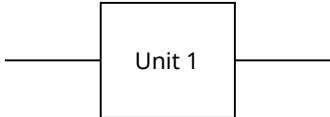
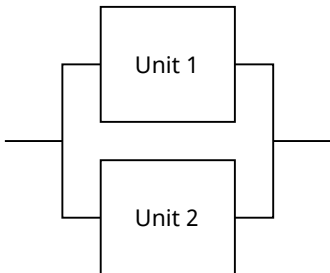


Single Component



Dual Redundancy



Triple Modular Redundancy

