

Cadence Optional Project-2: Inverter Schematic and Layout

[Start Assignment](#)

Due Friday by 11:59pm **Points** 100 **Submitting** a file upload

Follow all the steps shown in the [videos of Cadence Virtuoso tool](#)- Inverter Schematic and Layout. (You can re-use the schematic and simulation results from previous assignment.)

This optional project is due on **Thursday (04/22) 11:59 PM**.

Upload one PDF file containing the following screenshots/pages:

- (1) Title page
- (2) Schematic Screenshot (You can re-use the schematic and simulation results from previous assignment)
- (3) Schematic Simulation Results (You can re-use the schematic and simulation results from previous assignment)
- (4) Layout Screenshot
- (5) DRC, LVS screenshots, showing no errors and a match between schematic and layout views.
- (6) Post Layout Simulation on Extracted View.
- (7) Discussion/Reflections on the tool, and what you learnt. (1 - 2 paragraphs)