

Curren state		TMS=0		TMS=1	
0000(run test or idle)	bs_en	0000(run test or idle)	bs_en	0001(select DR scan)	bs_en
0001(select DR scan)	bs_en	0011(capture DR)	Clockdr, bs_en	1001(select IR)	Select, bs_en
0010(shift dr)	Shiftdr, clockdr, bs_en	0010(shift DR)	Shiftdr, clockdr, bs_en	0110(exit 1 DR)	bs_en
0011(capture DR)	Clockdr, bs_en	0010(shift DR)	Shiftdr, clockdr, bs_en	0110(exit 1 DR)	bs_en
0100(update dr)	Updatedr, bs_en	0000(Run Test idle)	bs_en	0001(select DR scan)	bs_en
0101(exit2 DR)	bs_en	0010(shift DR)	Shiftdr, clockdr, bs_en	0100(update dr)	Updatedr, bs_en
0110(exit 1 DR)	bs_en	0111(pause DR)	bs_en	0100(update dr)	Updatedr, bs_en
0111(pause DR)	bs_en	0111(pause DR)	bs_en	0101(exit2 DR)	bs_en
1000(test-logic rest)	select	0000(run test or idle)	bs_en	1000(test-logic rest)	select
1001(select IR)	select	1011(capture ir)	Clockir, select, bs_en	1000(test-logic rest)	select
1010(shift ir)	Shiftir, clockir, select, bs_en	1010(shift ir)	Shiftir, clockir, select, bs_en	1110(exit1 ir)	Select, bs_en
1011(capture ir)	Clockir, select, bs_en	1010(shift ir)	Shiftir, clocker, select, bs_en	1110(exit1 ir)	Select, bs_en
1100(update ir)	Updateir, select, bs_en	0000(run test or idle)	bs_en	0001(select DR scan)	bs_en
1101(exi21 ir)	Select, bs_en	1010(shift ir)	Shiftir, clockir, select	1100(update ir)	Updateir, select
1110(exit1 ir)	Select, bs_en	1111(pause ir)	select	1100(capture ir)	Updateir, select
1111(pause ir)	Select, bs_en	1111(pause ir)	select	1101(exi21 ir)	select