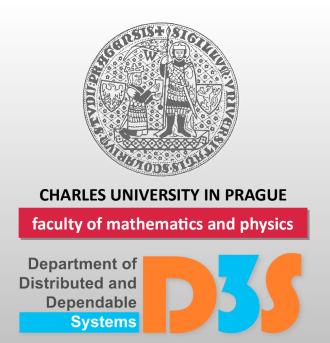
SPARC V9

Crash Dump Analysis 2015/2016





SPARC V9 overview

64-bit RISC architecture

- Each instruction is four bytes long
- Only load/store instructions with memory operands
- Orthogonal instruction set
- 32 GPRs, more in register windows
- Big-endian









SPARC V9 overview (2)



- Branch delay slots
- Mandatory alignment of memory accesses
- Register windows
 - Somewhat explicit memory stack









SPARC V9 manuals



- http://sparc.org/wp-content/uploads/2014/01/SPARCV9.pdf.gz
- SPARC Joint Programming Specification (JPS1): Commonality
 - www.fujitsu.com/downloads/PRMPWR/JPS1-R1.0.4-Common-pub.pdf
- Oracle SPARC Architecture 2015
 - http://www.oracle.com/technetwork/server-storage/sun-sparc-enterprise/documentation/sparc-architecture-2015-2868130.pdf
- Processor supplements
 - http://www.oracle.com/technetwork/server-storage/sun-sparc-enterprise/documentation/index.html









SPARC V9 ABI

SPARC Compliance Definition 2.4.1

- http://sparc.org/wp-content/uploads/2014/01/SCD.2.4.1.pdf.gz
- Authoritative source of information
- We will use a simplified view sufficient for common cases (integer arguments, etc.)









SPARC V9 registers

32 64-bit GPRs

- r0 − r31
 - r0 reads as zero, writes ignored
 - $r0 r7 \leftrightarrow g0 g7$ (Globals)
 - $r8 r15 \leftrightarrow o0 o7$ (Outs)
 - $r16 r23 \leftrightarrow l0 l7$ (Locals)
 - $r24 r31 \leftrightarrow i0 i7$ (Ins)









SPARC V9 registers (2)

Program Counter

- pc current instruction
- npc next instruction if no trap occurs
 - Address of the target for branches

Integer Condition Codes Register

- ccr
 - icc codes for 32-bit interpretation
 - xcc codes for 64-bit interpretation









Register Windows



- At any time, only one is active (current)
- Registers r8 r31 alias Outs, Locals and Ins of the current window
- Window n's Outs overlap with window ((n + 1) % NWINDOW)'s Ins

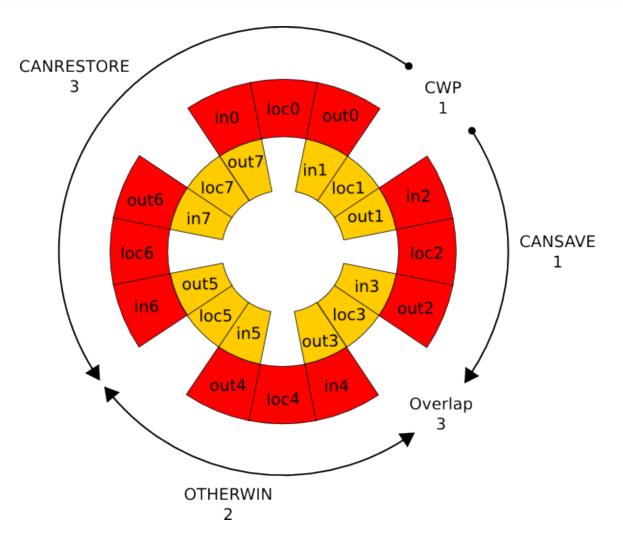








Register Windows (2)











Register Windows (3)

- CWP, CANSAVE, CANRESTORE and (OTHERWIN and CLEARWIN) are registers which define the state of the windowing mechanism
 - Invariant:

NWINDOW - 2

CANSAVE + CANRESTORE + OTHERWIN









Register Windows (4)

CWP – Current Window Pointer

- Corresponds to the current function
- CWP ← (CWP + 1) % NWINDOW on function call (SAVE instruction)
- CWP ← (CWP 1) % NWINDOW on function return (RESTORE instruction)









Register Windows (5)

- CANSAVE number of momentarily available windows for function call nesting
 - CANSAVE ← CANSAVE 1 on SAVE
 - CANSAVE ← CANSAVE + 1 on RESTORE
 - CANSAVE = 0 on SAVE => window spill TRAP









Register Windows (6)

- CANRESTORE number of momentarily available windows for function call returning
 - CANRESTORE ← CANRESTORE 1 on RESTORE
 - CANRESTORE ← CANRESTORE + 1 on SAVE
 - CANRESTORE = 0 on RESTORE => window fill TRAP









Register Windows (7)

- The stack is a backing store for register windows and register windows are caching parts of the stack
 - Window spill trap handler
 - The OS saves the window's Ins and Locals on the stack
 - Window fill trap handler
 - The OS restores the window's Ins and Locals from the stack









Flat Mode



- Simpler design
- More deterministic function duration times
- Poorer performance
- Up to GCC version 4.0.2
 - gcc -mflat









Flat Mode (2)



- No SAVE and RESTORE instructions
- 32 GPRs registers, much like e.g. MIPS
- We will not assume this mode









ABI in a Nutshell

- First 6 integer arguments passed in %o0 %o5
 - Other or additional arguments passed on stack
- Return value in %i0
- Return address in %i7
 - But need to add 8
- Stack pointer in %sp
 - But need to add 2047
- Frame pointer in %fp
 - But need to add 2047









ABI in a Nutshell (2)

- Stack frame needs to be 16B aligned
- Stack frame has a special format
 - Window save area for Ins and Locals
 - Stack bias of 2047
 - Larger stack frames can be efficiently accessed using 13-bit signed immediate offsets in instructions









ABI in a Nutshell (3)



$$00 - 05$$
, 07 , $g1$, $g4 - g5$

Non-volatile (preserved, callee-saved) registers

$$i0 - i7$$
, $10 - 17$, $o6$

Registers reserved for system

$$g6 - g7$$

Registers reserved for application

$$g2 - g3$$









SPARC V9 instructions

Only few hundreds of instructions

- Every instruction is 4B long, 4B-aligned
- Variants with register or immediate operand

Informal classification

- General purpose (arithmetic, logic, branch, etc.)
- System instructions (privileged operations)
- FPU instructions
- SIMD instructions (VIS I, VIS II)









SPARC V9 instructions (2)

- Most general purpose instructions have three operands
 - register register register
 - register immediate register
- INST rs1, rs2, rd
 - rd ← rs1 INST rs2
- ADD %i0, %i1, %l3
 - %I3 ← %i0 ADD %i1









SPARC V9 instructions (3)

Load / Store instructions

- LD [%rs1 + simm13], %rd
- LD [%rs1 + %rs2], %rd
- ST %rd, [%rs1 + simm13]
- ST %rd, [%rs1 + %rs2]
- Size suffixes (load / store instructions)
 - UB/SB (unsigned/signed byte), UH/SH (unsigned/signed halfword), UW/SW (unsigned/signed word), X (extended word)









SPARC V9 instructions (4)

Logical instructions

- Instructions with cc suffix modifies %icc and %xcc
 - Also with addition and subtraction instructions
- Instructions with n suffix negate %rs2 before applying

Synthetic instructions

- Not real instructions
- Understood by the assembler
- Aliases for common uses of the real instructions









Common instructions

- CALL, JMPL, Bcccond, BRrcond, RET, RETL
- SAVE, RESTORE, RETURN, NOP
- MOV, ADD, XOR, OR, AND, ANDcc, INC, DEC, CMP, SUB, SUBcc, SLLX, SRLX
- LDX, STX, CLRX
- SETHI









Common instructions (2)

CALL

- Call function
- Both real and synthetic instruction
 - Synthetic: JMPL address, %o7

JMPL address, %rd

- Jump and link
- %npc ← address
- %pc ← %pc + 4 (delay slot)









Common instructions (3)

Bccond

- (delayed) Branch on Integer Condition Code
 - Bccond{,a}{,pt|,pn} %icc, address
 - Bccond{,a}{,pt|,pn} %xcc, address
 - ccond is A (always), N (never), [N]E ([not] equal), G (greater), LE (less or equal), GE (greater or equal), L (less), etc.
 - Prediction bit
 - pn probably not taken
 - pt probably taken
 - Anul bit
 - a whether or not to cancel the delay instruction









Common instructions (4)

BRrcond

- (delayed) Branch on Register Condition
 - BRrcond{,a}{,pt|,pn} %rs1, address
 - BRrcond{,a}{,pt|,pn} %rs1, address
 - rcond is [N]Z ([not] zero), LEZ (<= 0), LZ (< 0), GZ (>0), GEZ (>= 0)
 - Prediction bit
 - pn probably not taken
 - pt probably taken
 - Anul bit
 - a whether or not to cancel the delay instruction









Common instructions (5)

RET

- Return from function
- Synthetic
- JMPL %i7+8, %g0

RETL

- Return from leaf function
- Synthetic
- JMPL %o7+8, %g0









Common instructions (6)

SAVE

- Allocate a new register window
 - Current Outs become new Ins
 - "ADD %rs1, imm, %rd"
 - %rs1 is from the current window
 - %rd is from the new window
 - SAVE %sp, -192, %sp

RESTORE

- Inverse operation to SAVE
- RESTORE %rs1,imm,%rd
 - RESTORE %i0, %l1, %o0
 - "like ADD"
 - Can be used to perform last-minute arithmetics on the result
 - %sp reverted by virtue of switching to the previous window









Common instructions (7)

RETURN

- Combination of RET and RESTORE
- Mind the delay slot

NOP

No operation

MOV

- Move register or simm13 to register
- Synthetic









Common instructions (8)

- ADD, XOR, OR, AND, ANDcc, INC, DEC, CMP, SUB, SUBcc, SLLX, SRLX
 - Add, exclusive OR, logical OR, logical AND, increment, decrement, compare, subtract, shift left logical, shift right logical
- LDX, STX, CLRX
 - Load from Memory, Store to Memory, Clear Memory









Common instructions (9)

SETHI

- Set high 22 bits of the source to result
 - sethi %hi(variable), %g1
 - Idx [%g1 + %lo(variable)], %g4
 - or %g1, %lo(variable), %g1









Function Prologue

save %sp, -imm, %sp

• • •









Function Epilogue

ret return %i7 + 0x8 restore R1, imm, R2 nop



. . .







Stack and Code Example

- Remember the a(), b() and c() from previous lessons?
 - Compile using gcc -01 -m64
 - Disassemble and single step main() and a()
 - Observe the stack









Stack and Code Example (2)

a: save %sp, -0xc0, %sp a+4: call +0x10 a+8: mov %i0, %o0

a+0xc: ret

a+0x10: restore %g0, %o0, %o0

main: save %sp, -0xc0, %sp main+4: call -0x34 <a> main+8: mov %i0, %o0

main+0xc: ret

main+0x10: restore %g0, %o0, %o0









Initial state

- No instructions executed
- Inherited stack pointer from main()'s caller

main: save %sp, -0xc0, %sp main+4: call -0x34 <a> main+8: mov %i0, %o0

main+0xc: ret

main+0x10: restore %g0, %o0, %o0









Allocate a new register window

- 192 bytes of stack space allocated
- Old Outs became new Ins

main: save %sp, -0xc0, %sp main+4: call -0x34 <a> main+8: mov %i0, %o0

main+0xc: ret

main+0x10: restore %g0, %o0, %o0

Oxfffffff7fffba8: 0
Oxfffffff7ffffbb0: 0
Oxfffffff7ffffbb8: 0
Oxfffffff7ffffbb8: 0
Oxfffffff7ffffbc0: 0
Oxfffffff7ffffbc8: 0
Oxfffffff7ffffbd0: 0
Oxfffffff7ffffbd8: 0
Oxfffffff7ffffbd8: 0

Oxfffffff7ffffbe8: Oxffffffff7ffffd18 Oxffffffff7ffffbf0: Oxffffffffffffd28 Oxffffffff7ffffbf8: test.sparc`environ Oxfffffff7ffffc00: 0x100000000

0xfffffff7ffffc08: 0x1c00

Oxfffffff7ffffc10: Oxffffffff7ffff461 Oxffffffff7ffffc18: start+0x7c

0xfffffff7ffffc20: 4

0xfffffff7ffffc28: 0xffffffff7ffffd28

0xfffffff7fffc30: 5

0xfffffff7ffffc38: 0xffffffffffffda8

Oxfffffff7fffc40: 0 Oxffffffffffffc48: 0 Oxffffffff7ffffc50: 0 Oxffffffffffffffc60: 1









Call a()

- No control transfer yet
- %o7 ← main+4
- %npc ← a
- %pc ← main+8

main: save %sp, -0xc0, %sp main+4: call -0x34 <a>

main+8: mov %i0, %o0

main+0xc: ret

main+0x10: restore %g0, %o0, %o0

```
Oxfffffff7ffffba0: 0
Oxffffffff7fffba8: 0
```

0xfffffff7fffbb0: 0

0xfffffffbb8: 0

0xfffffff7ffffbc0: 0

Oxfffffff7ffffbc8: 0 Oxffffffff7ffffbd0: 0

0xfffffff7fffbd8: 0

0xfffffff7ffffbe0: 1

Oxffffffffffbe8: Oxfffffffffffd18 Oxffffffffffbf0: Oxffffffffffffd28

0xffffffffc00: 0x10000000

0xfffffff7ffffc08: 0x1c00

0xfffffff7fffc10: 0xfffffff7ffff461

0xffffffffffffc18: _start+0x7c

0xfffffff7ffffc20: 4

0xfffffff7ffffc28: 0xffffffff7ffffd28

0xfffffff7fffc30: 5

0xffffffff7ffffc38: 0xffffffffffffda8

0xfffffff7fffc40: 0 0xffffffff7ffffc48: 0

0xfffffff7ffffc50: 0

0xfffffff7ffffc58: 0









Delay slot instruction

 Copy incoming argument to outgoing argument

main: save %sp, -0xc0, %sp main+4: call -0x34 <a> main+8: mov %i0, %o0

main+0xc: ret

main+0x10: restore %g0, %o0, %o0

Oxffffffffba0: 0
Oxfffffffffba8: 0
Oxffffffffffbb0: 0
Oxfffffffffffbb8: 0
Oxfffffffffffbc0: 0
Oxfffffffffffbc8: 0
Oxfffffffffffbd0: 0
Oxfffffffffffbd8: 0
Oxfffffffffffbb8: 0

Oxfffffff7ffffbe8: Oxffffffffffffd18 Oxffffffffffffbf0: Oxfffffffffffffd28 Oxffffffffffffbf8: test.sparc`environ Oxffffffffffffc00: 0x100000000

0xfffffff7ffffc08: 0x1c00

0xfffffff7ffffc10: 0xffffffff7ffff461 0xffffffffffffc18: _start+0x7c

0xfffffff7ffffc20: 4

0xfffffff7ffffc28: 0xffffffff7ffffd28

0xfffffff7fffc30: 5

0xfffffff7ffffc38: 0xffffffffffffda8

Oxfffffff7fffc40: 0 Oxfffffff7ffffc48: 0 Oxfffffff7ffffc50: 0 Oxfffffff7ffffc58: 0 Oxffffffff7ffffc60: 1









a: save %sp, -0xc0, %sp a+4: call +0x10 a+8: mov %i0, %o0

a+0xc: ret

a+0x10: restore %g0, %o0, %o0

Allocate a new register window

- 192 bytes of stack space allocated
- Old Outs became new Ins

```
0xfffffff7ffffba0: 0
0xfffffff7ffffba8: 0
0xfffffff7ffffbb0: 0
0xfffffff7ffffbb8: 0
0xfffffff7ffffbc0: 0
0xfffffff7fffbc8: 0
0xfffffff7fffbd0: 0
0xfffffff7fffbd8: 0
0xfffffff7ffffbe0: 1
0xffffffff7ffffbe8: 0xffffffff7ffffd18
0xfffffff7ffffc00: 0x100000000
0xfffffff7ffffc08: 0x1c00
0xfffffff7ffffc10: 0xffffffff7ffff461
0xfffffffffffc18: start+0x7c
0xfffffff7ffffc20: 4
0xffffffff7ffffc28: 0xfffffffffffd28
0xfffffff7ffffc30: 5
0xffffffff7ffffc38: 0xfffffffffffda8
0xfffffff7ffffc40: 0
0xfffffff7ffffc48: 0
0xfffffff7ffffc50: 0
0xfffffff7ffffc58: 0
0xfffffff7ffffc60: 1
```

```
0xfffffff7ffffae0: 0
0xfffffff7ffffae8: 0
0xfffffff7ffffaf0: 0
0xfffffffffffffffffaf8: 0
0xfffffff7fffb00: 0
0xfffffff7ffffb08: 0
0xfffffff7fffb10:0
0xfffffff7fffb18: 0
0xfffffff7ffffb20: 1
0xfffffff7ffffb28: 0
0xfffffff7fffb30:0
0xfffffff7fffb38:0
0xfffffff7fffb40: 0
0xfffffff7ffffb48: 0
0xfffffff7ffffb50: 0xffffffff7ffff3a1
0xffffffff58: main+4
0xfffffff7fffb60: 0
0xfffffff7ffffb68: 0
0xfffffff7fffb70:0
0xfffffff7fffb78: 0
0xfffffff7ffffb80: 0
0xfffffff7ffffb88: 0
0xfffffff7ffffb90: 0
0xfffffff7ffffb98: 0xffffffff7f736c90
```









a: save %sp, -0xc0, %sp a+4: call +0x10 a+8: mov %i0, %o0

a+0xc: ret

a+0x10: restore %g0, %o0, %o0

Call b()

- No control transfer yet
- %o7 ← a+4
- %npc ← b
- %pc ← a+8

Oxfffffff7fffba0: 0
Oxfffffffffffba8: 0
Oxffffffffffbb0: 0
Oxfffffffffffbb8: 0
Oxfffffffffffbc0: 0
Oxfffffffffffbc8: 0
Oxfffffffffffbd0: 0
Oxfffffffffffbd8: 0
Oxfffffffffffbd8: 0
Oxffffffffffffbe0: 1

Oxffffffffffbe8: Oxffffffffffd18 Oxfffffffffffbf0: Oxfffffffffffd28 Oxffffffffffffbf8: test.sparc`environ Oxffffffffffffc00: 0x100000000

0xffffffffffc08: 0x1c00

Oxfffffff7ffffc10: Oxffffffff7ffff461 Oxffffffffffffc18: start+0x7c

0xfffffff7ffffc20: 4

0xfffffff7ffffc28: 0xfffffffffffd28

0xfffffff7fffc30: 5

0xfffffff7ffffc38: 0xfffffffffffda8

0xfffffff7fffc40: 0 0xfffffffffffc48: 0 0xfffffffffffc50: 0 0xffffffffffffc58: 0

0xfffffff7ffffc60: 1

0xfffffff7ffffae0: 0

0xfffffff7ffffae8: 0

0xfffffff7ffffaf0: 0

0xfffffff7fffb00:0

0xfffffff7fffb08: 0

0xfffffff7fffb10:0

0xfffffff7fffb18:0

0xfffffff7fffb20: 1

0xfffffff7ffffb28: 0

0xfffffff7fffb30: 0 0xffffffffb38: 0

0xfffffff7fffb40: 0

0xfffffff7fffb48:0

0xfffffff7ffffb50: 0xffffffff7ffff3a1

0xfffffff58: main+4

0xfffffff7fffb60: 0

0xfffffff7fffb68: 0

0xfffffffb70: 0

0xfffffff7fffb78: 0

0xfffffff7fffb80: 0

0xfffffffb88: 0

0xfffffff7ffffb90: 0

0xfffffff7fffb98: 0xffffffff7f736c90









a: save %sp, -0xc0, %sp a+4: call +0x10 a+8: mov %i0, %o0

a+0xc: ret

a+0x10: restore %g0, %o0, %o0

Delay slot instruction

 Copy incoming argument to outgoing argument 0xfffffff7ffffba0: 0 0xfffffff7ffffba8: 0 0xfffffff7ffffbb0: 0 0xfffffff7ffffbb8: 0 0xfffffff7ffffbc0: 0 0xfffffff7fffbc8: 0 0xfffffff7fffbd0: 0 0xfffffff7fffbd8: 0 0xfffffff7ffffbe0: 1 0xffffffff7ffffbe8: 0xffffffff7ffffd18 0xfffffff7ffffc00: 0x100000000 0xfffffff7ffffc08: 0x1c00 0xfffffff7ffffc10: 0xffffffff7ffff461 0xfffffff7ffffc18: start+0x7c 0xfffffff7ffffc20: 4 0xffffffff7ffffc28: 0xfffffffffffd28 0xfffffff7ffffc30: 5

0xffffffff7ffffc38: 0xfffffffffffda8

0xfffffff7ffffc40: 0

0xfffffff7ffffc48: 0

0xfffffff7ffffc50: 0

0xfffffff7ffffc58: 0

0xfffffff7ffffc60: 1

0xfffffff7ffffae0: 0 0xfffffff7ffffae8: 0 0xfffffff7ffffaf0: 0 0xfffffffffffffffffaf8: 0 0xfffffff7fffb00: 0 0xfffffff7fffb08: 0 0xfffffff7fffb10:0 0xfffffff7fffb18: 0 0xfffffff7ffffb20: 1 0xfffffff7ffffb28: 0 0xfffffff7fffb30:0 0xfffffff7fffb38:0 0xfffffff7ffffb40: 0 0xfffffff7ffffb48: 0 0xfffffff7ffffb50: 0xffffffff7ffff3a1 0xffffffff58: main+4 0xfffffff7fffb60: 0 0xfffffff7fffb68: 0 0xfffffff7fffb70:0 0xfffffff7fffb78: 0 0xfffffff7ffffb80: 0 0xfffffff7ffffb88: 0 0xfffffff7ffffb90: 0 0xfffffff7ffffb98: 0xffffffff7f736c90









a: save %sp, -0xc0, %sp a+4: call +0x10 a+8: mov %i0, %o0

a+0xc: ret

a+0x10: restore %g0, %o0, %o0

 Step through and return from b() 0xfffffff7ffffba0: 0 0xfffffff7ffffba8: 0 0xfffffff7ffffbb0: 0 0xfffffff7ffffbb8: 0 0xfffffff7ffffbc0: 0 0xfffffff7ffffbc8: 0 0xfffffff7fffbd0: 0 0xffffffffdd8: 0 0xfffffff7ffffbe0: 1 0xffffffff7ffffbe8: 0xffffffff7ffffd18 0xfffffff7ffffc00: 0x100000000 0xfffffff7ffffc08: 0x1c00 0xfffffff7ffffc10: 0xffffffff7ffff461 0xffffffff7ffffc18: start+0x7c 0xfffffff7ffffc20: 4 0xffffffff7ffffc28: 0xfffffffffffd28

0xfffffff7ffffc38: 0xffffffff7ffffda8

0xfffffff7ffffae0: 0 0xfffffff7ffffae8: 0 0xfffffff7ffffaf0: 0 0xfffffffffffffffffaf8: 0 0xfffffff7fffb00: 0 0xfffffff7ffffb08: 0 0xfffffff7fffb10:0 0xfffffff7fffb18: 0 0xfffffff7ffffb20: 1 0xfffffff7ffffb28: 0 0xfffffff7fffb30:0 0xfffffff7fffb38:0 0xfffffff7ffffb40: 0 0xfffffff7ffffb48: 0 0xfffffff7ffffb50: 0xffffffff7ffff3a1 0xffffffff58: main+4 0xfffffff7ffffb60: 0 0xfffffff7ffffb68: 0 0xfffffff7fffb70:0 0xfffffff7fffb78: 0 0xfffffff7ffffb80: 0 0xfffffff7ffffb88: 0 0xfffffff7ffffb90: 0 0xfffffff7ffffb98: 0xffffffff7f736c90









0xfffffff7ffffc30: 5

0xfffffff7ffffc40: 0

0xfffffff7ffffc48: 0

0xfffffff7ffffc50: 0

0xfffffff7ffffc58: 0

a: save %sp, -0xc0, %sp a+4: call +0x10 a+8: mov %i0, %o0

a+0xc: ret

a+0x10: restore %g0, %o0, %o0

Return from a()

- No control transfer yet
- %npc ← %i7+8
- %pc ← a+0x10

0xfffffff7ffffba0: 0 0xfffffff7ffffba8: 0 0xfffffff7ffffbb0: 0 0xfffffff7ffffbb8: 0 0xfffffff7ffffbc0: 0 0xfffffff7ffffbc8: 0 0xfffffff7fffbd0: 0 0xfffffff7fffbd8: 0 0xfffffff7ffffbe0: 1 0xffffffff7ffffbe8: 0xffffffff7ffffd18 0xfffffff7ffffc00: 0x100000000 0xfffffff7ffffc08: 0x1c00 0xfffffff7ffffc10: 0xffffffff7ffff461 0xffffffff7ffffc18: start+0x7c 0xfffffff7ffffc20: 4

0xffffffff7ffffc28: 0xfffffffffffd28

0xfffffff7ffffc38: 0xfffffffffffda8

0xfffffff7ffffc30: 5

0xfffffff7ffffc40: 0

0xfffffff7ffffc48: 0

0xfffffff7ffffc50: 0

0xfffffff7ffffc58: 0

0xfffffff7ffffc60: 1

0xfffffff7ffffae0: 0 0xfffffff7ffffae8: 0 0xfffffff7ffffaf0: 0 0xfffffffffffffffffaf8: 0 0xfffffff7fffb00: 0 0xfffffff7ffffb08: 0 0xfffffff7fffb10:0 0xfffffff7fffb18: 0 0xfffffff7ffffb20: 1 0xfffffff7ffffb28: 0 0xfffffff7fffb30:0 0xfffffff7fffb38:0 0xfffffff7ffffb40: 0 0xfffffff7ffffb48: 0 0xfffffff7ffffb50: 0xffffffff7ffff3a1 0xffffffff58: main+4 0xfffffff7ffffb60: 0 0xfffffff7ffffb68: 0 0xfffffff7fffb70:0 0xfffffff7fffb78: 0 0xfffffff7ffffb80: 0 0xfffffff7ffffb88: 0 0xfffffff7ffffb90: 0 0xfffffff7ffffb98: 0xffffffff7f736c90









```
a: save %sp, -0xc0, %sp
a+4: call +0x10 <b>
a+8: mov %i0, %o0
```

a+0xc: ret

a+0x10: restore %g0, %o0, %o0

Restore the previous register window

- Free 192 bytes of stack space
- Old Ins become current Outs

```
Oxfffffffffba0: 0
Oxffffffffffba8: 0
Oxfffffffffffbb0: 0
Oxffffffffffbb8: 0
Oxfffffffffffbc0: 0
Oxfffffffffffbc8: 0
Oxfffffffffffbd0: 0
Oxffffffffffffbd8: 0
Oxfffffffffffbe0: 1
Oxfffffffffffbe8: 0xffffffffffffd18
Oxfffffffffffffbf0: 0xffffffffffffd28
Oxffffffffffffffbf8: test.sparc`environ
```

Oxfffffff7fffc00: 0x100000000 Oxfffffff7fffc08: 0x1c00

0xfffffff7fffc20: 4

0xfffffff7ffffc28: 0xfffffffffffd28

0xfffffff7fffc30: 5

0xfffffff7ffffc38: 0xffffffff7ffffda8

0xfffffff7fffc40: 0 0xffffffffffc48: 0 0xffffffffffc50: 0 0xfffffffffffc58: 0









Return from main()

- No control transfer yet
- %npc ← %i7+8
- %pc ← main+0x10

main: save %sp, -0xc0, %sp main+4: call -0x34 <a> main+8: mov %i0, %o0

main+0xc: ret

main+0x10: restore %g0, %o0, %o0

```
Oxfffffff7ffffba0: 0
Oxffffffff7ffffbb0: 0
Oxffffffff7ffffbb8: 0
Oxfffffffffffffbc0: 0
Oxffffffffffffbc8: 0
Oxfffffffffffbc8: 0
Oxfffffffffffbd0: 0
Oxfffffffffffbd8: 0
Oxfffffffffffbd8: 0
```

Oxfffffff7ffffbe8: Oxffffffff7ffffd18 Oxffffffff7ffffbf0: Oxffffffffffffd28 Oxffffffff7ffffbf8: test.sparc`environ Oxfffffff7ffffc00: 0x100000000

0xfffffff7ffffc08: 0x1c00

0xfffffff7ffffc10: 0xffffffff7ffff461 0xffffffffffffc18: _start+0x7c

0xfffffff7ffffc20: 4

0xfffffff7ffffc28: 0xffffffff7ffffd28

0xfffffff7ffffc30: 5

Oxfffffff7fffc40: 0 Oxfffffff7ffffc48: 0 Oxfffffff7ffffc50: 0 Oxfffffff7ffffc58: 0 Oxffffffff7ffffc60: 1









- Restore the previous register window
 - Free 192 bytes of stack space
 - Old Ins become current Outs

main: save %sp, -0xc0, %sp main+4: call -0x34 <a> main+8: mov %i0, %o0

main+0xc: ret

main+0x10: restore %g0, %o0, %o0

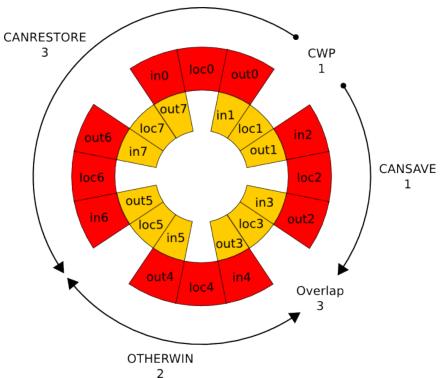








SPARC V9 ABI cheat sheet



i0	1 st argument / ret. val
i1	2 nd argument
i2	3 rd argument
i3	4 th argument
i4	5 th argument
i5	6 th argument
i6/fp	frame pointer
i7	return addr – 8
10	
l1	
12	
13	
I 4	
I 5	
I 6	
17	

non-volatile registers	
volatile registers	
Volumo regioners	

o0	1st argument for callee
o1	2 nd argument for callee
2	3 rd argument for callee
o3	4 th argument for callee
5 4	5 th argument for callee
o5	6 th argument for callee
o6/sp	stack pointer
o7	where callee will return – 8

g0	always 0
g1	
g2	
g2 g3 <mark>g4</mark>	
g4	
g5	
<mark>g5</mark> g6	
g7	cur. thread In Solaris kernel









SPARC V9 ABI cheat sheet (2)

