Mona Reimplemented: WS1S Logic with Mata

Michal Šedý

Email: xsedym02@stud.fit.vutbr.cz;

Abstract

This paper focuses on the reimplementation of the decision procedure for WS1S logic, a second-order logic that can be decided using finite automata. The well known tool for WS1S logic decision, Mona, employs automata with transitions represented through binary decision diagrams (BDDs). Due to the integration of BDDs in automata operations, tasks like reversal cannot be executed in the conventional manner of reverting individual edges. Instead, the reversal of each BDD must be computed, potentially resulting in an exponential blowup. Motivated by these limitations, Pavel Bednar reimplemented Mona using a pure automata approach with the Mata library. This work optimizes the automata methodology, resulting in a significant speedup, up to ten times faster, in WS1S decision compared to Bednar's original reimplementation.

Keywords: Finite Automata, Binary Decision Diagrams, WS1S, MONA, MATA

1 Introduction

The most well-known decision procedures are SAT and SMT [1], which are widely used in various applications such as verification (e.g., predicate abstraction), test generation, hardware synthesis, minimization, artificial intelligence, etc. The SAT (satisfiability) problem is a decision problem that asks whether a given propositional formula is satisfiable. The SMT (satisfiability modulo theories) problem extends the SAT problem to the satisfiability of first-order formulas with equality and atoms from various first-order theories. There are various higher-order decision procedures such as WS1S, WS2S, WSkS, S1S, etc.

This work focuses on WS1S, the weak monadic second-order theory of the first successor. The term "weak" refers to finite sets, "monadic" indicates unary relations, "second-order" allows the usage of quantifiers over the relations, and "first successor" means that there is only one successor (e.g., the structure is linear). WS1S [2] has an extremely simple syntax and semantics: it is variation of predicate logic with first-order variable that denote natural numbers and second-order variables that denote finite sets of natural numbers, it has a single function symbol, which denotes the successor function and has usual comparison operators such as \leq , =, \in and \supseteq . Richard Büchi presented approach how to decide WS1S using finite automata in [3] The main idea is to recursively transform each subformula of the main WS1S formula into deterministic finite automata (DFA) representing feasible interpretations and simulate boolean operations via the automata operations.

The most commonly used tool for deciding WS1S and WS2S is Mona¹, which employs Büchi's recursion approach for the construction of finite automata with binary

 $^{^{1}} accessible \ at \ https://www.brics.dk/mona/index.html$

decision diagrams (BDD) to represent all automaton transitions. The use of BDD makes the decision faster, but at the cost of making some automata operations, such as reversion, expensive (potentially resulting in exponential blowup). Despite this limitation, Mona is widely utilized in various fields of program verification, including the verification of programs with complex dynamic data structures [4, 5], string analysis [6], parametrized systems [7], distributed systems [8], automatic synthesis [9], hardware verification [10], and many others.

The previously mentioned problem with hard-to-compute automata operations when using BDDs motivated Bc. Pavel Bednář's master's thesis [11]. He reimplemented Mona's decision of WS1S by using a pure automata-based approach with the Mata automata library². The special type of edge, the *jumping edge* has been introduced. The jumping edge contains information about how many variables can be jumped over. The primary idea behind introducing the jumping edge was to enable jumps not only over inner states but also over automaton states, with no upper limit on the maximal jump. However, despite this innovation, the jumping edge did not yield significant improvements in terms of space or time compression. Furthermore, it appears that jumping edges led to an overcomplication of algorithms.

In our approach, we reimplemented Bednář's solution by enhancing each automaton state with an index corresponding to the variable ID, mirroring the indexing strategy used for each inner node in the ordered BDD employed by Mona. This index information allows us to determine the length of a jump based on the indices of the source and destination states in the automaton's transition. Due to the indexing sequence on states follows a pattern of $0,1,\ldots,n-1,0,1,\ldots,n-1,0,\ldots$, the longest jump can only reach to the next state with an index of 0. While this might appear to be a step backward from Bednář's approach, the limitation on the jump length simplifies all algorithms. Surprisingly, it results in a significantly faster decision of the input formula, up to 10 times faster, compared to the variant with jumping edges.

The first section introduces basic notations, definition of finite automata, binary decision diagrams, and WS1S. In the second section, we delve into the background of automata construction from WS1S formulas. The third section provides a detailed description of algorithms for intersection, union, complement, determinization, and minimization of automata with indexes. Moving to the fourth section, we present a comparison of decision times between the Mona tool, automata with jumping edges, and automata with indexes. The experiments are divided into two parts. Initially, automata operations are tested separately on the automata generated during Mona computation. Following that, the comparison is executed on the entire input formula.

2 Preliminaries

In this section, we briefly introduce the definitions of nondeterministic and deterministic automata, binary decision diagrams, and the WS1S logic.

2.1 Automata

Definition 1. A deterministic finite automaton is a 5-tuple $M=(Q,\Sigma,\delta,q_0,F),$ where its components are:

- ullet Q is a finite nonempty set of states,
- Σ is an alphabet,
- $\delta: Q \times \Sigma \to Q$ is a transition function,
- $q \in Q$ is an initial state, and
- $F \subseteq Q$ is a set of final states.

Definition 2. A nondeterministic finite automaton is a 5-tuple $M = (Q, \Sigma, \delta, Q_0, F)$, where Q, Σ , and F are defined identically as for the DFA. The transition function δ is defined as $\delta : Q \times \Sigma \to 2^Q$ and $Q_0 \subseteq Q$ is a nonempty set of initial states.

 $^{^2 {\}it available~at:~https://github.com/VeriFIT/mata}$

Nondeterminism allows the automaton to make transitions to more than one successor based on the current state and the read input symbol. In contrast, its deterministic variant can transition to at most one state. Nondeterminism keeps the automaton more compact, but certain operations such as complementation cannot be performed directly on NFA. Therefore determinization is required beforehand.

2.2 Binary Decision Diagrams

Representation of boolean function ϕ with n logical variable led to 2^n transitions for each automaton state in order to cover each possible combination of logical values. This exponential number of transitions can be reduced using BDDs. Binary decision diagrams provides a compact and most importantly canonical representation for logical functions in the form $\phi: 0, 1^* \to 0, 1$.

Definition 3. The binary decision diagram [12] is rooted, directed, connected, and acyclic graph defined as a 7-tuple G = (N, T, var, low, high, root, val) where:

- N is finite set on non-terminal (inner) nodes,
- T is a finite set of terminal nodes (leaves) such that $N \cap T = \emptyset$,
- $var: N \rightarrow N \cup T$ defines the low and high successors of the inner nodes,
- $root \in N \cup T$ is the root node, and
- $val: T \rightarrow 0, 1$ assignes logical value to the leaves.

The size of the BDD is not determined only by the number of logical variable being used withing the function ϕ but also by ordering of the variable in the BDD. The best variable ordering can result in BDD with linear (in the number of variables) number of nodes or exponential for worst ordering.

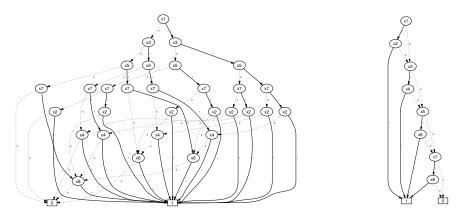


Fig. 1: Two bdds for the function $f(x_1, \ldots, x_{2n}) = x_1 x_2 + x_3 x_4 + \cdots, x_{2n-1} x_{2n}$ with bad variable ordering (on the left) and good variable ordering (on the right).

Consider the bolean function $f(x1, \ldots, x_{2n}) = x_1x_2 + x_3x_4 + \cdots, x_{2n-1}x_{2n}$. Using the variable ordering $x_1 < x_3 < \cdots x_{2n-1} < x_2 < x_4 < \cdots < x_{2n}$, the BDD needs 2^{n+1} nodes to represent the function. Using the oredering $x_1 < x_2 < x_3 < x_4 < \cdots < x_{2n-1} < x_{2n}$, the BDD consists of 2n+2 nodes. The example of such orderings is shown in the figure 1. The problem of finding the best variable ordering is NP-hard [13]. However, there exist various heuristics to handle this challenge [14].

Definition 4. Let \prec be a given ordering on logical variables Var, a BDD G is ordered (OBDD) with respect to \prec iff for every $n \in N$ the following conditions hold:

- 1. $low(n) \in N \implies var(n) \prec var(low(n))$
- 2. $high(n) \in N \implies var(n) \prec var(high(n))$

Definition 5. The OBDD G = (N, T, var, low, high, root, val) is reduced OBDD (ROBDD) if the following conditions are met:

- 1. $\forall t_1, t_2 \in T : val(t_1) \neq val(t_2)$
- 2. There are no isomorphic subgraphs in G
- 3. $\forall n_1, n_2 \in N : low(n_1) \neq low(n_2) \vee high(n_1) \neq high(n_2)$

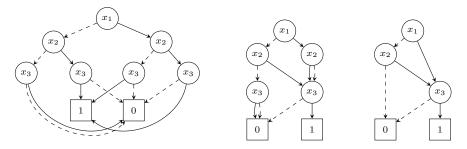


Fig. 2: From the left to the right, OBDD meeting the first condition, the second condition and the third condition from the definition.

Theorem 1. For every Boolean function ϕ over some se of variables Var and every variable ordering \prec on Var, there is a unique (up to isomorphism) reduced OBDD (wrt \prec) G_{ϕ} which represents ϕ . [12]

Based on the theorem 1, the checking equivalence of two functions ϕ_1 and ϕ_2 represented by reduced OBDDs (RODBBs) G_1 and G_2 ie equivalent to checking isomorphism of G_1 and G_2 .

Moreover, it several boolean function are represented with one *shared ROBDD* with multiple roots, as Mona does, the equivalence checking is reduced from isomorphism checking to simply checking the identity of the BDD roots.

2.3 WS1S

Richard Büchi shown that WS1S is equivalent with regular expression and therefore can be represented by finite automata[3]. In this subsection first the simplification of the WS1S formula and its semantic will be presented, followed by transformation of atomic formulae to the automata. The main source for this subsection was [15].

Formula simplification

First-order terms are ancoded as second-order terms, since a first-order value can be seen as a singleton second-order value. Also booleans can be encoded using the first position in the input automaton string.

All second-order terms are "flattened" by introductin new variables that contain the values of all subterms. For example the formula $A=(B\cup C)\cap D$ will be transformed into the form $\exists V:A=V\cap D\wedge V=B\cup C$, where V is a new variable.

Subformulae are simplified to contain fewer operators. As a result only basic operations have to be implemented by the solver. The abstract syntax for simplified WS1S fomulae can be defined by the following grammar:

$$\phi := \neg \phi' | \phi' \wedge \phi'' | \exists P_i : \phi' | P_i \subseteq P_j | P_i = P_j \setminus P_k | P_i = P_j + 1$$

Semantic

Given the main fomula ϕ_0 , we define its sematics inductively relative to a string w over the alphabet \mathbb{B}^k , where $\mathbb{B} = \{0,1\}$ and k is the number of veriables in ϕ_0 . Assubme every variable of ϕ_0 is assigned a unique number in the range $1,2,\ldots,k$ called the *variable index*. The string w now determinis an interpretation $w(P_i)$ of P_i

defined as the finite set $\{j \mid \text{the } j \text{th bit is the } P_i\text{-track is } 1\}$. For example, the formula $\phi_0 \equiv \exists C : A = B \setminus C$ has variables A, B, and C, which are assigned by the indexes 1, 2, and 3, respectively. A tipical string w over \mathbb{B}^3 looks like:

$$\begin{array}{ccc} A & & \begin{pmatrix} 1 \\ 0 \\ 1 \end{pmatrix} \begin{pmatrix} 0 \\ 0 \\ 1 \end{pmatrix} \begin{pmatrix} 1 \\ 1 \\ 0 \end{pmatrix} \begin{pmatrix} 0 \\ 0 \\ 0 \end{pmatrix}$$

Its important to note, that w with the suffix $(0^*)^T$ defines the smae interpretation as w. Therefore, the minimum w is such a string that there is no such nun-empty suffix. The semantics of a formula ϕ is defined inductively:

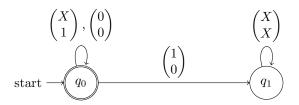
$$\begin{aligned} w &\vDash \neg \phi' & \text{iff} \quad w \nvDash \phi' \\ w &\vDash \phi' \land \phi'' & \text{iff} \quad w \vDash \phi' \land w \vDash \phi'' \\ w &\vDash \exists P_i : \phi' & \text{iff} \quad \exists finite(M) \subseteq \mathbb{N} : w[P_i \mapsto M] \vDash \phi' \\ w &\vDash P_i \subseteq P_j & \text{iff} \quad w(P_i) \subseteq w(P_j) \\ w &\vDash P_i = P_j \setminus P_k & \text{iff} \quad w(P_i) = w(P_j) \setminus w(P_k) \\ w &\vDash P_i = P_j + 1 & \text{iff} \quad w(P_i) = \{j + 1 \mid j \in w(P_j)\} \end{aligned}$$

where we use the notation $w[P_i \mapsto M]$ for the shortest string w' that interprets all variables P_j , $j \neq i$ as w does, but interprets P_i as M. Note that if we assume that w is minimum than w' decomposes into $w' = w \cdot w''$, there w'' is a string of letter of the form $(0^*X0^*)^T$ where the ith component is the only one that may be different from 0.

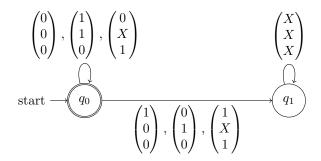
Automaton construction

The input formula ϕ is recursively transformed into the deterministic finite automaton that represents the set of satisfying strings $L(\phi) = \{w \mid w \models \phi\}$. The translation of atomic and composite formulae to the deterministic finite automata follows:

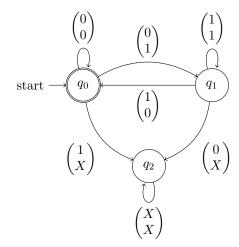
•
$$\phi = P_1 \subseteq P_2$$
:



• $\phi = P_1 = P_2 \setminus P_3$:



• $\phi = P_1 = P_2 + 1$:



- $\phi = \neg \phi'$: Negation of a formula corresponds to automaton complementation, so if we have already calculated A' such that $L(\phi') = L(A')$, then $L(\neg \phi') =$ $CL(\phi) = CL(A') = L(CA')$, where C denotes both the language complementatino and automata complementation. If the automaton is complete and deterministic, then the complementation can be done by swapping accepting and nonaccepting states.
- $\phi = \phi' \wedge \phi''$: Conjunction corresponds to language intersection, $L(\phi' \wedge \phi'') = L(\phi') \cap$ $L(\phi'')$. So the resulting automaton A is obtained by production fo automata $A' \times A''$, where $L(\phi') = L(A')$ and $L(\phi'') = L(A'')$.
- $\phi = \exists P_i : \phi'$: Intuitively, the desired automaton A acts as the automaton A' for ϕ' except that it is allowed to guess the bits on P_i -track. The resulting automaton Ais nondeterministic. Its necessary to apply determinization and adjust automaton A in such a way that each $w \in L(A)$ is minimal.

3 Automata representation

3.1 Mona

3.2 Automata with Jumping Edges

3.3 Automata with Indexed States

Definition 6. Let $M = (Q, \Sigma, \delta, Q_0, F)$ be an NFA. The automaton M is called an automaton with indexing if there exists an index function $\iota: Q \to \mathbb{N}_0$ such that the following conditions hold:

- 1. $\forall q \in Q_0 : \iota(q) = 0$ 2. $\forall q \in F : \iota(q) = 0$ 3. $\forall q, r \in Q : \nexists a \in \Sigma : r \in \delta(q, a) \land \iota(r) \neq 0 \land \iota(q) \geq \iota(r)$

The first and second conditions reflect the fact that only roots or leaves in the BDD can be initial and final states, respectively. The third condition demands that the part of the automaton simulating the BDD must be acyclic, with the exception of the root nodes/states with index 0.

References

[1] Vojnar Tomáš: Lecture notes in Static Analysis and Verification: SAT and SMT Solving. BUT - Faculty of Information Technology (2023). https://www.fit.vutbr. cz/study/courses/SAV/public/Lectures/sav-lecture-10.pdf

- [2] Klarlund, N.: A theory of restrictions for logics and automata. In: Computer Aided Verification, CAV '99. LNCS, vol. 1633
- [3] Büchi, J.R.: Weak second-order arithmetic and finite automata. Mathematical Logic Quarterly 6(1-6), 66–92 (1960) https://doi.org/10.1002/malq.19600060105
- [4] Møller, A., Schwartzbach, M.I.: The pointer assertion logic engine. In: Proceedings of the ACM SIGPLAN 2001 Conference on Programming Language Design and Implementation. PLDI '01, pp. 221–231. Association for Computing Machinery, New York, NY, USA (2001). https://doi.org/10.1145/378795.378851
- [5] Madhusudan, P., Parlato, G., Qiu, X.: Decidable logics combining heap structures and data. SIGPLAN Not. 46(1), 611–622 (2011) https://doi.org/10.1145/1925844.1926455
- [6] Tateishi, T., Pistoia, M., Tripp, O.: Path- and index-sensitive string analysis based on monadic second-order logic. ACM Trans. Softw. Eng. Methodol. 22(4) (2013) https://doi.org/10.1145/2522920.2522926
- [7] Baukus, K., Bensalem, S., Lakhnech, Y., Stahl, K., Equation, V.: Abstracting ws1s systems to verify parameterized networks. (2001). https://doi.org/10.1007/3-540-46419-0_14
- [8] Klarlund, N., Nielsen, M., Sunesen, K.: A case study in verification based on trace abstractions. In: Broy, M., Merz, S., Spies, K. (eds.) Formal Systems Specification, pp. 341–373. Springer, Berlin, Heidelberg (1996)
- [9] Sandholm, A., Schwartzbach, M.I.: Distributed safety controllers for web services. In: Astesiano, E. (ed.) Fundamental Approaches to Software Engineering, pp. 270–284. Springer, Berlin, Heidelberg (1998)
- [10] Basin, D., Klarlund, N.: Automata based symbolic reasoning in hardware verification. Form. Methods Syst. Des. **13**(3), 255–288 (1998) https://doi.org/10.1023/A: 1008644009416
- [11] Pavel, B.: Rozhodování ws1s pomocí symbolických automatů [online]. Diplomová práce, Vysoké učení technické v BrněBrno (2023 [cit. 2024-01-20]). SUPERVISOR:. https://theses.cz/id/kq7t5j/
- [12] Vojnar Tomáš: Lecture notes in Static Analysis and Verification: Binary Decision Diagrams. BUT Faculty of Information Technology (2023). https://www.fit.vutbr.cz/study/courses/SAV/public/Lectures/sav-lecture-07.pdf
- [13] Bollig, B., Wegener, I.: Improving the variable ordering of obdds is np-complete. IEEE Transactions on Computers **45**(9), 993–1002 (1996) https://doi.org/10. 1109/12.537122
- [14] Rice, M., Kulhari, S.: A Survey of Static Variable Ordering Heuristics for Efficient BDD/MDD Construction, Technical Report. http://www.cs.ucr.edu/~skulhari/StaticHeuristics.pdf (2008)
- [15] Klarlund, N., Møller, A.: MONA Version 1.4 User Manual. BRICS, Department of Computer Science, Aarhus University, (2001). BRICS, Department of Computer Science, Aarhus University. Notes Series NS-01-1. Available from http://www. brics.dk/mona/. Revision of BRICS NS-98-3