

Mona Reimplemented: WS1S Logic with Mata

Michal Šedý

Email: xsedym02@stud.fit.vutbr.cz;

Abstract

This paper focuses on the reimplementation of the decision procedure for WS1S logic, a second-order logic that can be decided using finite automata. The well known tool for WS1S logic decision, Mona, employs automata with transitions represented through binary decision diagrams (BDDs). Due to the integration of BDDs in automata operations, tasks like reversal cannot be executed in the conventional manner of reverting individual edges. Instead, the reversal of each BDD must be computed, potentially resulting in an exponential blowup. Motivated by these limitations, Pavel Bednar reimplemented Mona using a pure automata approach with the Mata library. This work optimizes the automata methodology, resulting in a significant speedup, up to ten times faster, in WS1S decision compared to Bednar’s original reimplementation.

Keywords: Finite Automata, Binary Decision Diagrams, WS1S, MONA, MATA

1 Introduction

The most well-known decision procedures are SAT and SMT [1], which are widely used in various applications such as verification (e.g., predicate abstraction), test generation, hardware synthesis, minimization, artificial intelligence, etc. The SAT (satisfiability) problem is a decision problem that asks whether a given propositional formula is satisfiable. The SMT (satisfiability modulo theories) problem extends the SAT problem to the satisfiability of first-order formulas with equality and atoms from various first-order theories. There are various higher-order decision procedures such as WS1S, WS2S, WSkS, S1S, etc.

This work focuses on WS1S, the weak monadic second-order theory of the first successor. The term “weak” refers to finite sets, “monadic” indicates unary relations, “second-order” allows the usage of quantifiers over the relations, and “first successor” means that there is only one successor (e.g., the structure is linear). WS1S [2] has an extremely simple syntax and semantics: it is variation of predicate logic with first-order variable that denote natural numbers and second-order variables that denote finite sets of natural numbers, it has a single function symbol, which denotes the successor function and has usual comparison operators such as \leq , $=$, \in and \supseteq . Richard Büchi presented approach how to decide WS1S using finite automata in [3]. The main idea is to recursively transform each subformula of the main WS1S formula into deterministic finite automata (DFA) representing feasible interpretations and simulate boolean operations via the automata operations.

The most commonly used tool for deciding WS1S and WS2S is Mona¹, which employs Büchi’s recursion approach for the construction of finite automata with binary

¹accessible at <https://www.brics.dk/mona/index.html>

decision diagrams (BDD) to represent all automaton transitions. The use of BDD makes the decision faster, but at the cost of making some automata operations, such as reversion, expensive (potentially resulting in exponential blowup). Despite this limitation, Mona is widely utilized in various fields of program verification, including the verification of programs with complex dynamic data structures [4, 5], string analysis [6], parametrized systems [7], distributed systems [8], automatic synthesis [9], hardware verification [10], and many others.

The previously mentioned problem with hard-to-compute automata operations when using BDDs motivated Bc. Pavel Bednár's master's thesis [11]. He reimplemented Mona's decision of WS1S by using a pure automata-based approach with the Mata automata library². The special type of edge, the *jumping edge* has been introduced. The jumping edge contains information about how many variables can be jumped over. The primary idea behind introducing the jumping edge was to enable jumps not only over inner states but also over automaton states, with no upper limit on the maximal jump. However, despite this innovation, the jumping edge did not yield significant improvements in terms of space or time compression. Furthermore, it appears that jumping edges led to an overcomplication of algorithms.

In our approach, we reimplemented Bednár's solution by enhancing each automaton state with an index corresponding to the variable ID, mirroring the indexing strategy used for each inner node in the ordered BDD employed by Mona. This index information allows us to determine the length of a jump based on the indices of the source and destination states in the automaton's transition. Due to the indexing sequence on states follows a pattern of $0, 1, \dots, n-1, 0, 1, \dots, n-1, 0, \dots$, the longest jump can only reach to the next state with an index of 0. While this might appear to be a step backward from Bednár's approach, the limitation on the jump length simplifies all algorithms. Surprisingly, it results in a significantly faster decision of the input formula, up to 10 times faster, compared to the variant with jumping edges.

The first section introduces basic notations, definition of finite automata, binary decision diagrams, and WS1S. In the second section, we delve into the background of automata construction from WS1S formulas. The third section provides a detailed description of algorithms for intersection, union, complement, determinization, and minimization of automata with indexes. Moving to the fourth section, we present a comparison of decision times between the Mona tool, automata with jumping edges, and automata with indexes. The experiments are divided into two parts. Initially, automata operations are tested separately on the automata generated during Mona computation. Following that, the comparison is executed on the entire input formula.

2 Preliminaries

In this section, we briefly introduce the definitions of nondeterministic and deterministic automata, binary decision diagrams, and the WS1S logic.

2.1 Automata

Definition 1. A *deterministic finite automaton* is a 5-tuple $M = (Q, \Sigma, \delta, q_0, F)$, where its components are:

- Q is a finite nonempty set of states,
- Σ is an alphabet,
- $\delta : Q \times \Sigma \rightarrow Q$ is a transition function,
- $q \in Q$ is an initial state, and
- $F \subseteq Q$ is a set of final states.

Definition 2. A *nondeterministic finite automaton* is a 5-tuple $M = (Q, \Sigma, \delta, Q_0, F)$, where Q , Σ , and F are defined identically as for the DFA. The transition function δ is defined as $\delta : Q \times \Sigma \rightarrow 2^Q$ and $Q_0 \subseteq Q$ is a nonempty set of initial states.

²available at: <https://github.com/VeriFIT/mata>

Nondeterminism allows the automaton to make transitions to more than one successor based on the current state and the read input symbol. In contrast, its deterministic variant can transition to at most one state. Nondeterminism keeps the automaton more compact, but certain operations such as complementation cannot be performed directly on NFA. Therefore determinization is required beforehand.

2.2 Binary Decision Diagrams

Representation of the boolean function ϕ with n logical variables leads to 2^n transitions for each automaton state in order to cover every possible combination of logical values. This exponential number of transitions can be reduced using Binary Decision Diagrams (BDDs). Binary Decision Diagrams provide a compact and, most importantly, canonical representation for logical functions in the form $\phi : \{0, 1\}^* \rightarrow \{0, 1\}$.

Definition 3. The binary decision diagram [12] is rooted, directed, connected, and acyclic graph defined as a 7-tuple $G = (N, T, \text{var}, \text{low}, \text{high}, \text{root}, \text{val})$ where:

- N is finite set on non-terminal (inner) nodes,
- T is a finite set of terminal nodes (leaves) such that $N \cap T = \emptyset$,
- $\text{var} : N \rightarrow N \cup T$ defines the low and high successors of the inner nodes,
- $\text{root} \in N \cup T$ is the root node, and
- $\text{val} : T \rightarrow \{0, 1\}$ assigns logical value to the leaves.

The size of the BDD is not determined only by the number of logical variables used within the function ϕ but also by the ordering of the variables in the BDD. The best variable ordering can result in a BDD with a linear (in the number of variables) number of nodes, while the worst ordering can lead to an exponential size.

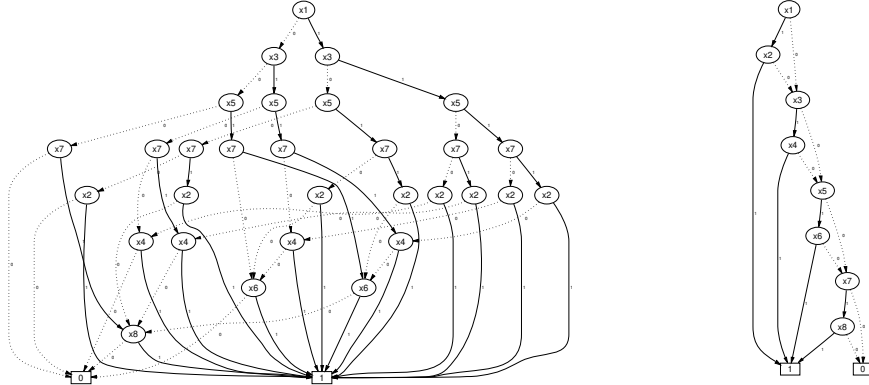


Fig. 1: Two bdds for the function $f(x_1, \dots, x_{2n}) = x_1x_2 + x_3x_4 + \dots + x_{2n-1}x_{2n}$ with bad variable ordering (on the left) and good variable ordering (on the right).

Consider the Boolean function $f(x_1, \dots, x_{2n}) = x_1x_2 + x_3x_4 + \dots + x_{2n-1}x_{2n}$. Using the variable ordering $x_1 < x_3 < \dots < x_{2n-1} < x_2 < x_4 < \dots < x_{2n}$, the Binary Decision Diagram (BDD) requires 2^{n+1} nodes to represent the function. Using the ordering $x_1 < x_2 < x_3 < x_4 < \dots < x_{2n-1} < x_{2n}$, the BDD consists of $2n + 2$ nodes. An example of such orderings is shown in Figure 1. The problem of finding the best variable ordering is NP-hard [13]. However, various heuristics exist to address this challenge [14].

Definition 4. Let \prec be a given ordering on logical variables Var , a Binary Decision Diagram (BDD) G is ordered (OBDD) with respect to \prec if, for every $n \in \mathbb{N}$, the following conditions hold:

1. $\text{low}(n) \in N \implies \text{var}(n) \prec \text{var}(\text{low}(n))$
2. $\text{high}(n) \in N \implies \text{var}(n) \prec \text{var}(\text{high}(n))$

Definition 5. The OBDD $G = (N, T, \text{var}, \text{low}, \text{high}, \text{root}, \text{val})$ is a Reduced OBDD (ROBDD) if the following conditions are satisfied:

1. $\forall t_1, t_2 \in T : \text{val}(t_1) \neq \text{val}(t_2)$
2. There are no isomorphic subgraphs in G
3. $\forall n_1, n_2 \in N : \text{low}(n_1) \neq \text{low}(n_2) \vee \text{high}(n_1) \neq \text{high}(n_2)$

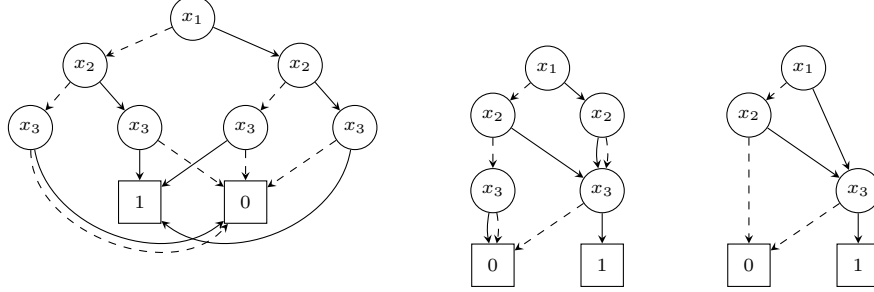


Fig. 2: From left to right, an OBDD satisfies the first, second, and third conditions as specified in the definition of ROBDD.

Theorem 1. For every Boolean function ϕ over some set of variables Var and every variable ordering \prec on Var , there is a unique (up to isomorphism) reduced OBDD (with respect to \prec) G_ϕ which represents ϕ . [12]

Based on Theorem 1, checking the equivalence of two functions, ϕ_1 and ϕ_2 , represented by Reduced OBDDs (ROBDDs) G_1 and G_2 is equivalent to checking the isomorphism of G_1 and G_2 .

Moreover, if several Boolean functions are represented with one shared ROBDD with multiple roots, as Mona does, the equivalence checking is reduced from isomorphism checking to simply checking the identity of the BDD roots.

2.3 WS1S

Richard Büchi showed that WS1S is equivalent to regular expressions and can therefore be represented by finite automata [3]. In this subsection, the simplification of the WS1S formula and its semantics will be presented, followed by the transformation of atomic formulae to automata. The main source for this subsection was [15].

Formula simplification

First-order terms are encoded as second-order terms since a first-order value can be seen as a singleton second-order value. Also, booleans can be encoded using the first position in the input automaton string.

All second-order terms are 'flattened' by introducing new variables that contain the values of all subterms. For example, the formula $A = (B \cup C) \cap D$ will be transformed into the form $\exists V : A = V \cap D \wedge V = B \cup C$, where V is a new variable.

Subformulae are simplified to contain fewer operators. As a result, only basic operations have to be implemented by the solver. The abstract syntax for simplified WS1S formulas can be defined by the following grammar:

$$\phi := \neg\phi' | \phi' \wedge \phi'' | \exists P_i : \phi' | P_i \subseteq P_j | P_i = P_j \setminus P_k | P_i = P_j + 1$$

Semantic

Given the main formula ϕ_0 , we define its semantic inductively relative to a string w over the alphabet \mathbb{B}^k , where $\mathbb{B} = 0, 1$ and k is the number of variables in ϕ_0 . Assume every variable of ϕ_0 is assigned a unique number in the range $1, 2, \dots, k$, called the *variable index*. The string w now determines an interpretation $w(P_i)$ of P_i , defined as the finite

set $j, |$, the j th bit in the P_i -track is 1. For example, the formula $\phi_0 \equiv \exists C : A = B \setminus C$ has variables A , B , and C , which are assigned the indices 1, 2, and 3, respectively. A typical string w over \mathbb{B}^3 looks like:

$$\begin{array}{c} A \\ B \\ C \end{array} \quad \begin{pmatrix} 1 \\ 0 \\ 1 \end{pmatrix} \begin{pmatrix} 0 \\ 0 \\ 1 \end{pmatrix} \begin{pmatrix} 1 \\ 1 \\ 0 \end{pmatrix} \begin{pmatrix} 0 \\ 0 \\ 0 \end{pmatrix}$$

It's important to note that w with the suffix $(0^*)^T$ defines the same interpretation as w . Therefore, the minimum w is such a string that there is no such non-empty suffix. The semantics of a formula ϕ is defined inductively:

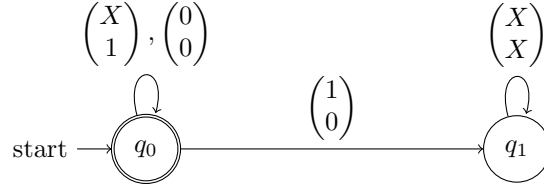
$$\begin{array}{ll} w \models \neg\phi' & \text{iff } w \not\models \phi' \\ w \models \phi' \wedge \phi'' & \text{iff } w \models \phi' \wedge w \models \phi'' \\ w \models \exists P_i : \phi' & \text{iff } \exists \text{finite}(M) \subseteq \mathbb{N} : w[P_i \mapsto M] \models \phi' \\ w \models P_i \subseteq P_j & \text{iff } w(P_i) \subseteq w(P_j) \\ w \models P_i = P_j \setminus P_k & \text{iff } w(P_i) = w(P_j) \setminus w(P_k) \\ w \models P_i = P_j + 1 & \text{iff } w(P_i) = \{j + 1 \mid j \in w(P_j)\} \end{array}$$

where we use the notation $w[P_i \mapsto M]$ for the shortest string w' that interprets all variables P_j , $j \neq i$, as w does but interprets P_i as M . Note that if we assume that w is minimum, then w' decomposes into $w' = w \cdot w''$, where w'' is a string of letters of the form $(0^*X0^*)^T$, and the i th component is the only one that may be different from 0.

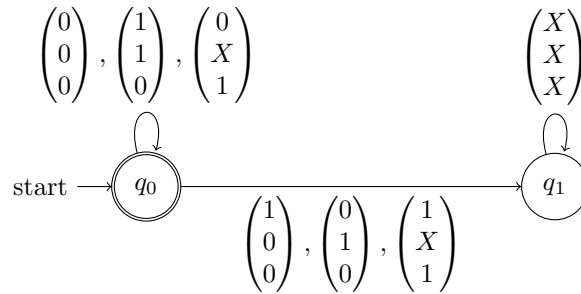
Automaton construction

The input formula ϕ is recursively transformed into the deterministic finite automaton that represents the set of satisfying strings $L(\phi) = w, |, w \models \phi$. The translation of atomic and composite formulae to deterministic finite automata follows:

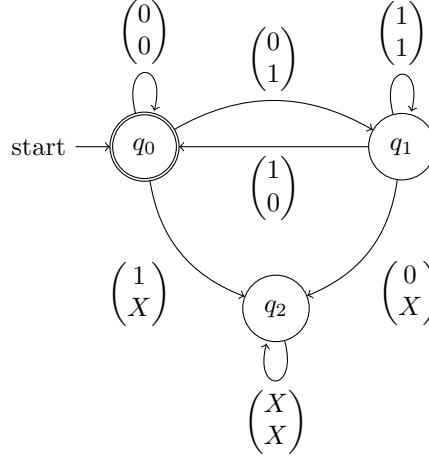
- $\phi = P_1 \subseteq P_2$:



- $\phi = P_1 = P_2 \setminus P_3$:



- $\phi = P_1 = P_2 + 1$:



- $\phi = \neg\phi'$: Negation of a formula corresponds to automaton complementation. If we have already calculated A' such that $L(\phi') = L(A')$, then $L(\neg\phi') = \mathbb{C}L(\phi') = \mathbb{C}L(A') = L(\mathbb{C}A')$, where \mathbb{C} denotes both language complementation and automata complementation. If the automaton is complete and deterministic, then complementation can be done by swapping accepting and non-accepting states.
- $\phi = \phi' \wedge \phi''$: Conjunction corresponds to language intersection, $L(\phi' \wedge \phi'') = L(\phi') \cap L(\phi'')$. So, the resulting automaton A is obtained by the production of automata $A' \times A''$, where $L(\phi') = L(A')$ and $L(\phi'') = L(A'')$.
- $\phi = \exists P_i : \phi'$: Intuitively, the desired automaton A acts as the automaton A' for ϕ' except that it is allowed to guess the bits on the P_i -track. The resulting automaton A is nondeterministic. It is necessary to apply determinization and adjust the automaton A in such a way that each $w \in L(A)$ is minimal.

3 Automata representation

This section presents three different approaches on how to incorporate BDDs into finite automata. First, Mona's approach using shared BDDs is shown, followed by approaches that utilize jumping edges or state indexing.

3.1 Mona

Mona represents the transition function not only using a single BDD (as in the case of Kripke structures) but with a *shared multi-terminal* BDD (SMTBDD). The main difference from standard BDDs is that the leaves of SMTBDD do not contain boolean values 0 or 1 but rather states of the automaton.

Definition 6. Let $M = (Q, \Sigma, \delta, q_0, F)$ be DFA. The SMTBDD is defined as a 7-tuple $G = (N, T, var, low, high, R, val)$ where:

- N is finite set on non-terminal (inner) nodes,
- T is a finite set of terminal nodes (leaves) such that $N \cap T = \emptyset$,
- $var : N \rightarrow N \cup T$ defines the low and high successors of the inner nodes,
- $R \subseteq N \cup T$ is nonempty set of rules, and
- $val : T \rightarrow Q$ assigns states to the leaves.

For example, let the formula $\phi \equiv \exists p, q : p \neq q \wedge p \in X \cap Y \wedge q \in X \cap Y$ be represented by the automaton M in Figure 3, where each state r , s , and t contains information about whether it is accepting or not and points to its root node in the SMTBDD describing its transition relation. The data structure also contains a pointer to the initial state.

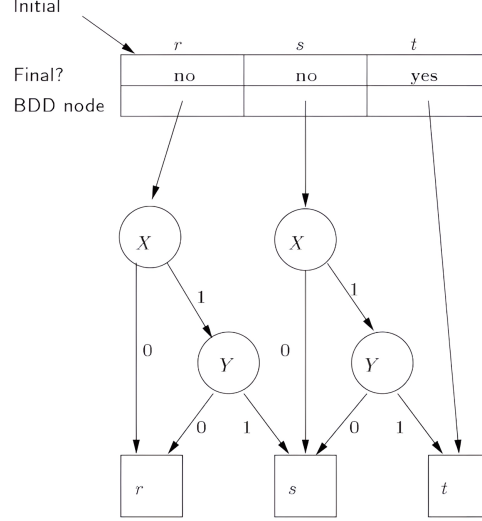


Fig. 3: Monna automaton for the formula $\phi \equiv \exists p, q : p \neq q \wedge p \in X \cap Y \wedge q \in X \cap Y$.

3.2 Automata with Skip Edges

The main idea of Bednár’s skip edges [11] is to integrate BDD nodes directly into the transitions of the automaton. This can be easily noted, as BDD has an automata-like structure. To simulate the functionality of a BDD, where each node has an assigned variable, it is necessary to use skip edges.

Definition 7. Let $M = (Q, \Sigma, \delta, Q_0, F)$ be an NFA, then automaton with skip edges $N = (Q, \Sigma, \delta', Q_0, F)$ has transition function defined as $\delta' : Q \times \Sigma \rightarrow 2^{\mathbb{N} \times Q}$.

The transition function in the automaton with skip edges provides information about target states and the number of variables that have been jumped over. The skip edge with a length of 1 is a special case that has the same functionality as edges in NFA. $(n, r) \in \delta'(q, a)$ denotes that the automaton can move from state q to r after reading a symbol a and then jump over $n - 1$ variables.

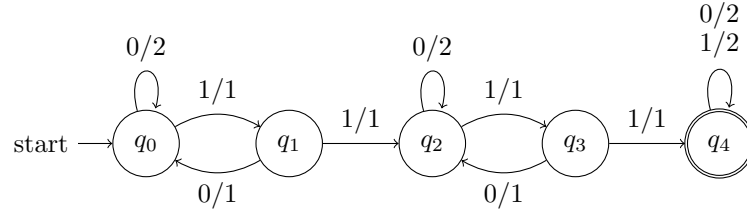


Fig. 4: An automaton with skip edges representing the language from Figure 3. A skip edge labeled with a/n can be interpreted as a transition of length n reading symbol a .

The potential benefit of skip edges is derived from the fact that the length of the jump edge is not limited. Therefore, the jump edge can traverse over many BDDs and automaton states. The demonstration of this benefit is shown in Figure 5. However, it has to be mentioned that this approach overcomplicated algorithms and did not show improvement in space or time.

3.3 Automata with Indexed States

The approach of an automaton with indexed states has been the main focus of the reimplementations. This automaton integrates BDDs directly while maintaining the indexes on its nodes (automaton states).

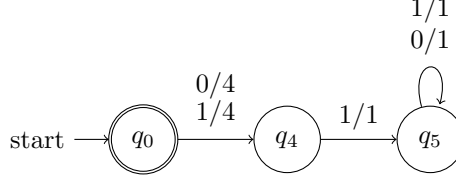


Fig. 5: Automaton with skip edges and a variable X representing formula $3 \in X$.

Definition 8. Let $M = (Q, \Sigma, \delta, Q_0, F)$ be an NFA. The automaton M is called an automaton with indexing if there exists an index function $\iota : Q \rightarrow \mathbb{N}_0$ such that the following conditions hold:

1. $\forall q \in Q_0 : \iota(q) = 0$
2. $\forall q \in F : \iota(q) = 0$
3. $\forall q, r \in Q : \nexists a \in \Sigma : r \in \delta(q, a) \wedge \iota(r) \neq 0 \wedge \iota(q) \geq \iota(r)$

The first and second conditions reflect the fact that only roots or leaves in the BDD can be initial and final states, respectively. The third condition demands that the part of the automaton simulating the BDD must be acyclic, with the exception of the root nodes/states with index 0.

The index information determines the length of a jump based on the indices of the source and destination states in the automaton's transition. Due to the indexing sequence following a pattern of $0, 1, \dots, n-1, 0, 1, \dots, n-1, 0, \dots$, the longest jump can only reach to the next state with an index of 0. Although it might seem like a step back from Bednár's method, the restriction on jump length actually simplifies all algorithms. Interestingly, this leads to a noticeably faster evaluation of the input formula, up to 10 times faster when compared to the version incorporating skip edges.

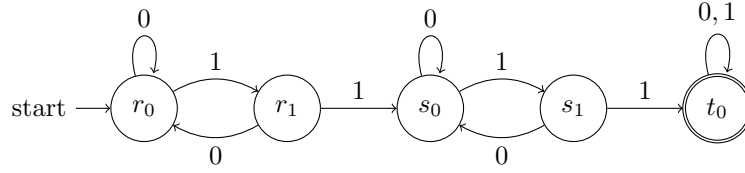


Fig. 6: Automaton with indexed states representing the language from Figure 3.

4 Automata operations

This section presents operations on automata with indexed states, which are utilized in the reimplementation of Mona using the Mata library. The primary concept of these operations is derived from Mata and modified to adapt state indexing.

4.1 Complement

The complementation method is the most straightforward approach for automata. It involves taking a complete deterministic automaton and swapping its accepting and nonaccepting states. Unlike classical complementation, only the accepting property of states with index 0 is altered.

Definition 9. Let $M = (Q, \Sigma, \delta, q_0, F)$ be a complete DFA with an index function $\iota : Q \rightarrow \mathbb{N}_0$. The complement of M is defined as $\mathbb{C}M = (Q, \Sigma, \delta, q_0, F')$, where:

$$F' = \{q \in Q \mid \iota(q) = 0 \wedge q \notin F\} \setminus \{q_0\}$$

4.2 Intersection

The standard intersection algorithm generates the resulting automaton using product construction. It begins with the pair of initial states (one from each automaton) and moves synchronously to the pair of successors based on the transition symbol. The key idea is inspired by the *Apply* method [16], commonly used in Binary Decision Diagram (BDD) operations. In this adaptation of the standard intersection algorithm, the successor pair for the analyzed product pair is generated based on the state (or states in the case of identical indices) with the lower index. Meanwhile, the state with the higher index is duplicated.

Algorithm 1: An Intersection Algorithm

Data: Two DFA $M = (Q_M, \Sigma_M, \delta_M, i_M, F_M)$ and $N = (Q_N, \Sigma_N, \delta_N, i_N, F_N)$ and two index functions $\iota_M : Q_M \rightarrow \mathbb{N}_0$ and $\iota_N : Q_N \rightarrow \mathbb{N}_0$

Output: Resulting DFA $A = (Q \subseteq Q_M \times Q_N, \Sigma \subseteq \Sigma_M \cup \Sigma_N, \delta, i, F) = M \cap N$ and index function $\iota_Q : Q \rightarrow \mathbb{N}_0$

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1 Procedure Index( $s_M, s_N$ )
2   if  $\iota_N(s_N) = 0$  then
3     return  $\iota_M(s_M)$ 
4   if  $\iota_M(s_M) = 0$  then
5     return  $\iota_N(s_N)$ 
6   return  $\min(\iota_M(s_M), \iota_N(s_N))$ 

7 Procedure Createq_State( $x, y$ )
8    $worklist \leftarrow worklist \cup \{(x, y)\}$ 
9    $Q \leftarrow Q \cup \{(x, y)\}$ 
10   $\iota((x, y)) \leftarrow Index(x, y)$ 
11  if  $x \in F_M \wedge y \in F_N$  then
12     $F \leftarrow F \cup (x, y)$ 

13  $i \leftarrow (i_M, i_N)$ 
14  $\iota(i) \leftarrow 0$ 
15  $worklist \leftarrow \{i\}$ 
16  $Q \leftarrow Q \cup \{i\}$ 
17 while  $worklist \neq \emptyset$  do
18    $s \leftarrow worklist.pop()$ 
19    $(s_M, s_N) \leftarrow s$ 
20   if  $\iota_M(s_M) = \iota_N(s_N)$  then
21     forall  $a \in \Sigma_M \cap \Sigma_N : \delta_M(s_M, a) \in Q_M \wedge \delta_N(s_N, a) \in Q_N$  do
22        $\delta(s, a) \leftarrow (\delta_M(s_M, a), \delta_N(s_N, a))$ 
23        $Create\_State(\delta_M(s_M, a), \delta_N(s_N, a))$ 
24   else if  $(\iota_M(s_M) < \iota_N(s_N) \wedge \iota_M(s_M) \neq 0) \vee \iota_N(s_N) = 0$  then
25     forall  $a \in \Sigma_M : \delta_M(s_M, a) \in Q_M$  do
26        $\delta(s, a) \leftarrow (\delta_M(s_M, a), s_N)$ 
27        $Create\_State(\delta_M(s_M, a), s_N)$ 
28   else
29     forall  $a \in \Sigma_M : \delta_M(s_M, a) \in Q_M$  do
30        $\delta(s, a) \leftarrow (s_M, \delta_N(s_N, a))$ 
31        $Create\_State(s_M, \delta_N(s_N, a))$ 
32 return  $A, \iota$ 

```

4.3 Determinization

The standard determinization algorithm constructs the resulting automaton through subset construction. It initiates the process with a set of initial states and creates transitions to the set of targets, which consists of successors of states from the examined

set. This method requires modification for compatibility with the index function, similarly as in the intersection algorithm. Specifically, the set of successors of the examined state (representing the set of states) is formed by the states with the lowest index, while the remaining states are duplicated.

Algorithm 2: A Determinization Algorithm

Data: A NFA $M = (Q_M, \Sigma, \delta_M, I_M, F_M)$ and an index function $\iota_M : Q_M \rightarrow \mathbb{N}_0$

Output: A DFA $A = (Q \subseteq 2^{Q_M}, \Sigma, \delta, i, F)$ such that $L(M) = L(A)$ and an index function $\iota : Q \rightarrow \mathbb{N}_0$

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1  $i \leftarrow I_M$ 
2  $\iota(i) \leftarrow 0$ 
3  $worklist \leftarrow \{i\}$ 
4 while  $worklist \neq \emptyset$  do
5    $s \leftarrow worklist.pop()$ 
6    $waiting \leftarrow \{q \in s \mid \iota_M(q) \neq \iota(s)\}$ 
7    $cont \leftarrow s \setminus waiting$ 
8    $symbols \leftarrow \{a \in \Sigma \mid \forall q \in cont : \delta(q, a) \neq \emptyset\}$ 
9   forall  $a \in symbols$  do
10     $s\_next \leftarrow waiting \cup \bigcup_{q \in cont} \delta(q, a)$ 
11     $\delta(s, a) \leftarrow s\_next$ 
12     $worklist \leftarrow worklist \cup \{s\_next\}$ 
13    if  $\forall q \in s\_next : \iota_M(q) = 0$  then
14       $\iota(s\_next) \leftarrow 0$ 
15    else
16       $\iota(s\_next) \leftarrow \min(\{q \in s\_next \mid \iota_M(q) \neq 0\})$ 
17    if  $iota(s\_next) = 0 \wedge F_M \cap s\_next \neq \emptyset$  then
18       $F \leftarrow F \cup \{s\_next\}$ 
19  if  $waiting \neq \emptyset$  then
20    forall  $a \in \Sigma \setminus symbols$  do
21       $\delta(s, a) \leftarrow waiting$ 
22       $worklist \leftarrow worklist \cup \{waiting\}$ 
23      if  $\forall q \in waiting : \iota_M(q) = 0$  then
24         $\iota(waiting) \leftarrow 0$ 
25      else
26         $\iota(waiting) \leftarrow \min(\{q \in waiting \mid \iota_M(q) \neq 0\})$ 
27      if  $iota(waiting) = 0 \wedge F_M \cap waiting \neq \emptyset$  then
28         $F \leftarrow F \cup \{waiting\}$ 
29 return  $A$ 

```

4.4 Union

The only distinction between the union of two NFAs and the union of two NFAs with index functions is the necessity to unify the functions as well.

Definition 10. Let $M = (Q_M, \Sigma_M, \delta_M, I_M, F_M)$ be the first NFA with an index function $\iota_M : Q_M \rightarrow \mathbb{N}_0$ and $N = (Q_N, \Sigma_N, \delta_N, I_N, F_N)$ be the second NFA with an index function $\iota_N : Q_N \rightarrow \mathbb{N}_0$, where $Q_M \cap Q_N = \emptyset$. The union of M and N is an NFA $U = (Q_M \cup Q_N, \Sigma_M \cup \Sigma_N, \delta, I_M \cup I_N, F_M \cup F_N)$ where:

$$\delta(a, q) = \begin{cases} \delta_M(a, q) & q \in Q_M \\ \delta_N(a, q) & q \in Q_N \end{cases}$$

with an index function $\iota : Q_M \cup Q_N \rightarrow \mathbb{N}_0$ similarly as the transition function:

$$\iota(q) = \begin{cases} \iota(q) & q \in Q_M \\ \iota(q) & q \in Q_N \end{cases}$$

4.5 Projection

The basic idea of a projection is to remove all transitions going from the states with the index of the variable being removed and redirect all incoming edges into its successors. The only exception is the projection of the variable with the index 0. In that case, the redirection cannot be done due to the restriction that states with the index 0 cannot be jumped over. The solution to this problem is to create an edge for every letter of the alphabet and every successor of the examined state.

Algorithm 3: A Projection Algorithm

Data: A NFA $M = (Q_M, \Sigma, \delta_M, I, F)$, an index function $\iota_M : Q_M \rightarrow \mathbb{N}_0$, and $id \in \mathbb{N}_0$ of a variable being projected.

Output: A NFA $A = (Q \subseteq Q_M, \Sigma, \delta, I, F)$ and new index function $\iota : Q \rightarrow \mathbb{N}_0$

```

1   $A \leftarrow M$ 
2  if  $id = 0$  then
3       $\iota \leftarrow \iota_M$ 
4      forall  $q \in Q_M$  do
5          if  $\iota_M(q) \neq id$  then
6              continue
7           $succ \leftarrow \{r \in Q_M \mid \exists a \in \Sigma : \delta_M(q, a) \neq \emptyset\}$ 
8          forall  $a \in \Sigma$  do
9               $\delta(q, a) \leftarrow targets$ 
10     return  $A$ ;
11 forall  $q \in Q_M$  do
12     if  $\iota_M(q) \neq id$  then
13         continue
14      $succ \leftarrow \{r \in Q_M \mid \exists a \in \Sigma : \delta_M(q, a) \neq \emptyset \wedge \iota_M(r) \neq id\}$ 
15     forall  $a \in \Sigma, r \in Q_M : q \in \delta_M(r, a) \wedge \iota_M(r) \neq id$  do
16          $\delta(r, a) \leftarrow \delta(r, a) \cup succ$ 
17      $A.remove(q)$ 
18 forall  $q \in Q$  do
19     if  $\iota_M(q) > id$  then
20          $\iota(q) = \iota_M(q) - 1$ 
21     else
22          $\iota(q) = \iota_M(q)$ 
23 return  $A, \iota$ 

```

After the projection of the variable. There exists paths over symbol 0 leading from nonacceptance states into the accepting. This changes the language represented by the automaton and therefore every such state has to be marked as accepting with the exception to the initial state.

The automaton $M = (Q, \Sigma, \delta, I, F_M)$ is transformed into the automaton of the form $A = (Q, \Sigma, \delta, I, F)$, where $F = F_M \cup \{q \in Q \mid \text{exist path over 0 leading to } f \in F_M\}$.

4.6 Revert

Standart reversion algorithm switches initial and final states of the automaton and flips the direction of all transitions. This practice (of course with the inversion of the indexes) can be used on the automata with indexed states only when there in single

variable, otherwise each transition of the length $1 < n$ over a symbol $a \in \Sigma$ has to be reverted and divided into two sections. The first section containing transitions of the length $n-1$ over all symbols from Σ and the second section consisting of the transition of the length 1 containing the symbol a .

Definition 11. Let $n \in \mathbb{N}$ be the number of variables and $\iota : Q \rightarrow \mathbb{N}_0$ be the index function. The length of a transition between states $q, r \in Q$ over a letter $a \in \Sigma$ is determined as:

$$\text{len}(q \xrightarrow{a} r) = \begin{cases} \iota(r) - \iota(q) & \iota(r) \neq 0 \\ n - \iota(q) & \text{otherwise} \end{cases}$$

Definition 12. Let $M = (Q_M, \Sigma, \delta_M, I, F)$ be NFA, $n \in \mathbb{N}$ be the number of variables being used in the automaton, and $\iota : Q \rightarrow \mathbb{N}_0$ be the index function. The reverted automaton is defined as $A = (Q := Q_N \cup Q_M, \Sigma, \delta, I, F)$ with the index function $\iota : Q \cup \mathbb{N}_0$ where:

- $Q_N = \{(q, a, r) \in Q_M \times \Sigma \times Q_M \mid r \in \delta(q, a) \wedge \text{len}(q \xrightarrow{a} r) \neq 1\}$ is a set of new states.
- $\delta(q, a) = \begin{cases} \{r \in Q_M \mid q \in \delta_M(r, a) \wedge \text{len}(r \xrightarrow{a} q) = 1\} \cup \\ \{(r, b, q) \in Q_N \mid \exists b \in \Sigma : q \in \delta_M(r, b)\} & q \in Q_M \\ \{(r, a, q) \in Q_N \mid r \in \delta(a, q)\} & \text{otherwise} \end{cases}$
- $\iota(q) = \begin{cases} n - 1 - \iota_M(q) & q \in Q_M \\ \iota_M(q') + 1 & q := (q', a, r') \in Q_N \end{cases}$

4.7 Minimization

The fast minimization for incomplete deterministic automata [17] performing in $\mathcal{O}(m \log n)$ time has been implemented. Unfortunately, benchmarks have revealed that minimization needs to be performed not only according to the forward language equivalence but also by considering the backward language equivalence. This algorithm cannot be used for this task, as even though the input automaton is deterministic, it can still have nondeterministic transitions in the backward direction.

Since this algorithm is unsuitable for automata minimization and the minimization algorithm based on the simulation provided by the Mata library is too slow, the naive Brzozowski algorithm [18] has been chosen. This algorithm minimizes DFA by reverting and determinizing the input automaton, and then reverting and determinizing it again.

Automata minimization performed by the Brzozowski algorithm corresponds to the elimination of isomorphic subgraphs in BDD reduction. The elimination of nodes with equivalent *high* and *low* successors can also be performed on the automata. The elimination of a state is simply done by redirecting all incoming transitions to all its successors. The only exception is the elimination of a state with index 0. Those states will have to remain in the automaton even if their *high* and *low* successors are the same state.

Definition 13. Let $M = (Q, \Sigma, \delta, i, F)$ be DFA with the index function $\iota : Q \rightarrow \mathbb{N}_0$. The automaton M is called reduced DFA if it is in the minimal form and $\forall q \in Q : \iota(q) = 0 \vee (\nexists r \in Q : \forall a \in \Sigma : r = \delta(q, a))$.

5 Experimental results

This section is dedicated to comparing the performance of Mona, its reimplementation using skip edges, and its reimplementation using state indexing. The experiments were performed on a machine equipped with an AMD Ryzen 7 3800XT 8-Core processor and 32 GB of memory. The comparison begins with an analysis of intersection and projection operations, with the focus on the execution times. Subsequently, the focus shifts to a comprehensive evaluation of the implementations on WS1S formulae

decision. The benchmark formulae employed in these experiments have been sourced from the Gaston project³, and Bednár’s Master thesis [11].

5.1 Operations performance

Individual operations, such as intersection and projection, have been tested on automata generated during the decision of WS1S procedures from [11]. Complementation was omitted from testing due to its simplicity, and minimization was excluded since the Brzozowski algorithm is widely known for its slow performance. Determinization was not tested because the tool Mona does not produce nondeterministic automata. Nevertheless, the impact of determinization can be indirectly observed during the examination of the projection of the first variable. Each projection must be followed by a determinization, and by projecting the first variable and subsequently the rest, the automaton attains the highest level of nondeterminism.

Intersection

Figures 7 and 8 reveal that the State Indexing reimplementation was, on average, approximately ten times faster than Skip Edges. When comparing State Indexing to the tool Mona, it becomes evident that Mona is still faster on average than State Indexing. However, there are instances where State Indexing surpasses Mona by more than 50 times in speed. Additionally, State Indexing performs intersection at its worst case, with outliers, only up to ten times slower than Mona.

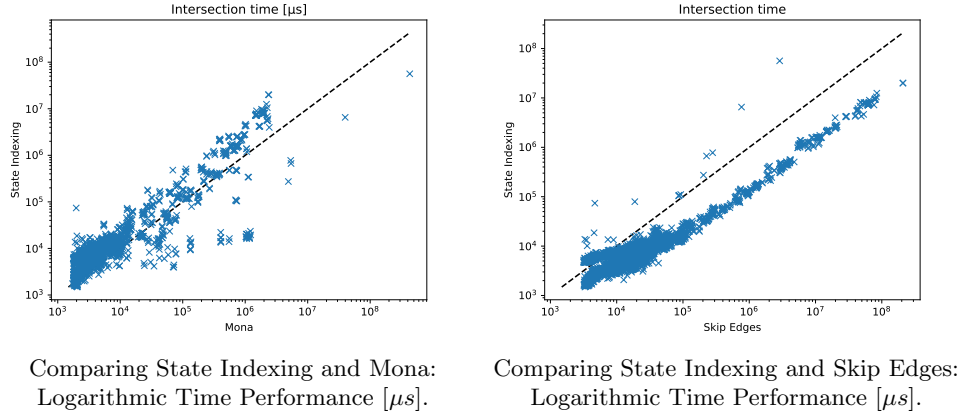


Fig. 7: Comparison of intersection time for the tool Mona, reimplementation using Skip Edges, and reimplementation using State Indexing.

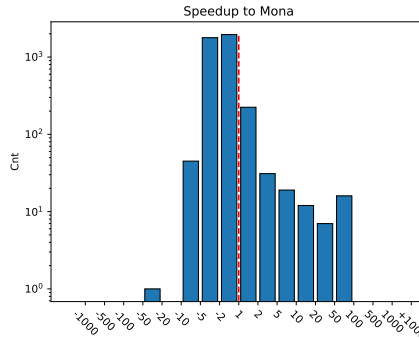


Fig. 8: State Indexing speedup compared to Mona intersection time. The vertical axis represents the number of occurrences (Logarithmic scale), while the horizontal axis indicates the speedup (positive) or slowdown (negative).

³available at: <https://www.fit.vutbr.cz/research/groups/verifit/tools/gaston/.cs>

Projection of the last variable

It is interesting to observe, as depicted in Figures 9 and 10, that the reimplementation with State Indexing was, on average, faster not only than the Skip Edges version but also than Mona. For certain inputs, State Indexing proved to be more than a thousand times faster than the tool Mona. Despite this, the maximal slowdown for State Indexing was only a factor of 5.

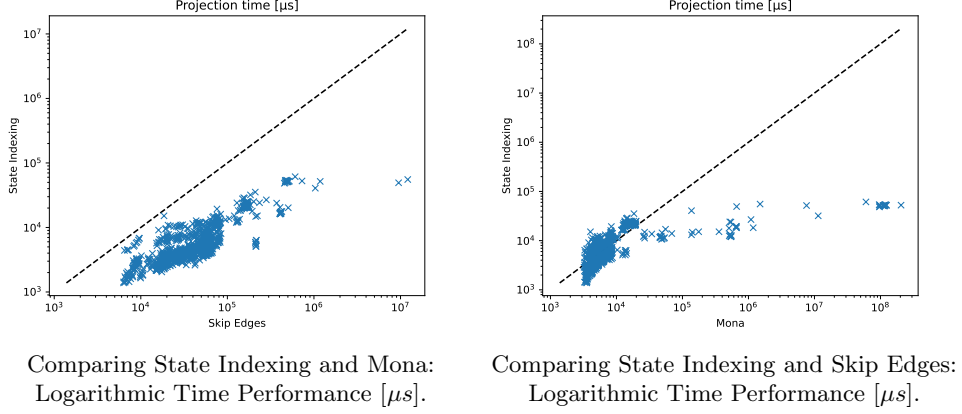


Fig. 9: Comparison of projection and determinization time for the tool Mona, reimplementation using Skip Edges, and reimplementation using State Indexing.

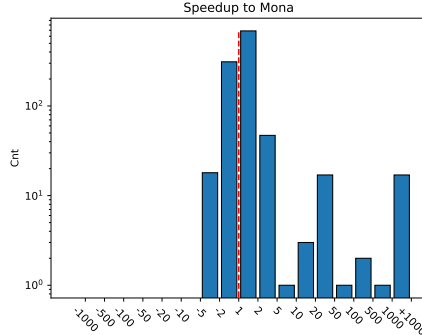


Fig. 10: State Indexing speedup compared to Mona projection time. The vertical axis represents the number of occurrences (Logarithmic scale), while the horizontal axis indicates the speedup (positive) or slowdown (negative).

Projection of the first variable

The projection of the first variable (the variable with the lowest id) was included in the experiments because projecting such a variable yields an automaton with the highest level of nondeterminism, and determinization is performed after every projection. Consequently, it can effectively substitute experiments focused on determinization.

Unsurprisingly, the State Indexing reimplementation is about ten times faster than Skip Edges. However, it is noteworthy that Mona and State Indexing performed almost identically. This similarity might be due to a less optimized projection algorithm in the Mona tool for the first variable or a potentially suboptimal determinization algorithm.

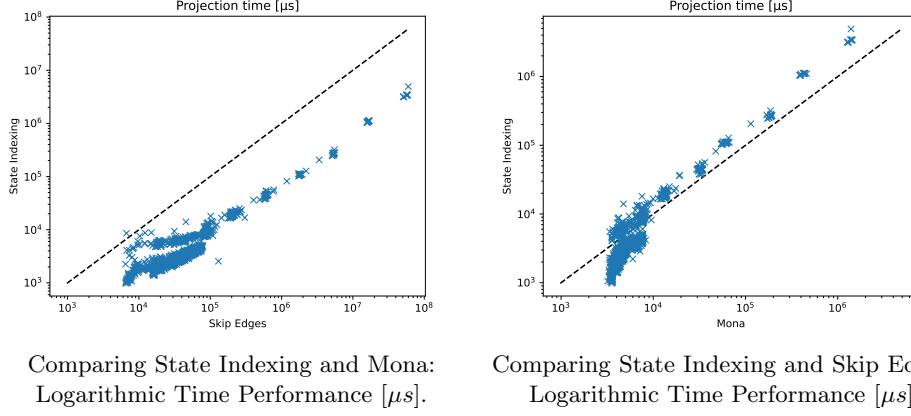


Fig. 11: Comparison of projection and determinization time for the tool Monas, reimplementation using Skip Edges, and reimplementation using State Indexing.

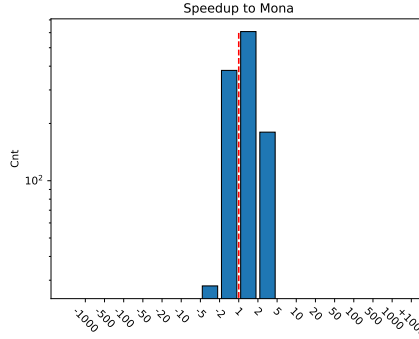


Fig. 12: State Indexing speedup compared to Monas projection time. The vertical axis represents the number of occurrences (Logarithmic scale), while the horizontal axis indicates the speedup (positive) or slowdown (negative).

5.2 WS1S formulae

This subsection presents results of the experiments performed on set of parametric families of WS1S formulae. The first benchmark **horn-in** consists of formulae used to evaluate the Toss toll as presented in [19], the second set of families (**horn-leq**) comes from the work of D’Antoni et al. [20], artificially enhanced by added quantifier alternations, and the last set of benchmarks (**horn-trans**, **set-closed**, and **set-singletons**) from the dWiNA tool [21].

Benchmark	SE-Mona [ms]	SI-Mona [ms]	Monas [ms]
horn-in	$\infty(7)$	$\infty(6)$	$\infty(16)$
horn-leq	209	101	$\infty(18)$
horn-leq (+3)	198	88	$\infty(18)$
horn-leq (+4)	193	88	$\infty(18)$
horn-trans	$\infty(15)$	$\infty(20)$	$\infty(16)$
set-closed	$\infty(5)$	$\infty(4)$	$\infty(5)$
set-singletons	$\infty(4)$	$\infty(4)$	$\infty(5)$

Table 1: Results of parametrized benchmarks up to $k = 20$. SE-Mona represents Monas reimplementation using Skip Edges and SI-Mona stands for the reimplementation utilizing State Indexing. $\infty(n)$ represents that the cumulative time of the computation of formulae $\leq n$ exceeded 10 second limit.

Experiments were focused on the cumulation time of computing parametrized formulae from easiest formulae to the most difficult. Once the cumulative time exceeded on the computation of the n -th formula, the tool was stopped with the result $\infty(n)$ otherwise the result was the value of the cumulative time.

Surprisingly, Mona completely failed on the `horn-leq` benchmarks. Mona consumed more time than ten seconds while both its reimplementation, that use DFAs instep of BDDs, finished within 200 milliseconds.

On the other hand Mona outperformed both reimplementations on `horn-in` benchmarks. This could be caused by using unefficient minimization algorithm (Brzozowski), because on this benchmarks the minimization takes 98% of the computation time.

As expected, the State Indexing reimplementation has been faster than Skip Edges and Mona in majority of benchmarks. Altho, the speedup was not as big as one can expect based on the performance of reimplemented automata operations. This behavior is cause by high number of performed minimization, that have not been optimized.

6 Conclusion

This paper presented new reimplementation of the tool Mona for decision procedures in WS1S. The main motivation was to used pure automata-base approach instep of the combination of BDDs and automata. The reimplementation is based on the Bc. Pavel Bednár's master's thesis, where jumping edges have been used to mimic behavior of BDDs used by Mona. Our reimplementation uses state indexing with Mata automata library to simulate indexes of inner nodes in BDDs.

Using the pure automata-base approach, the state indexing method resulted in ten times faster computation of determinization, intersection, and projection. On benchmark formulae, that have been designed to stress-test Mona, both reimplementations performed better, only with one exception, that can be caused by using inefficient Brzozowski minimization algorithm. Our reimplementation outperformed on Bednár's reimplementation on majority of benchmarks with two to ten speedup.

The reimplementation have can be further improved by implementing better simulation based minimization, utilizing abstraction, or by performing operations on nondeterministic finite automata instead of deterministic.

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