

# Differentiable Neural Computers

HYBRID COMPUTING USING A NEURAL NETWORK WITH  
DYNAMIC EXTERNAL MEMORY (GRAVES ET AL. 2016)

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Logic and Computation

# Overview: Probabilistic Programming

## Cross-domain

- Data Flow Programming
- Bayesian Reasoning
- Machine Learning
- Functional Programming

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PROGRAM	MODEL
Discrete	Continuous
Deterministic	Stochastic
Static	Adaptive

## Overview: DNC

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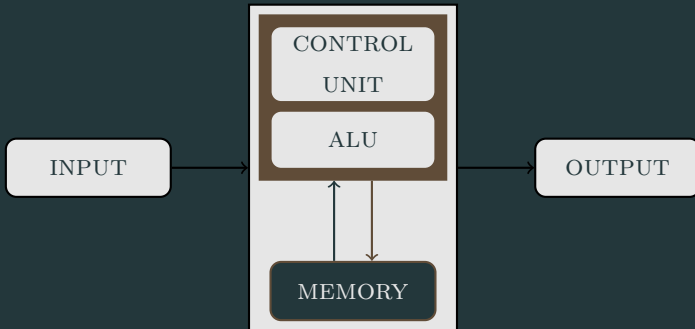
## Differentiable Neural Computer

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- Extension of NTMs
  - End-to-end differentiable
  - Auto-associative memory
  - Turing complete
  - + Memory attention mechanisms
- Mimic mammalian biological memory
- Employ classical concepts of computation

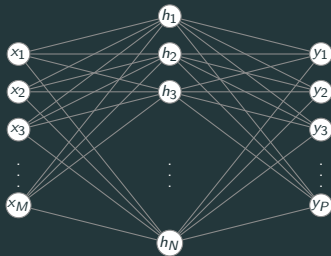
# Introduction: Classic Computation

## Von Neumann architecture



# Introduction: RNNs

## Simple Neural Net

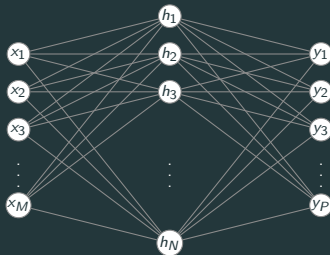


$$y = g(h), \quad h = f(x)$$

No memory

# Introduction: RNNs

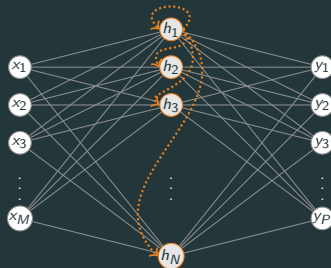
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## Simple Recurrent Net

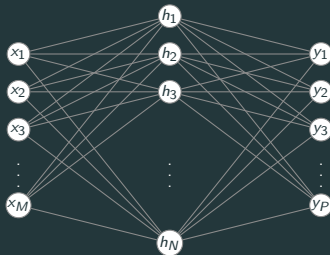


$$h(t) = f([x(t); h(t-1)])$$

Finite, non-contiguous memory

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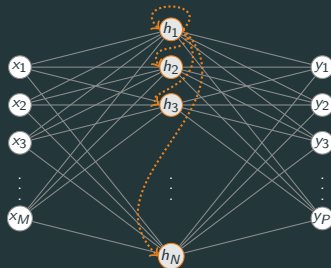
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Finite, non-contiguous memory

*"If training vanilla neural nets is optimization over functions,  
training recurrent nets is **optimization over programs**."*

## Approach

Train a RNN to act as a **controller** to interact with a memory matrix  $M$  of  $N$  (arbitrary many) addresses.



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## 1. Content Lookup

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- Compare controller output with memory objects  
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## 3. Dynamic Allocation

- Mark memory locations with  $\{0, 1\}$  to **signal usage**
- Manipulate signals during R/W operations to enable **reallocation**
- Generalization to **unbounded memory**

# Controller

A deep long short-term memory network receiving input:

$$\boldsymbol{\mathcal{X}}_t = [\mathbf{x}_t; \mathbf{r}_{t-1}^1; \dots; \mathbf{r}_{t-1}^R]$$

and producing output:

$$(\mathbf{v}_t, \boldsymbol{\xi}_t) = \mathcal{N}([\boldsymbol{\mathcal{X}}_1; \dots; \boldsymbol{\mathcal{X}}_T]; \vartheta)$$

where  $\mathcal{N}$  a set of state equations and  $\vartheta$  their trainable parameters.

## Controller: State Equations

A more detailed look into  $\mathcal{N}$ :

$$\mathbf{i}_t' = \sigma(W_i'[\boldsymbol{\chi}_t; \mathbf{h}_{t-1}'; \mathbf{h}_t'^{-1}] + \mathbf{b}_i') \quad (\text{input gate})$$

$$\mathbf{f}_t' = \sigma(W_f'[\boldsymbol{\chi}_t; \mathbf{h}_{t-1}'; \mathbf{h}_t'^{-1}] + \mathbf{b}_f') \quad (\text{forget gate})$$

$$\mathbf{s}_t' = \mathbf{f}_t' \mathbf{s}_{t-1}' + \mathbf{i}_t' \tanh(W_s'[\boldsymbol{\chi}_t; \mathbf{h}_{t-1}'; \mathbf{h}_t'^{-1}] + \mathbf{b}_s') \quad (\text{state})$$

$$\mathbf{o}_t' = \sigma(W_o'[\boldsymbol{\chi}_t; \mathbf{h}_{t-1}'; \mathbf{h}_t'^{-1}] + \mathbf{b}_o') \quad (\text{output gate})$$

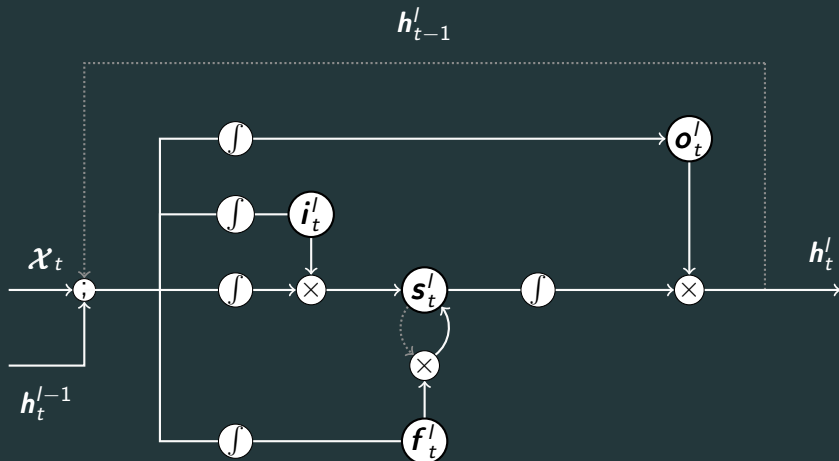
$$\mathbf{h}_t' = \mathbf{o}_t' \tanh(\mathbf{s}_t') \quad (\text{hidden})$$

$$\mathbf{v}_t = W_y[\mathbf{h}_t^1; \dots; \mathbf{h}_t^L] \quad (\text{output vector})$$

$$\boldsymbol{\xi}_t = W_\xi[\mathbf{h}_t^1; \dots; \mathbf{h}_t^L] \quad (\text{interface vector})$$

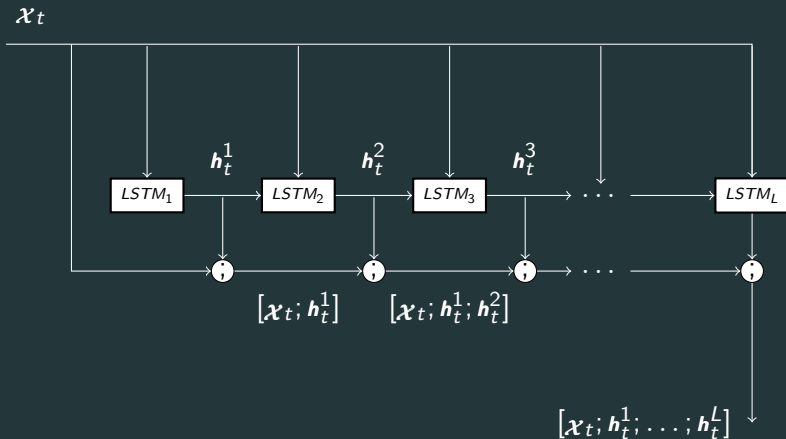
## Controller: Signal-Flow

### Single LSTM layer



## Controller: Full View

### LSTM Network (multiple layers)



## Controller: Outputs

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$$\boldsymbol{y}_t = \boldsymbol{v}_t + W_R[\boldsymbol{r}_t^1; \dots; \boldsymbol{r}_t^R] \quad (\text{Memory-conditioning})$$

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Interface vector  $\xi_t$

- Read keys
- Read strengths
- Write key
- Write strength
- Erase vector
- Write vector
- Free gates
- Allocation gate
- Write gate
- Read modes

# Memory Addressing: RW

Read operations

$$\mathbf{r}_t^i = \mathbf{M}_t^T \mathbf{w}_t^{r,i}$$

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Write operations

$$M_t = M_{t-1} \circ (\mathbf{1} - \mathbf{w}_t^T \mathbf{e}_t^T) + \mathbf{w}_t^W \mathbf{v}_t^T$$

## Further Reading

- [Neural Turing Machines](#) (Graves, Wayne, Danihelka)
- [Entity Networks](#) (Henaff, Weston, Szlam, Bordes, LeCun)
- [Principles of Probabilistic Programming Languages](#) (Goodman)
- [Backprop as a Functor](#) (Fong, Spivak, Tuyras)
- [Formal Methods for Probabilistic Programming](#) (Selsam, Liang, Dill)