Differentiable Neural Computers

Hybrid Computing using a neural network with dynamic external memory (Graves et al. 2016)

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Logic and Computation

Differentiable Neural Computer

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A recurrent neural network coupled with an external memory.

• Extension of NTMs

Differentiable Neural Computer

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 - End-to-end differentiable

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 - Auto-associative memory

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 - Turing complete

Differentiable Neural Computer

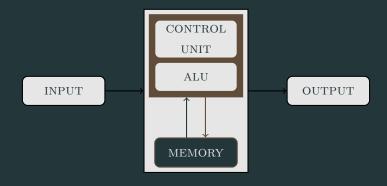
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 - + Memory attention mechanisms

Differentiable Neural Computer

- Extension of NTMs
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 - Turing complete
 - + Memory attention mechanisms
- Mimic mammalian biological memory
- Employ classical concepts of computation

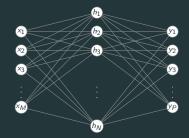
Introduction: Motivation

Von Neumann architecture



Introduction: Motivation

Simple Neural Net

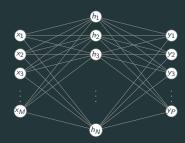


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No memory

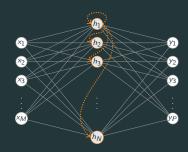
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Simple Neural Net



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Recurrent Neural Net



$$h(t) = f([x(t); h(t-1)])$$

Finite, non-contiguous memory

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3. Dynamic Allocation

- Mark memory locations with $\{0,1\}$ to signal usage
- Manipulate signals during R/W operations to enable reallocation
- Generalization to unbounded memory

Controller

A deep long-short term memory unit receiving

$$\boldsymbol{\mathcal{X}}_t = [\boldsymbol{x}_t; \boldsymbol{r}_{t-1}^1; \dots \boldsymbol{r}_{t-1}^R]$$

and producing

a