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rf433 PUT.ino
#include "rf433.h"
void setup()
  Serial.begin(9600);
  rf(-1,10);
byte buf[RF\_MAX\_LEN]\{'s','i','s','\ddot{a}','l','t','\ddot{o}'\};
int milli=millis();
void loop()
 if(millis()>milli+1000){
  milli=millis();
  rfPUT((byte*)buf, 7);
 if(Serial.available()){
  Serial.print(Serial.readString());
rf433 GET.ino
#include "rf433.h"
void setup()
  Serial.begin(9600);
  rf(11,-1);
byte buf[RF MAX LEN];
void loop()
  if (byte ile=rfGET(buf)){
  for (byte i = 0; i < ile; i++) Serial.print((char)buf[i]);
  Serial.println();
}
rf433.h
#ifndef rf433 h
#define rf433 h
#include <Arduino.h>
#include <stdlib.h>
#undef abs
#undef double
#undef round
#define RF MAX LEN 30
#define VW MAX PAYLOAD RF MAX LEN-3
#define VW RX RAMP LEN 160
#define VW RX SAMPLES PER BIT 8
#define VW RAMP INC (VW RX RAMP LEN/VW RX SAMPLES PER BIT)
#define VW RAMP TRANSITION VW RX RAMP LEN/2
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#define VW RAMP ADJUST 9
#define VW RAMP INC RETARD (VW RAMP INC-VW RAMP ADJUST)
#define VW RAMP INC ADVANCE (VW RAMP INC+VW RAMP ADJUST)
#define VW HEADER LEN 8
extern "C"
        extern void rf(int rxPin,int txPin);
        extern uint8 t rfPUT(byte* buf, byte len);//(>RF MAX LEN - 3)?0:1
        extern byte rfGET(byte* buf);
#endif
rf433.cpp
#include "rf433.h"
#include <util/crc16.h>
static uint8 t vw rx pin = 11;
static uint8 t vw tx pin = 12;
static uint8 t vw tx buf[(RF MAX LEN * 2) + VW HEADER LEN] = \{0x2a, 0x2a, 0x
0x2a, 0x2a, 0x38, 0x2c;
static uint8 t vw tx len = 0;
static uint8 t vw tx index = 0;
static uint8 t vw tx bit = 0;
static uint8 t vw tx sample = 0;
static volatile uint8 t vw tx enabled = 0;
static uint16 t vw tx msg count = 0:
static uint8 t vw rx sample = 0;
static uint8 t vw rx last sample = 0;
static uint8 t vw rx pll ramp = 0;
static uint8 t vw rx integrator = 0;
static uint8 t vw rx active = 0;
static volatile uint8 t vw rx done = 0;
static uint8 t vw rx enabled = 0;
static uint16 t vw rx bits = 0;
static uint8 t vw rx bit count = 0;
static uint8 t vw rx buf[RF MAX LEN];
static uint8 t vw rx count = 0:
static volatile uint8 t vw_rx_len = 0;
static uint8 t vw rx bad = 0;
static uint8 t vw rx good = 0;
static uint8 t symbols[] = \{0xd, 0xe, 0x13, 0x15, 0x16, 0x19, 0x1a, 0x1c, 0x23, 0x25, 0x26, 0x29, 0x16, 0x
0x2a, 0x2c, 0x32, 0x34;
extern "C"
uint16 t vw crc(uint8 t *ptr, uint8 t count)
        uint16 t crc = 0xffff;
        while (count--> 0) crc = crc ccitt update(crc, *ptr++);
        return crc;
uint8 t vw symbol 6to4(uint8 t symbol)
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uint8 ti;
  for (i = 0; i < 16; i++) if (symbol == symbols[i]) return i;
  return 0;
void vw rx start()
  if (!vw_rx_enabled)
       vw rx enabled = true;
       vw rx active = false;
void vw_rx_stop()
  vw_rx_enabled = false;
static uint8 t timer calc(uint16 t speed, uint16 t max ticks, uint16 t *nticks)
  uint16 t prescalers [] = {0, 1, 8, 64, 256, 1024, 3333};
  uint8 t prescaler=0;
  unsigned long ulticks;
  if (speed == 0)
     *nticks = 0;
     return 0;
  for (prescaler=1; prescaler < 7; prescaler += 1)
     float clock time = (1.0 / (float(F CPU) / float(prescalers[prescaler])));
     float bit time = ((1.0 / \text{float(speed)}) / 8.0);
     ulticks = long(bit time / clock time);
     if ((ulticks > 1) && (ulticks < max ticks))
       break;
  if ((prescaler == 6) || (ulticks < 2) || (ulticks > \max \text{ ticks}))
     *nticks = 0;
     return 0;
  *nticks = ulticks;
  return prescaler;
void vw setup(uint16 t speed)
  uint16_t nticks;
  uint8 t prescaler;
  prescaler = _timer_calc(speed, (uint16_t)-1, &nticks);
  if (!prescaler) return;
  TCCR1A = 0;
  TCCR1B = BV(WGM12);
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TCCR1B |= prescaler;
  OCR1A = nticks;
  #ifdef TIMSK1
   TIMSK1 \models BV(OCIE1A);
      #else
   TIMSK = BV(OCIE1A);
      #endif
  pinMode(vw tx pin, OUTPUT);
  pinMode(vw rx pin, INPUT);
}
void rf(int rxPin,int txPin){
 vw tx pin = (uint8 t)txPin;
 vw rx pin = (uint8 t)rxPin;
 vw setup(2000);
 if(rxPin!=-1)vw rx start();
void vw pll()
  if (vw rx sample) vw rx integrator++;
  if (vw rx sample != vw rx last sample)
      vw_rx_pll_ramp += ((vw_rx_pll_ramp < VW_RAMP_TRANSITION)</pre>
                      ? VW RAMP INC RETARD
                      : VW_RAMP_INC_ADVANCE);
      vw rx last sample = vw rx sample;
  else
      vw rx pll ramp += VW RAMP INC;
  if (vw rx pll ramp >= VW RX RAMP LEN)
      vw rx bits >>= 1;
      if (vw_rx_integrator >= 5)
         vw rx bits = 0x800;
      vw rx pll ramp -= VW RX RAMP LEN;
      vw rx integrator = 0;
      if (vw rx active)
         if (++vw rx bit count >= 12)
             uint8 t this byte =
                (vw symbol 6\text{to}4(\text{vw rx bits }\&\ 0\text{x}3\text{f})) << 4
                | vw symbol 6\text{to}4(\text{vw rx bits} >> 6);
             if (vw rx len == 0)
                vw rx count = this byte;
                if (vw rx count \leq 4 \parallel vw rx count \geq RF MAX LEN)
                     vw rx active = false;
                     vw_rx bad++;
              return;
```

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}
              vw_rx_buf[vw_rx_len++] = this_byte;
              if (vw_rx_len >= vw_rx_count)
                vw rx active = false;
                vw_rx_good++;
                vw rx done = true;
              vw_rx_bit_count = 0;
         }
       else if (vw_rx_bits == 0xb38)
         vw rx active = true;
         vw rx bit count = 0;
         vw rx len = 0;
         vw_rx_done = false;
  }
void vw_tx_start()
  vw_tx_index = 0;
  vw_tx_bit = 0;
  vw_tx_sample = 0;
  vw tx enabled = true;
void vw_tx_stop()
  digitalWrite(vw tx pin, false);
  vw_tx_enabled = false;
uint8_t vx_tx_active()
  return vw_tx_enabled;
void vw_wait_tx()
  while (vw_tx_enabled);
void vw wait rx()
  while (!vw_rx_done);
uint8 t vw wait rx max(unsigned long milliseconds)
  unsigned long start = millis();
  while (!vw_rx_done && ((millis() - start) < milliseconds));
  return vw_rx_done;
uint8_t rfPUT(byte* buf, byte len)
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```
uint8 t i;
  uint8 t index = 0;
  uint16 t crc = 0xffff;
  uint8 t *p = vw tx buf + VW HEADER LEN;
  uint8 t count = (uint8 t)(len + 3);
  if (len > VW_MAX PAYLOAD) return false;
  vw wait tx();
  crc = crc ccitt update(crc, count);
  p[index++] = symbols[count >> 4];
  p[index++] = symbols[count & 0xf];
  for (i = 0; i < len; i++)
      crc = crc ccitt update(crc, (uint8 t)buf[i]);
      p[index++] = symbols[(uint8 t)buf[i] >> 4];
      p[index++] = symbols[(uint8 t)buf[i] & 0xf];
  crc = \sim crc;
  p[index++] = symbols[(crc >> 4) & 0xf];
  p[index++] = symbols[crc & 0xf];
  p[index++] = symbols[(crc >> 12) \& 0xf];
  p[index++] = symbols[(crc >> 8) \& 0xf];
  vw tx len = index + VW HEADER LEN;
  vw tx start();
  return true;
byte rfGET(byte* buf)
  uint8 t rxlen;
  if (!vw rx done) return 0;
  rxlen = vw rx len - 3;
  memcpy(buf, vw rx buf + 1, RF MAX LEN);
  vw rx done = false;
  if((vw crc(vw rx buf, vw rx len) == 0xf0b8)==0)return 0;
  return rxlen;
SIGNAL(TIMER1 COMPA vect){
  if (vw rx enabled &&!vw tx enabled) vw rx sample = digitalRead(vw rx pin);
  if (vw tx enabled && vw tx sample++ == 0){
      if (vw tx index \geq= vw tx len)
         vw tx stop();
         vw tx msg count++;
      else
         digitalWrite(vw tx pin, vw tx buf[vw tx index] & (1 << vw tx bit++));
         if (vw tx bit \geq = 6)
         {
             vw tx bit = 0;
             vw tx index++;
```

```
}
if (vw_tx_sample > 7) vw_tx_sample = 0;
if (vw_rx_enabled && !vw_tx_enabled) vw_pll();
}
```