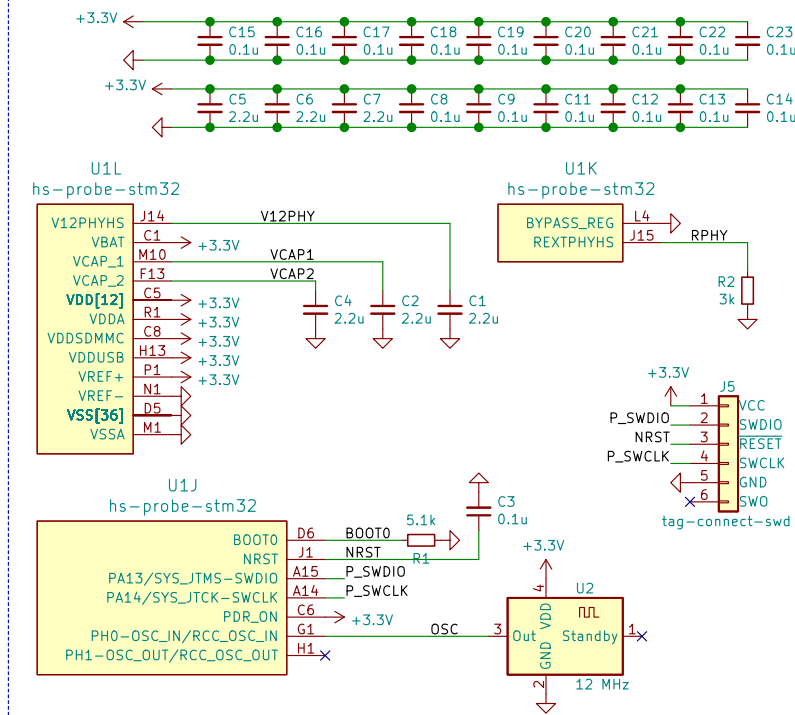
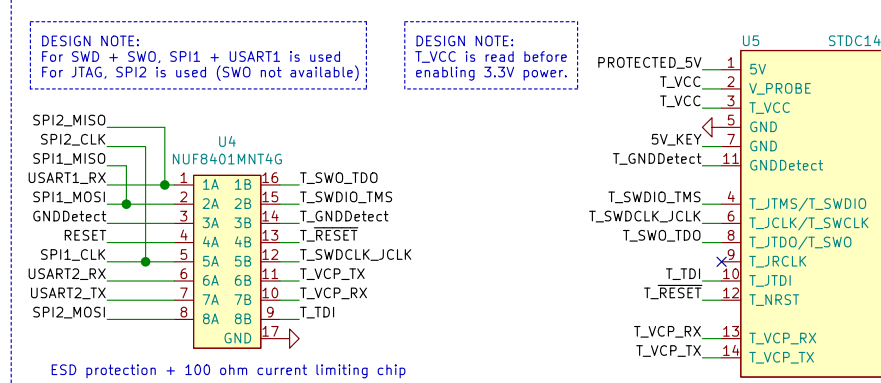


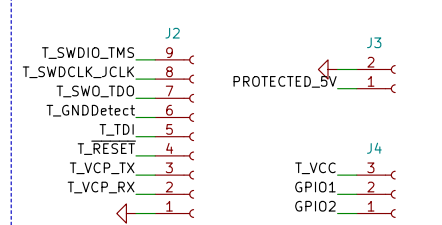
## MCU Power, Config and Clock



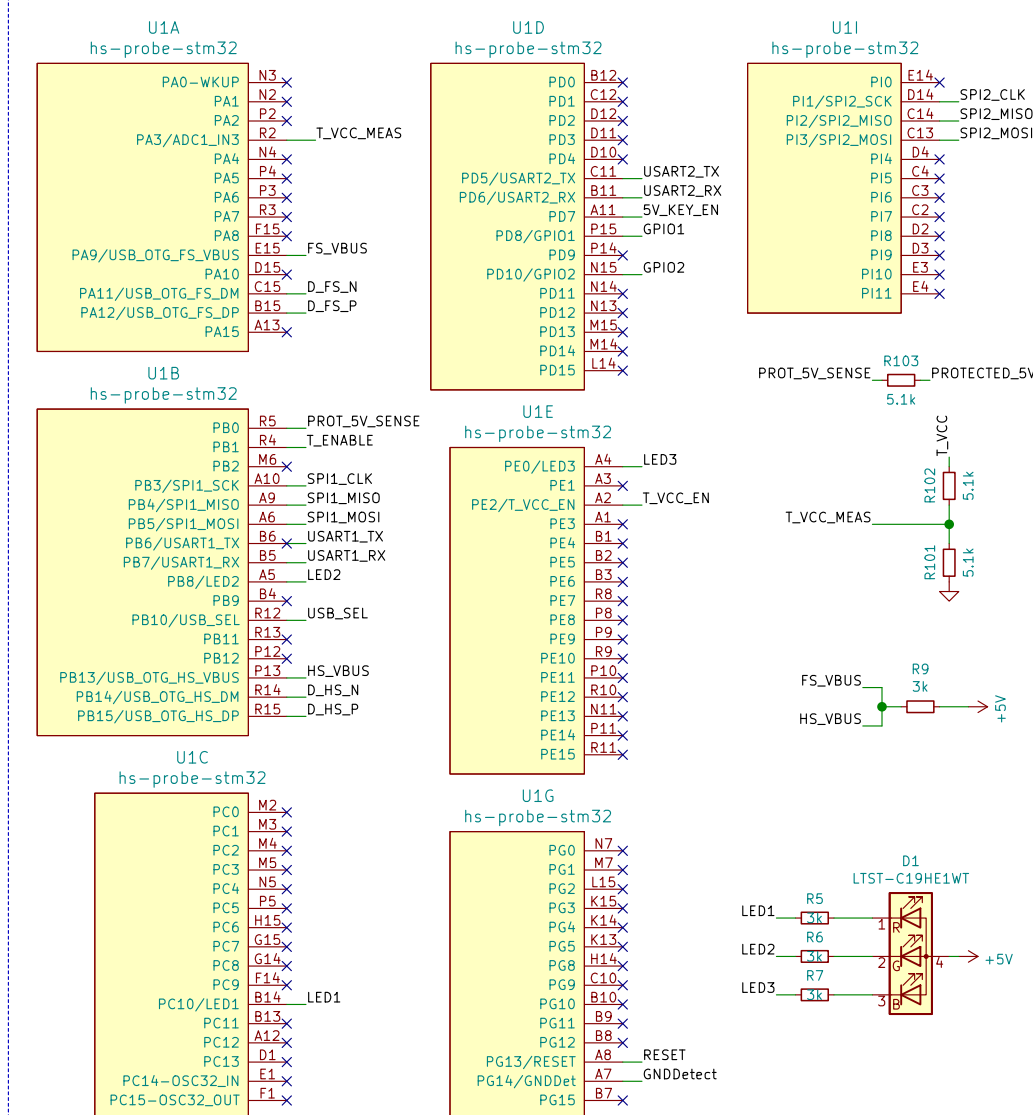
## Target SWD/JTAG port with ESD protection



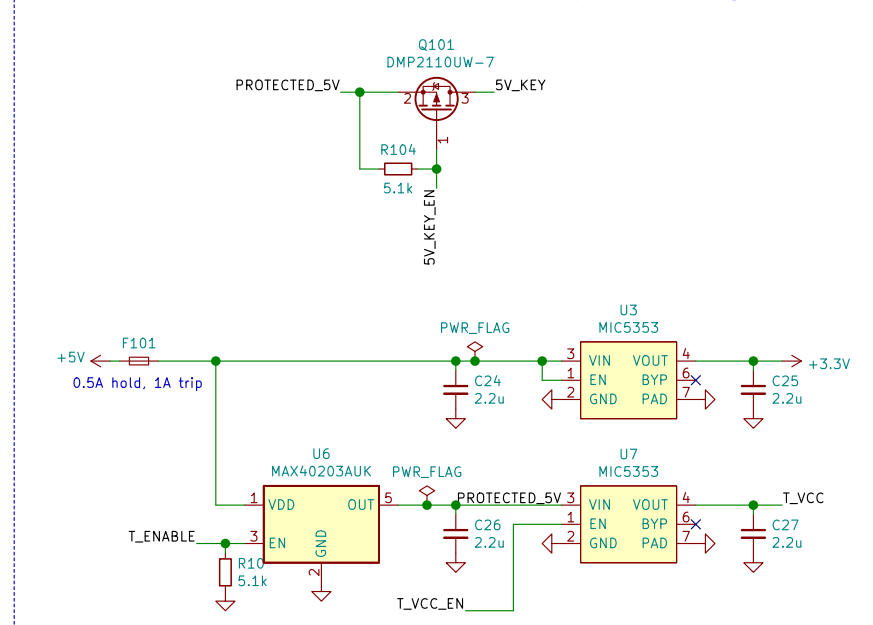
## Board-edge castellated vias



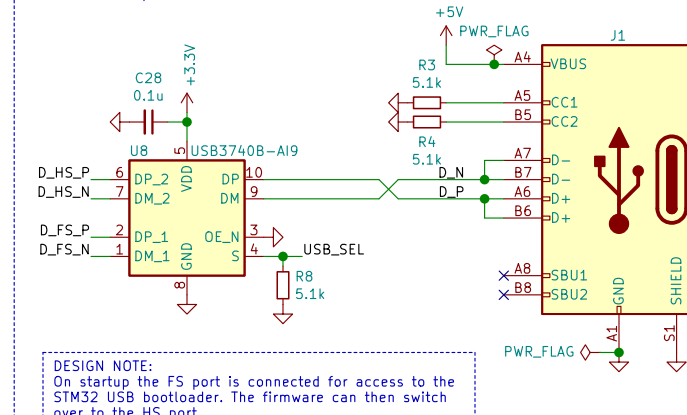
## MCU I/O



## Probe power and optional target power



## USB-C port with USB MUX



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Designed by: Emil Fresk  
Twitter / Github: @korken89  
Link: <https://github.com/probe-rs/hs-probe>

Sheet: /	File: hs-probe.sch	
<b>Title: Rusty High-Speed Probe</b>		
Size: A3	Date: 2020-11-28	Rev: v1.3
KiCad E.D.A. kicad 5.1.8		Id: 1/1