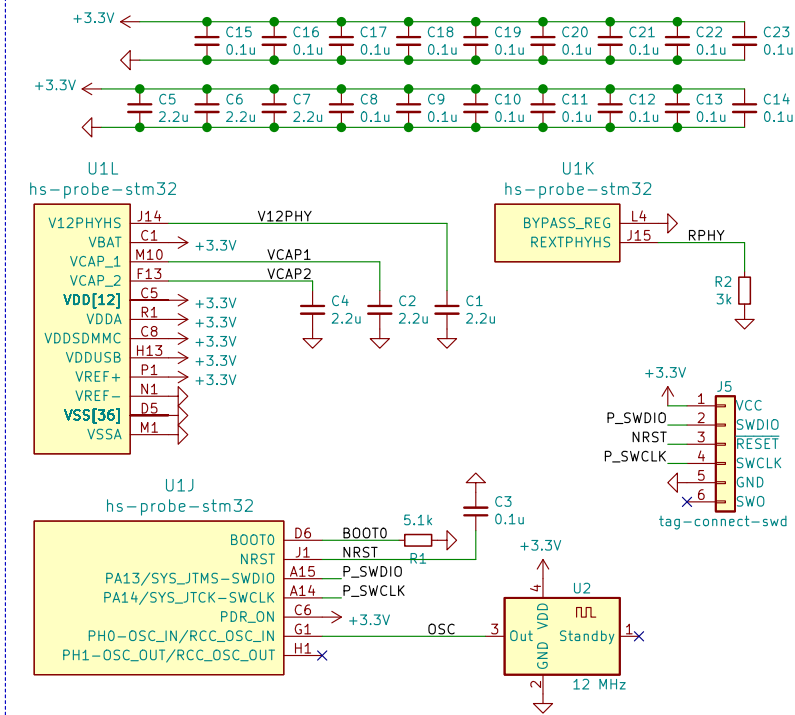
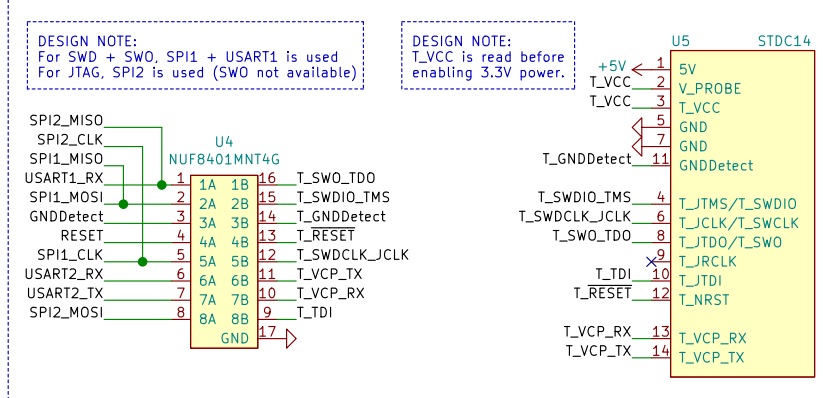


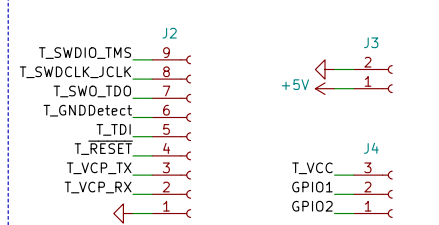
MCU Power, Config and Clock



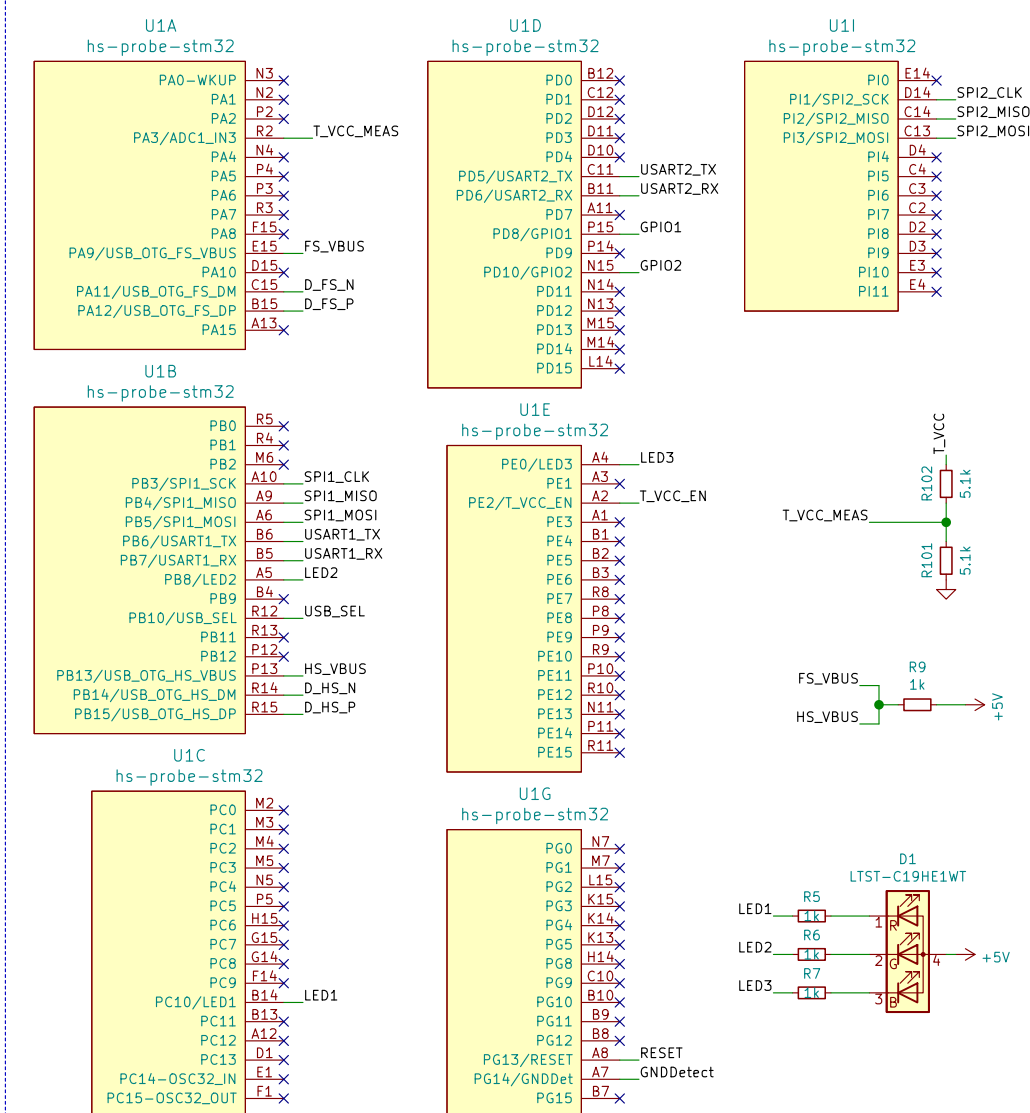
Target SWD/JTAG port with ESD protection



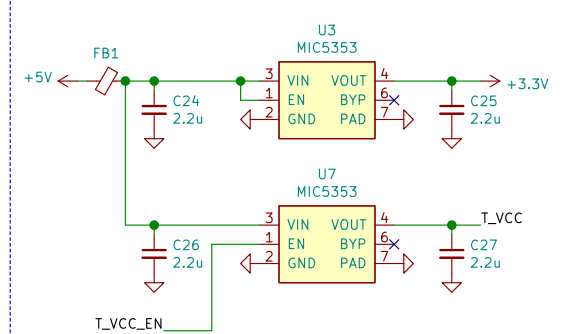
Board-edge castellated vias



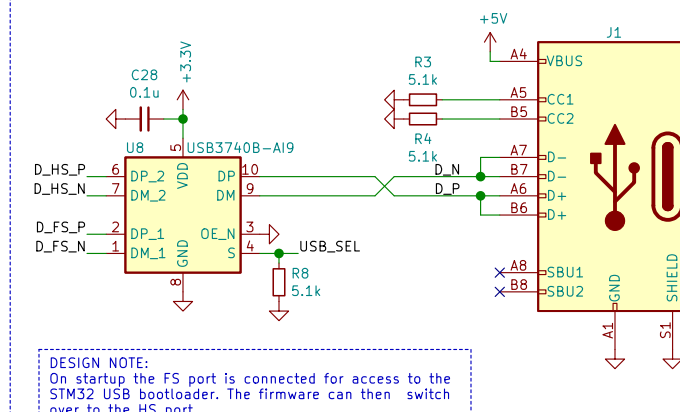
MCU I/O



Probe power and optional target power



USB-C port with USB MUX



Designed by: Emil Fresk
 Twitter / Github: @korken89
 Link: <https://github.com/korken89/hs-probe>

Sheet: /	File: hs-probe.sch	
Title: Rusty High-Speed Probe		
Size: A3	Date: 2020-07-19	Rev: v1.1
KiCad E.D.A. kicad 5.1.6		Id: 1/1