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C

Flash & Sensors

The image contains three circuit diagrams for a flash memory and two sensors.

MX25R1635F Flash: This diagram shows the connection of an MX25R1635F flash memory (U12) to a +1V8 power supply. The CS pin (pin 1) is connected to FLASH_CS through a 100k resistor (R31). The VCC pin (pin 8) is connected to +1V8 with a 0.1uF capacitor (C52). The DI(100) pin (pin 5) is connected to MOSI, and the DO(101) pin (pin 2) is connected to MISO. The CLK pin (pin 6) is connected to SCK. The I/O pins (pins 3, 7, and 9) are marked with an 'X', indicating they are not used.

DESIGN NOTE: The flash datasheet requires the CS pin to be high during power up.

LSM6DS3 Sensor: This diagram shows the connection of an LSM6DS3 sensor (U13) to a +1V8 power supply. The VDDIO pin (pin 8) is connected to +1V8 with a 0.1uF capacitor (C53). The SDO/SAO pin (pin 1) is connected to MISO, and the SDX pin (pin 3) is connected to MISO. The SDA pin (pin 14) is connected to MOSI, and the SCL pin (pin 13) is connected to SCK. The CS pin (pin 12) is connected to ACC_CS. The INT1 pin (pin 4) is connected to ACC_INT, and the INT2 pin (pin 9) is marked with an 'X', indicating it is not used.

LPS22HBTR Sensor: This diagram shows the connection of an LPS22HBTR sensor (U14) to a +1V8 power supply. The VDDIO pin (pin 1) is connected to +1V8 with a 0.1uF capacitor (C54). The SCK pin (pin 2) is connected to SCK, the SDI pin (pin 4) is connected to MISO, and the SDO pin (pin 5) is connected to MISO. The CS pin (pin 6) is connected to PRES_CS. The INT/DRDY pin (pin 7) is connected to PRES_INT. The GND pins (pins 9, 8, and 3) are connected to ground.

BT Antenna & Tuning

DESIGN NOTE
IFA antenna should
not need matching,
added just in case.

1

USB connector

USB_C_Receptacle_USB2.0

J2

VBUS- A4 VBUS_CON

CC1 A5 USB_CC1

CC2 B5 USB_CC2

D- A7 D-

D- B7 D-

D+ A6 D+

D+ B6 D+

SHIELD

GND

S1

A1

R13 5.1k

R14 5.1k

D7

C24 4.7u

C25 4.7u

FB2

VBUS

D8

ESD122DMYR

D- 2

3 D+

SBU1 A8

SBU2 B8

LTE Antenna & Tuning

The diagram illustrates the LTE antenna tuning circuit. The input signal from the LTE_ANT is fed into the circulator (U10, MMB130-2600). The output of the circulator is connected to a series combination of components: C2 (0.3pF), L1 (1.0nH), C3 (DNP), C4 (DNP), C5 (2.4pF), and L2 (24nH). The output of this series chain is connected to the LTE_ANT1 port. A ground connection is shown between C4 and L2. The antenna Ant3 is connected to the LTE_ANT1 port via a 2-wire connection. Below the main circuit, there are 12 components labeled R4 through R12, all marked 'DNP' (Do Not Populate).

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[illegible]

Mechanical

The diagram illustrates a mechanical system with four nodes, labeled H1, H2, H3, and H4, arranged in a horizontal line. Each node is represented by a red circle with a white center. To the right of each node is a corresponding mechanical tab, labeled T1, T2, T3, and T4. Each tab is a red rectangle with a white center, featuring a small white semi-circle on its left side. The tabs are arranged in two rows: T1 and T3 are in the top row, while T2 and T4 are in the bottom row. The tabs are connected to the nodes by thin red lines, indicating a mechanical linkage or assembly.

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