



# AX2210

## Product Summary

[AX2210-SU-101-EN]

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**AppoTech Limited**

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# AX2210 8-bit CMOS Microcontroller

## High performance 8-bit MCU

- DC-48MHz operation
- Compatible with 8051
- All instructions are single-cycle except branching instructions
- Two data pointer for indirect addressing

- CRC controller for CRC16 calculation
- One IR controller
- Full speed USB2.0 host/device controller
- One RTCC
- MIC AGC

## Program Memory and Data Memory

- 48K Bytes ROM
- 24K Bytes RAM
- 32K Bytes OTP
- 256 Bytes DRAM share with RAM

## Analog Peripheral Features

- One 32 KHz Crystal Oscillator
- One internal RC oscillator
- Full speed USB 2.0 PHY
- 48MHz PLL-based clock generator
- 16-bit Stereo DAC
- 16-bit  $\delta$ - $\Sigma$  ADC
- 8 Channels 10-bit ADC
- One class A/B Amplifier
- MIC Amplifier/Bias
- 2 channels AMUX
- 2 channels 4 levels Low Voltage Detector
- Power-on reset
- Two LDO: 5V to 1.8V, 5V to 3.3V

## Interrupt Feature

- 16 Vectored interrupts
- External wake up/interrupt on 4 GPIOs
- 2 Levels interrupt priority

## Flexible I/O

- 29 GPIO pins
- All GPIO pins can be programmable as input or output individually
- All GPIO pins are internal pull-up /pull-down selectable individually
- CMOS/TTL level Schmitt triggered input

## Power Supply

- VDDLDO is 3.2V to 5.5V
- VDDIO is 3.0V to 3.6V
- VDDCORE is 1.6V to 2.0V

## Digital Peripheral Feature

- Two multi-function 8-bit timers, support Capture and PWM mode
- Two multi-function 16-bit timers, support Capture and PWM mode
- Watchdog
- One full-duplex UART
- MPEG-1, MPEG-2, MPEG-2.5 Audio Layer 1,2,3 decoder. Bit rate 8-448Kbps, 8-48KHz, CBR/VBR/ABR
- MP3 Encode
- IIS controller
- Two SPI, support DMA
- One EMI controller, use for external memory access
- SD Card Host controller

## Program and Debug Support

- In-System Programming (ISP) support
- In-System Debugging (ISD) support

## Packages

- LQFP48
- SOP28
- DIE form

## Temperature

- Operating temperature: -40°C to +85°C
- Storage temperature: -65°C to +150°C

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# **1 PRODUCT OVERVIEW**

## **1.1 Description**

AX2210 is an 8051 Compatible high performance mixed signal 8-bit microcontroller. It integrates advanced digital and analog peripherals to suit for a variety of applications. For digital peripherals, it supports a variety of digital interfaces including UART, EMI SPI and four timers, integrates an USB 2.0 full speed Host/Device controller and a MP3 Decoder/Encoder Engine, etc. For analog peripherals, it integrates RCOSC, DAC, ADC, PLL, LVD, XOSC and LDO. The microcontroller employs an advanced architecture and compatible with standard 8051 with throughput up to 48 MIPS. All instructions are single-cycled except program branching.

The MP3 Decoder Engine can decode MPEG-1, MPEG-2, MPEG-2.5 Audio Layer 1, 2, 3 decoder under 24 MHz.

AX2210 has 48K-byte (24K x 16) IROM, 32K-byte OTP (32K x 8), 16K-byte (8K x 16) IRAM, 8K-byte (4Kx16) RAM2 and 3936 x 24-bit Decode RAM (DECRAM) for MP3 decoder. IROM, IRAM and part of DECRAM can be used as program memory. Part of IROM, DECRAM and IRAM can be used as data memory.

The USB 2.0 Host/Device controller FIFO is shared with IRAM. It supports USB2.0 full speed.

Flexible I/O with different programmable capabilities like pull-up, pull-down or wake-up allows AX2210 to suit different I/O requirements in the market.

In order to support portable applications and reduce power consumption, three low-power saving modes are added: IDLE mode, HOLD mode and SLEEP mode.

## 1.2 System Architecture

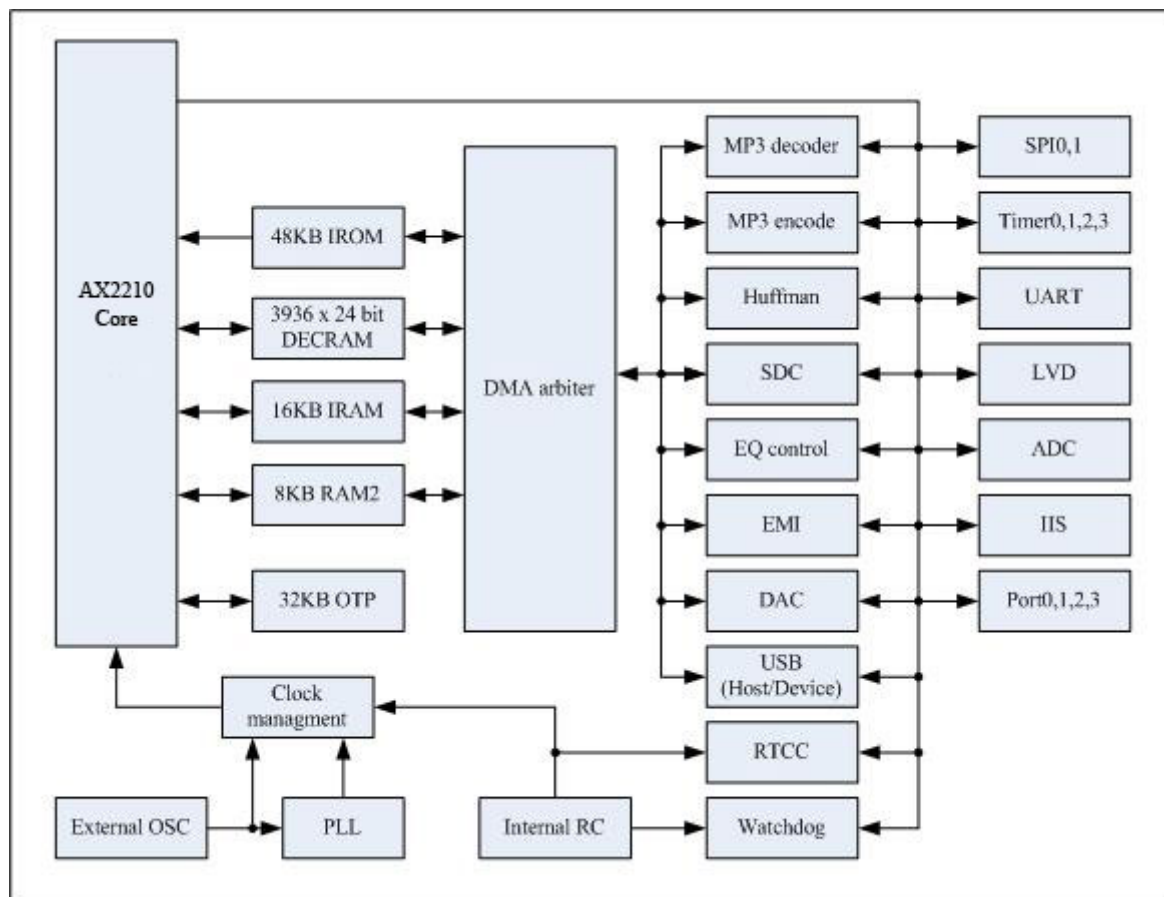


Figure 1-1 AX2210 architecture

## 2 PIN Definitions

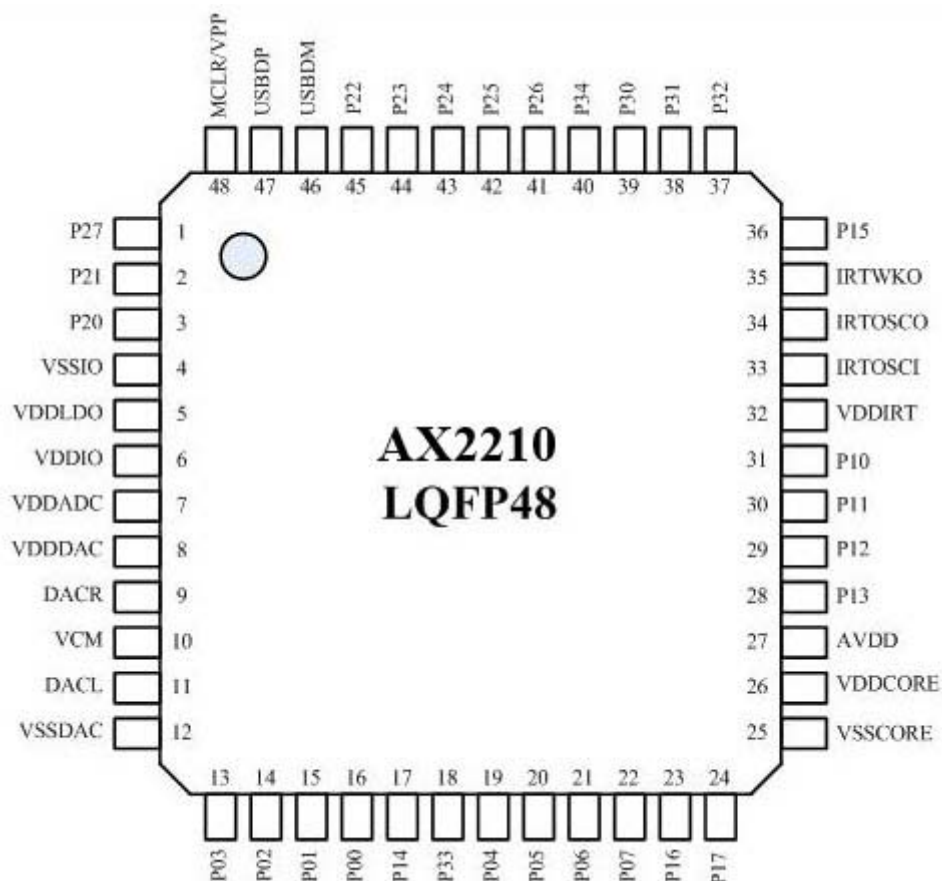
### 2.1 Part Numbering

AX2210

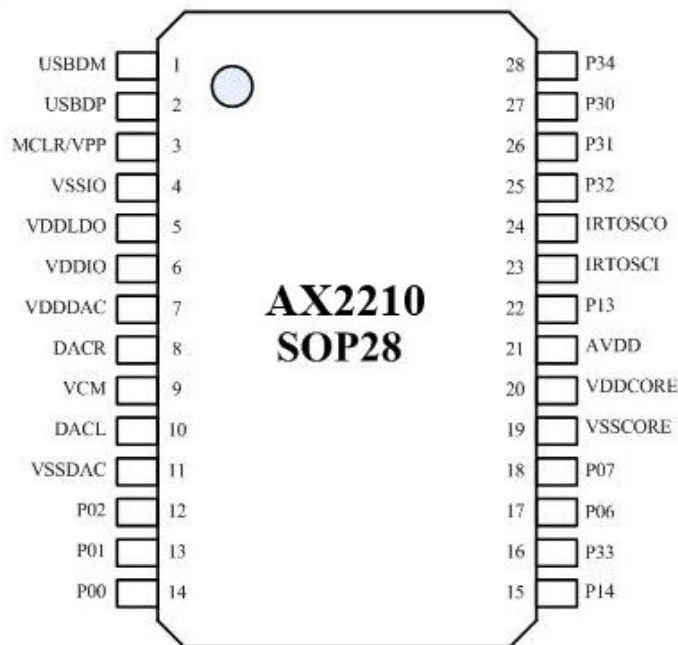
### 2.2 Packages

LQFP48 and SOP28

### 2.3 Pin Assignment



**Figure 2-1 Pin assignment for LQFP48**



**Figure 2-2 Pin assignment for SOP28**

## 2.4 Pin Descriptions

Figure 2-1 shows the pin assignments of LQFP48 package. Table 2-1 shows the pin descriptions of LQFP48 package.

Figure 2-2 shows the pin assignments of SOP28 package. Table 2-2 shows the pin descriptions of SOP28 package.

**Table 2-1 LQFP48 pin description**

Pin No.	Name	Type	Function	Other Function
1	P27	I/O	GPIO	PPD7: Parallel Port Data 7 SPI0DOUT0: SPI0 Data output 0
2	P21	I/O	GPIO	PPD1: Parallel Port Data 1 IISDI0: IIS Data input 0
3	P20	I/O	GPIO	PPD0: Parallel Port Data 0 IISBCLK: IIS BCLK
4	VSSIO	P	IO Ground	
5	VDDLDO	P	LDO Power	
6	VDDIO	P	IO Power	

7	VDDADC	P	ADC Power	
8	VDDDAC	P	DAC Power	
9	DACR	O	DAC Right Channel	
10	VCM	O	DAC Bandgap voltage reference	
11	DACL	O	DAC Left Channel	
12	VSSDAC	P	DAC Ground	
13	P03	I/O	MICIN0 / AUXL0	
14	P02	I/O	MININ1 / AUXR0	
15	P01	I/O	GPIO AUXR1 ADC5	SDDATA2: SD Data 2 UARTTX1: UART TX1
16	P00	I/O	GPIO AUXL1 ADC4	SDDATA1: SD Data 1 UARTRX1: UART RX1 SPI0DI2: SPI0 Data input 0
17	P14	I/O	GPIO SDDATA3: SD Data 3	T3PWM: Timer3 PWM Output T3CAP: Timer3 Capture Pin SPI0DOUT2: SPI0 Data output 2
18	P33	I/O	GPIO ADC0/LVD detect	32K/system clock/XOSC12M output
19	P04	I/O	GPIO ADC2 Port Interrupt/Wakeup	T1PWM: Timer1 PWM Output SPI1DOUT: SPI1 Data Output SPI0DOUT1: SPI0 Data Output 1
20	P05	I/O	GPIO ADC3 Port Interrupt/Wakeup	T0CAP: Timer0 Capture Pin SPI1CLK: SPI1 Clock SPI0CLK1: SPI0 Clock1
21	P06	I/O	GPIO ADC1	SPI1DI: SPI1 Data Input SPI0DI1: SPI0 Data Input
22	P07	I/O	GPIO IR Input Port Interrupt/Wakeup	T1CAP: Timer1 Capture Pin
23	P16	I/O	GPIO AM input REFCLK: IIS Reference clock	UARTTX0: UART Data Out 0 T2PWM: Timer2 PWM Output T2CAP: Timer2 Capture Pin
24	P17	I/O	GPIO FM Input IISWS: IIS WS	T2CKIN: Timer2 Clock In

**PRODUCT SUMMARY**

25	VSSCORE	P	Digital Ground	
26	VDDCORE	P	Digital Power	
27	AVDD	P	IO Power	
28	P13	I/O	GPIO	DP: Debug Pin
29	P12	I/O	GPIO	
30	P11	I/O	GPIO	
31	P10	I/O	GPIO	PPWR: Parallel Port Write
32	VDDIRT	P	IRT 1.5V IO Power	
33	IRTOSCI	I	IRTC 32K Oscillator Input	
34	IRTOSCO	O	IRTC 32K Oscillator Output	
35	IRTWKO	O	IRTC Wake up Output	
36	P15	I/O	GPIO	T3CKIN: Timer3 Clock In
37	P32	I/O	GPIO	SDDATA0: SD Data 0
38	P31	I/O	GPIO	SDCMD: SD Command
39	P30	I/O	GPIO	SDCLK: SD Clock
40	P34	I/O	GPIO XOSC12I Port Interrupt/Wakeup	T0PWM: Timer0 PWM Output UARTRX0:UART Data In 0 SPI0CLK2: SPI0 Clock2
41	P26	I/O	GPIO XOSC12O	PPD6: Parallel Port Data 6 SPI0CLK0: SPI0 CLK 0
42	P25	I/O	GPIO	PPD5: Parallel Port Data 5 SPI0DIN0: SPI0 Data input 0
43	P24	I/O	GPIO	PPD4: Parallel Port Data 4 IISDO1:IIS Data output1
44	P23	I/O	GPIO	PPD3: Parallel Port Data 3 IISDI1:IIS Data input 1
45	P22	I/O	GPIO	PPD2: Parallel Port Data 2 IISDO0:IIS Data output0
46	USBDM	I/O	USB Negative Input/output	
47	USBDP	I/O	USB Positive Input/output	
48	MCLR/VPP	I	Master Clear, Low Active	OTP Programming Power

**Table 2-2 SOP28 pin description**



Pin No.	Name	Type	Function	Other Function
1	USBDM	I/O	USB Negative Input/output	USBDM
2	USBDP	I/O	USB Positive Input/output	USBDP
3	MCLR/VPP	I	Master Clear, Low Active	DP: Debug Pin
4	VSSIO	P	IO Ground	
5	VDDLDO	P	LDO Power	
6	VDDIO	P	IO Power	
7	VDDDAC	P	DAC & ADC Power	
8	DACR	O	DAC Right Channel	
9	VCM	O	DAC Bandgap voltage reference	SDCLK: SD Clock
10	DACL	O	DAC Left Channel	SDCMD: SD Command
11	VSSDAC	P	DAC Ground	SDDATA0: SD Data 0
12	P02	I/O	MININ1 / AUXR0	
13	P01	I/O	GPIO AUXR1 ADC5	SDDATA2: SD Data 2 UARTTX1: UART TX1
14	P00	I/O	GPIO AUXL1 ADC4	SDDATA1: SD Data 1 UARTRX1: UART RX1 SPI0DI2:SPI0 Data input 0
15	P14	I/O	GPIO SDDATA3: SD Data 3	T3PWM: Timer3 PWM Output T3CAP: Timer3 Capture Pin SPI0DOUT2: SPI0 Data output 2
16	P33	I/O	GPIO ADC0/LVD detect	32K/system clock/XOSC12M output
17	P06	I/O	GPIO ADC1	SPI1DI: SPI1 Data Input SPI0DI1:SPI0 Data Input
18	P07	I/O	GPIO IR Input Port Interrupt/Wakeup	T1CAP: Timer1 Capture Pin
19	VSSCORE	P	Digital Ground	
20	VDDCORE	P	Digital Power	
21	AVDD	P	IO Power	
22	P13	I/O	GPIO	DP: Debug Pin
23	IRTOSCI	I	IRTC 32K Oscillator Input	
24	IRTOSCO	O	IRTC 32K Oscillator Output	

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25	P32	I/O	GPIO	SDDATA0: SD Data 0
26	P31	I/O	GPIO	SDCMD: SD Command
27	P30	I/O	GPIO	SDCLK: SD Clock
28	P34	I/O	GPIO XOSC12I Port Interrupt/Wakeup	T0PWM: Timer0 PWM Output UARTRX0:UART Data In 0 SPI0CLK2: SPI0 Clock2

## Appendix I Revision History

Date	Version	Comment	Revised by
2011-04-20	1.0.0	First version	Erica Cheong
2012-01-19	1.0.1	Update logo	Karen Keung

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