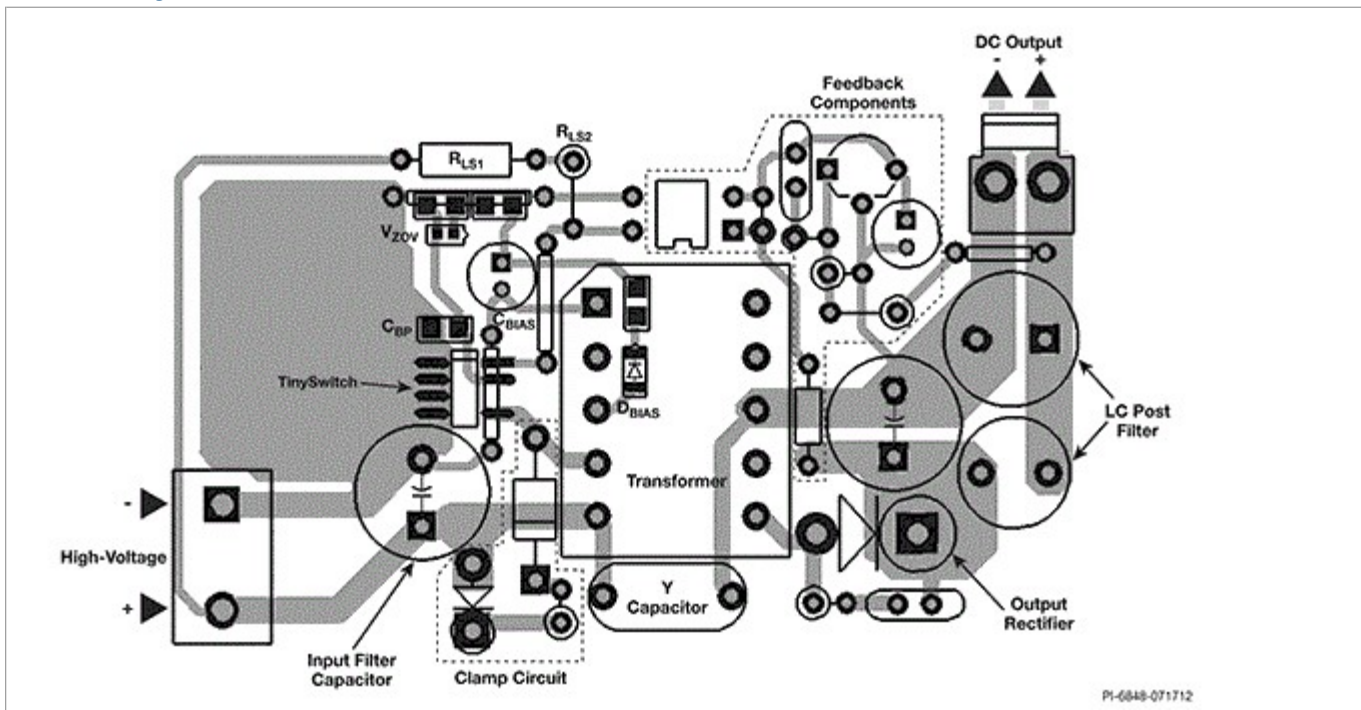


Board Layout Recommendations



Click on the "Show me" icon to highlight relevant areas on the sample layout.

	Description	Show Me
1	Maximize source area for good heat-sinking	
2	Keep drain trace short	
3	The BYPASS pin capacitor should be located as close as possible to the BYPASS and SOURCE pins	
4	Keep noisy traces away from EN/UV pin	
5	Route bias winding currents back to the bulk cap	
6	Keep clamp loop short	
7	Connect Y capacitor to the B+ rail on the primary side for better surge immunity. Keep Y capacitor traces short	
8	The area of the loop connecting the secondary winding, the output rectifier and the output filter capacitor should be minimized	