Bhagya Chandrika Kota

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Objective

Enthusiastic web developer eager to explore software development, machine learning, VLSI, and contribute to innovative projects using coding, Git, and Agile methods

Education

KLUNIVERSITY, Guntur **2020 – 2024**
Bachelors of Technology in the branch of Electronics and Communication Engineering *CGPA: 9.49/10*
Coursework: VLSI, Machine learning

Sri Saradha junior college , Vijayawada **2018 – 2020**
Board of Intermediate in MPC *CGPA: 9.72/10*

Technical Skills

Programming Languages: C/C++, Core Java, SQL, HTML/CSS , Javascript , Verilog
Frameworks: Flask, Django, React.js
Software & Tools: Xilinx Vivado Tool, Cadence EDA , Git
Basic Knowledge in Web development and Machine Learning

Experience

Microsoft **May 2023 – July 2023**
SWE Intern *Hyderabad*
- Implemented Text Rotation feature for Excel on iPad using C++.
- Utilized Xcode and iOS Development tools to enhance user experience.
- Collaborated with the team and managed version control using Git

Microsoft **May 2022 – June 2022**
Microsoft Engage Mentee *Remote*
- Created a content-based recommendation system using Jupyter Notebook.
- Implemented a web application for the recommendation system using the Flask framework

Swecha AP **May 2021 – June 2021**
Web Development Intern *Vijayawada*
- Developed an interactive educational website on triangles using HTML, CSS, and JavaScript.
- Created informative content, practice sums, quizzes, and simulations.

Projects

SMART CAR PARKING UISNG IOT | *Arduino IOT Cloud Git*
- Developed smart car parking system with energy-saving measures and real-time vacancy indication using Ultrasonic and IR sensors, servo motors, and LED displays

PORTFOLIO WEBSITE | *VSCODE Git*
- Created a responsive portfolio website using HTML, CSS, and JavaScript to present my projects, skills, and portfolio in an appealing and user-friendly format.

CNN-BASED IC LAYOUT RECOGNITION | *Cadence EDA, Jupyter Notebook. Git*
- Implemented an accurate pattern recognition model for identifying gate layouts in larger circuits, advancing electronic device design and technology

RFID-based Access Control System | *Adafruit Iot ,Arduino IDE*
- Developed secure room security solution using mobile phones and IoT with RFID. Implemented Smart Lock System with Node MCU for authorized access and online monitoring

Academic Research

Dual Band Hexagonal Patch Antenna For Wireless and ISM Application **July 2023**
- Developed high-performance Dual Band Hexagonal Patch Antenna for wireless and ISM applications, achieving low return loss, VSWR, and high gain (3 dBi) across frequencies.

Certifications

Certificate 1: CLA: Programming Essentials in C by Cisco Network
Certificate 2 : VLSI - Design Verification by Tessolve
Certificate 3 : Microsoft Certified: Azure Fundamentals

Achievements

NeoCodeathon July 2022 Edition: Secured 88th place
Academic Excellence Award: Secured 9.45 CGPA in B.Tech 2nd year