ECE 214 - Lab #5
OpAmp Circuits with Positive Feedback

25 February 2019

Introduction: Two LM741 Operational Amplifiers (OpAmps) are used to produce an oscillator. A system level block diagram is shown in Figure 1. The oscillator is formed by connecting the output of a Schmitt trigger Figure 2 to the input of an inverting integrator, and connecting the output of the inverting integrator back to the input of the Schmitt trigger. The entire oscillator circuit is shown in Figure 3. The only input to the circuit is a DC voltage $+\text{V}_{\text{SUP}}$. The circuit produces both a square wave and a triangular wave output signal. You are to design an oscillator which meets the following specification:

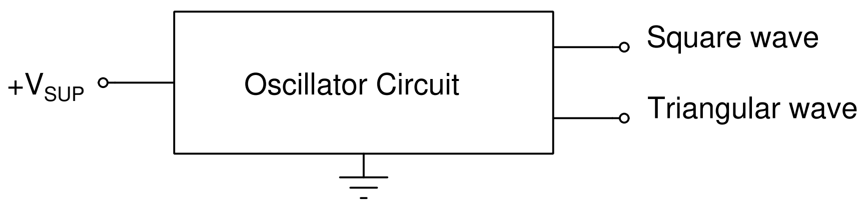


Figure 1: Block diagram of the oscillator circuit.

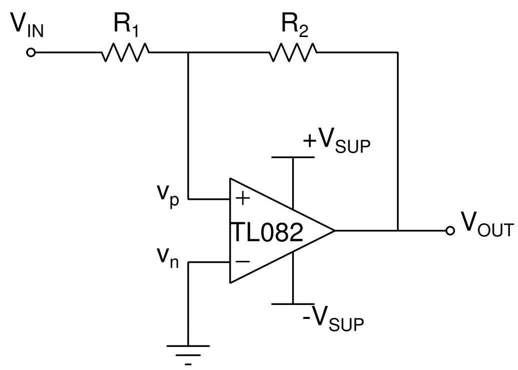
Circuit Specification:

1. Input: $+\text{V}_{\text{SUP}} = 10$ V DC
2. Outputs:
   (a) Square wave and triangular wave
   (b) Frequency $= 4.0 \pm 0.3$ kHz
   (c) Duty cycle $= 50 \pm 3$%

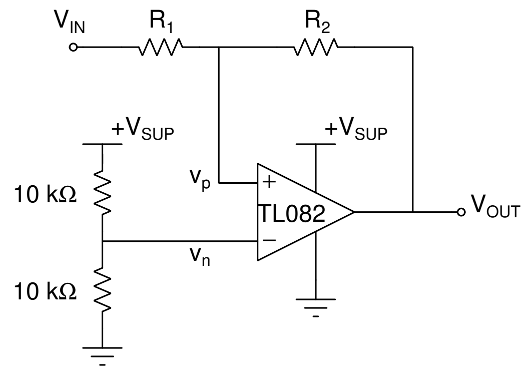
Pre–Lab:

1. There are many ways to implement a Schmitt trigger. One method uses an OpAmp with positive feedback as shown in the circuits in Figure 2. In these circuits, the output (V_{OUT}) is connected through resistor R_2 to the non-inverting input (v_p) of the OpAmp. When an Op Amp is configured with positive feedback: $\text{v}_\text{p} \neq \text{v}_\text{n}$. Rather V_{out} takes on one of only two values: $+\text{V}_{\text{SUP}}$ or $-\text{V}_{\text{SUP}}$. The output voltage $\text{V}_{\text{OUT}} = +\text{V}_{\text{SUP}}$ when $\text{v}_\text{p} > \text{v}_\text{n}$, and $\text{V}_{\text{OUT}} = -\text{V}_{\text{SUP}}$ when $\text{v}_\text{p} < \text{v}_\text{n}$. V_{OUT} transitions between $-\text{V}_{\text{SUP}}$ and $+\text{V}_{\text{SUP}}$ when $\text{v}_\text{p} = \text{v}_\text{n}$. The input voltages V_{IN} which cause the output to switch between $-\text{V}_{\text{SUP}}$ and $+\text{V}_{\text{SUP}}$, and between $+\text{V}_{\text{SUP}}$ and $-\text{V}_{\text{SUP}}$ are known as the trigger levels.

2. Assume the OpAmp in Figure 2(a) is ideal and $\text{R}_1 = 3$ kΩ and $\text{R}_2 = 10$ kΩ.
   (a) Sketch the transfer function as Vin increases from $-\text{V}_{\text{SUP}}$ V to $+\text{V}_{\text{SUP}}$ V.
   (b) Sketch the transfer function as Vin decreases from $+\text{V}_{\text{SUP}}$ V to $-\text{V}_{\text{SUP}}$ V.



(a) Schmidtt trigger circuit with dual rail voltages.



(b) Schmitt trigger with a single rail voltage.

Figure 2: Schmitt trigger circuit using an Op Amp with positive feedback.

Analyze the Schmitt trigger circuit in Figure 2(b) and determine the values of resistors R_1 and R_2 such that the Schmitt trigger levels are separated by more than 3.5 V, but less than 4.5 V. Assume the OpAmp is ideal and $+V_{SUP} = 10$ V. With the chosen values of R_1 and R_2, what are the two Schmitt trigger levels?

3. Simulate the transfer function of the Schmitt trigger circuit in Figure 2(b) with the resistor values you calculated in step 2. Use DC analysis to sweep the input voltage from 0 to 10 V. Set the power supply voltage to 10 V, and the input to a DC voltage. A MATLAB® template file and an hspc template file are available on the course web site. Analyze the behavior of this circuit using both the ideal OpAmp and the LM741 OpAmp.

   (a) Simulate the output voltage as the input voltage is increased from 0 V to $+V_{SUP}$ V.

   (b) Simulate the output voltage as the input voltage is decreased from $+V_{SUP}$ V to 0 V.

   (c) What are the trigger levels for the Ideal and LM741 OpAmps.?

   (d) How does the LM741 OpAmp compare with an ideal OpAmp?

   Make sure all axes on your graphs are properly labeled.

4. Do the simulated results for the LM741 OpAmp meet the requirement that the trigger levels are separated by more than 3.5 V, but less than 4.5 V? If not, adjust the values of R_1 and R_2 so that the trigger levels meet this requirement. Record the final values of R_1 and R_2 in your notebook.

5. Describe how the circuit shown in Figure 3 functions. Sketch the shape of the expected output signals V_{OUT_1} and V_{OUT_2} as a function of time. Assume the variable resistor is set to the mid-point resistance.

6. Assume the OpAmps are ideal. Derive the formula that relates the oscillation frequency to the values of the components R_1, R_2, R_3, and C. Make an entry in the table of contents indicating the page where the derivation and formula are located.

7. Let $C = 0.1\,\mu$F, $+V_{SUP} = 10$ V, and $R_{variable} = 1$ kΩ. Use the formula from step 6, and the values of R_1 and R_2 determined during the Lab Procedure of Lab #4, step 6(d), to determine the value of R_3 needed to produce an oscillation frequency of 4 kHz.

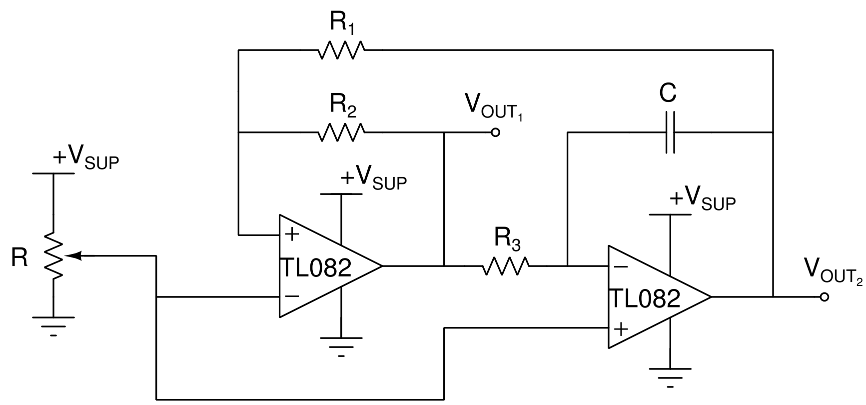


Figure 3: Oscillator circuit using a Schmitt trigger and an inverting integrator.

8. Perform a transient simulation of the circuit of Figure 3 with the LM741 OpAmp. What is the simulated value of the oscillation frequency? What is the duty cycle? You will need to generate your own MATLAB® file and `hspc` file. Use the MATLAB® and `hspc` files from previous labs as templates.

9. Since the LM741 OpAmp is not ideal, the value of R_3 you calculated will be slightly off. Adjust the value of R_3, if needed, to produce an oscillation frequency of 4 kHz.

10. Since the saturation voltages of the TL082 are not 0 V and 10 V, the duty cycle of the waveform may not be 50%. Adjust the variable resistor to produce a 50 ± 3% duty cycle. You may have to adjust the value of R_3 to keep the frequency in the range of 4 ± 0.3 kHz.

Lab Procedure:

Build the oscillator circuit in Figure 3 using the component values you determined in the Pre-Lab. Set the potentiometer (variable resistor) near the mid-point resistance.

1. Measure V_{OUT_1} and V_{OUT_2} on the scope. Are the frequency and duty cycle what you expected? Include a sketch or photograph of V_{OUT_1} and V_{OUT_2} in your notebook.

2. Use the FFT function of the scope to examine the output signal in the frequency domain. Include a sketch or photograph of the frequency response of V_{OUT_1} and V_{OUT_2} in your notebook.

3. Do you observe both even and odd harmonics in the frequency spectrum? If so, your duty cycle $\neq 50$%. The duty cycle can be adjusted by changing the DC reference voltage controlled by the variable resistor. Adjust the variable resistor while observing the FFT signal on the scope. Determine the resistance values that minimize the even harmonics. Compare the variable resistor value to the simulated value from step 10 of the Pre-Lab.

4. This oscillator circuit should generate a 4 ± 0.3 kHz output signal. Redesign the circuit if necessary to meet this specification. What was the final value of resistor R_3?

5. Measure the magnitude of the first five harmonics for both the triangular– and square–wave outputs and compare these results to the theoretical values based on the coefficients of the

Fourier series. Record the results in a table in your notebook. Reference this table in your table of contents.

Post–Lab:

Compare the performance of the simulated design with the actual design. How did the final component values of the actual design compare with the simulated design. Did the simulation provide you with a good prediction of the actual circuit performance?