

## ECE 214 - Lab #1 — Test Equipment Loading

1 February 2021

### Introduction

In this lab you are to determine the voltage ratio,  $V_{OUT}/V_{IN}$ , produced by the voltage divider with resistors  $R_a$  and  $R_b$  shown in [Figure 1](#).

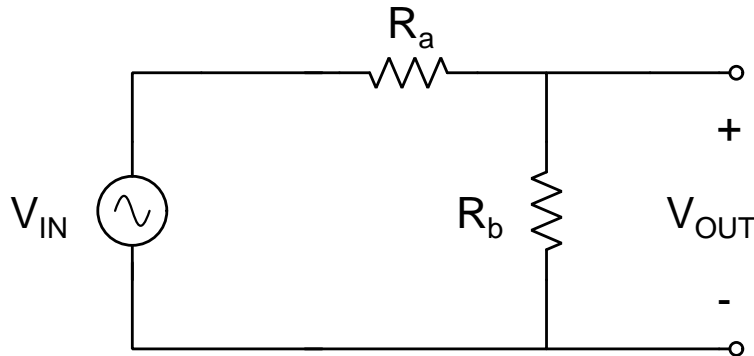


Figure 1: The circuit under test (CUT) is an ideal resistor divider circuit.

This lab illustrates that the actual values of the components, and the impedances associated with the test equipment and cables, must be considered when determining the actual value of the voltage across a circuit element. All voltage sources, including batteries and waveform generators, have a finite source impedance. All measurement equipment, including oscilloscopes and digital volt meters (DVMs), have a finite input impedance that presents a load to the circuit. Cables that connect the circuit to the test equipment introduce additional impedances in the circuit. The schematic of the circuit under test (CUT) when connected to a waveform generator (WG) and oscilloscope (scope) is shown in [Figure 2](#).

The engineering notebook should be used to record all calculations, preliminary and final designs, circuit simulation schematics and results, measured data, graphs, and analysis pertaining to the laboratory experiments. A photograph of your breadboard showing the working circuit should be included in the notebook. Basically, everything you do in this lab should be recorded in the notebook.

### Parts List

1. 10  $\Omega$  resistors (2)
2. 10 k $\Omega$  resistors (2)
3. 1 M $\Omega$  resistors (2)

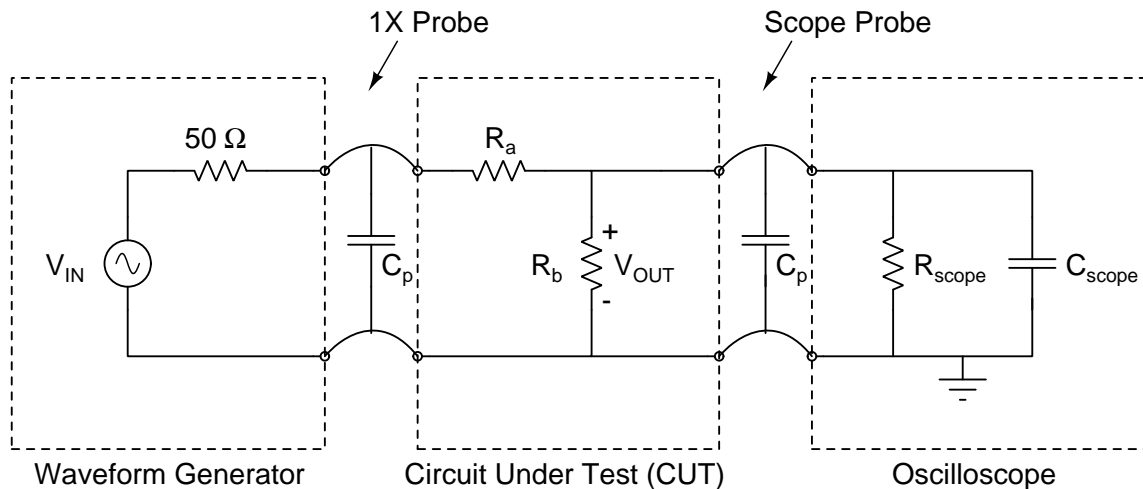


Figure 2: Measurement set-up for the resistor divider circuit.

### Lab Procedure

1. Obtain two resistors with each of the following values:  $10\ \Omega$ ,  $10\ \text{k}\Omega$ , and  $1\ \text{M}\Omega$ . Measure the resistance of each resistor using your digital volt meter (DVM).
2. For each of the three pairs of resistors, place two resistors with the same value on the bread-board to make a series circuit as shown in [Figure 1](#). If the resistors have slightly different values, make sure you keep track of which resistor is which.
3. Create a table in your notebook with the column and row headings shown in [Table 1](#).
4. Assuming that the resistors had the ideal values, calculate the ratio of  $V_{OUT}/V_{IN}$  assuming an ideal world where the voltage source is ideal (with zero source resistance), the probes are perfect connectors (with zero resistance, capacitance, and inductance), and  $V_{OUT}$  is measured with an ideal scope (with infinite input impedance). Record the ratio under the column labeled “Ideal Components.”
5. Using the measured values of the resistors, calculate the expected ratio of  $V_{OUT}/V_{IN}$  assuming an ideal world where the voltage source is ideal (with zero source resistance), the probes are perfect connectors (with zero resistance), and  $V_{OUT}$  is measured with an ideal scope (with infinite input impedance). Record the values under the column labeled “Real Components.”

$R_a$	$R_b$	$V_{OUT}/V_{IN}$		
		Ideal Components	Real Components	%Error (R Tolerance)
$10\ \Omega$	$10\ \Omega$			
$10\ \text{k}\Omega$	$10\ \text{k}\Omega$			
$1\ \text{M}\Omega$	$1\ \text{M}\Omega$			

Table 1: Measurement error due to tolerance in the resistor values.

6. Calculate the %Error caused by the tolerance in the resistor values. The %Error is defined by:

$$\%Error = 100 * \frac{|\frac{V_{OUT}}{V_{IN}}(\text{Ideal Components}) - \frac{V_{OUT}}{V_{IN}}(\text{Real Components})|}{\frac{V_{OUT}}{V_{IN}}(\text{Ideal Components})} \quad (1)$$

Record the %Error caused by the tolerance in the resistor values under the column labeled “% Error (R Tolerance).”

7. Additional parasitic capacitances and resistances.

- (a) Cables that connect an electrical test instrument to a circuit introduce additional capacitance into the circuit. The BNC to Alligator Clip cable (1X probe) that you were provided with has a capacitance of approximately 100 pF/meter; the X1-X10 Scope Probe that you were provided with has a capacitance of approximately 120 pF/meter when the black switch on the scope probe is set to the 1X position. Both of these cables are approximately one meter in length.
  - (b) Electrical test equipment also introduces additional impedances into a circuit. The input impedance of the Digilent Analog Discovery 2 oscilloscope (scope) consists of a 10 M $\Omega$  resistor in parallel with a 24 pF capacitance. The output impedance of the Digilent Analog Discovery 2 waveform generator (WG) when used with a properly configured BNC Adapter Board is 50  $\Omega$ .
  - (c) Create a table in your notebook with the column and row headings shown in [Table 2](#).
  - (d) Using the measured values of the resistors, the capacitance of the 1X and scope probes, the output resistance of the WG, and the input impedance of the scope, calculate the expected ratio of  $V_{OUT}/V_{IN}$  for the three values of resistors when the input signal is a sine wave at a frequency of 5 kHz. Show all calculations in your notebook. Record the calculated ratio of  $V_{OUT}/V_{IN}$  in the table under the column labeled “Calculated.”
8. Connect the BNC Adapter to the Analog Discovery. Ensure that the jumpers on the BNC Adapter board are set to 50  $\Omega$  and DC coupling. Configure the WG to generate a 5 kHz sinusoidal signal with an output voltage of 1 V peak.
9. Connect the output of the WG directly to the input of the scope and measure the WG output voltage and frequency. Verify that the WG is producing a 1 V peak signal with a frequency of 5 kHz. Make sure the black switch on the scope probe is set to the 1X position.
10. Use the scope to measure the peak value of the output voltage ( $V_{OUT}$ ) across resistor  $R_b$  for the three pairs of resistors. Record the values of  $V_{OUT}$  under the column labeled “Measured.”
11. Calculate the %Calculated-Measured (%CM) Error for the three pairs of resistors. The %CM Error is defined by:

$$\%CM \text{ Error} = 100 * \frac{|\frac{V_{OUT}}{V_{IN}}(\text{Calculated}) - \frac{V_{OUT}}{V_{IN}}(\text{Measured})|}{\frac{V_{OUT}}{V_{IN}}(\text{Calculated})} \quad (2)$$

Record the values under the column labeled “%CM Error.” Your %CM Error should be on the order of 1% or less. If the error is greater than 2%, there is either a mistake in your calculation or an inaccuracy in your measurement. Try and obtain values such that the %CM Error is

$R_a$	$R_b$	$V_{OUT} / V_{IN}$			
		Calculated	Measured	% C-M Error	% Total Error
$10\ \Omega$	$10\ \Omega$				
$10\ \text{k}\Omega$	$10\ \text{k}\Omega$				
$1\ \text{M}\Omega$	$1\ \text{M}\Omega$				

Table 2: Measurement error introduced by the measurement equipment and cables.

less than 1.5% for all three resistor pairs. Explain in your notebook any discrepancy between the measured and calculated values. Indicate possible sources of the error.

12. Calculate the %Total Error for the three resistor pairs. The %Total Error is defined by:

$$\% \text{Total Error} = 100 * \frac{|\frac{V_{OUT}}{V_{IN}}(\text{Ideal}) - \frac{V_{OUT}}{V_{IN}}(\text{Measured})|}{\frac{V_{OUT}}{V_{IN}}(\text{Ideal})} \quad (3)$$

Record the values under the column labeled “%Total Error.”

13. For which pair of resistor values does the measurement equipment have a minimal effect on the circuit under test? Why? Explain in your notebook any discrepancy between the measured and calculated values. Indicate possible source of the error.
14. Change the WG output to a square wave with a 1 V peak amplitude. Keep the frequency at 5 kHz.
15. Using 10 k $\Omega$  resistors, record the shape of the rising edge of the signal as observed on the oscilloscope. Make sure to expand the time scale to see the details of the rising edge of the signal. It is not just a vertical line. The signal should appear similar to the response of a first order RC system studied in ECE 210. Measure both the rise time and the time constant of the signal at  $V_{OUT}$ .
16. What is the approximate mathematical equation which can be used to describe this waveform?

## Post-Lab

- Use NGSpice to simulate the expected output voltage  $V_{OUT}$  as a function of the resistance  $R$  for the circuit of **Figure 2**. In the simulation, include the capacitance of each probe, the output resistance of the WG, and the input impedance of the scope. A Matlab<sup>®</sup> template file is available on the course website at: ([https://davidkotecki.com/ECE214/docs/Matlab/ECE214\\_2021\\_Lab1\\_B.m](https://davidkotecki.com/ECE214/docs/Matlab/ECE214_2021_Lab1_B.m)).
- Provide a summary of the major results and conclusions from this lab. Compare the measured results with the calculated and simulated results. What is the most important concept you learned in this laboratory? Make an entry of the location of this information in the table of contents. Such a summary should be provided in every lab where the measured results are compared to the calculated and simulated results, and the most important concepts learned in the lab are explained.