PTask: Operating System Abstractions to Manage GPUs as Compute Devices

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Motivation

- There are lots of GPUs
 - 3 of top 5 supercomputers use GPUs
 - In all new PCs, smart phones, tablets
 - Great for gaming and HPC/batch
 - Unusable in other application domains
- GPU programming challenges
 - GPU+main memory disjoint
 - Treated as I/O device by OS

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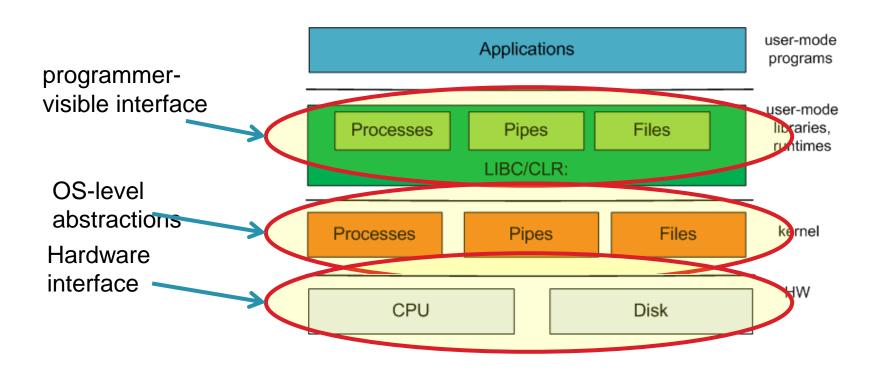
Motivation

- There are lots of GPUs
 - 3 of top 5 supercomputers use GPUs
 - In all new PCs, smart These two things are related:
 - Great for gaming and We need OS abstractions
 - Unusable in other application domains
- GPU programing/challenges
 - GPU+main memory disjoint
 - Treated as I/O device by OS

Outline

- The case for OS support
- PTask: Dataflow for GPUs
- Evaluation
- Related Work
- Conclusion

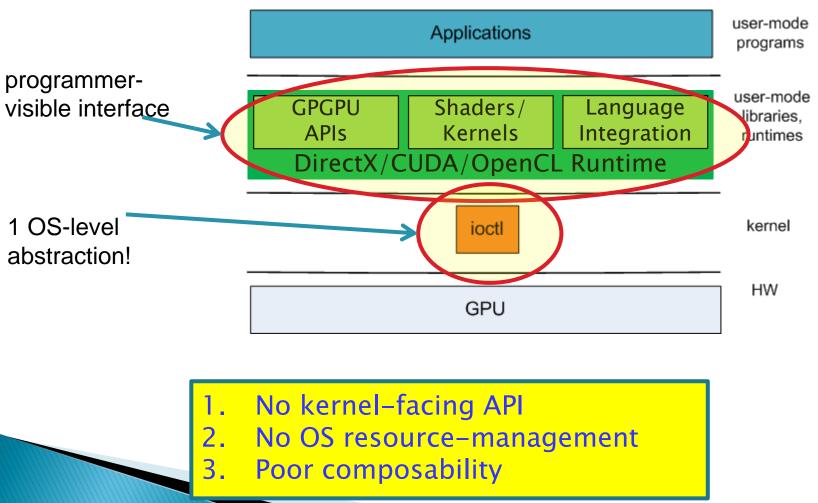
Traditional OS-Level abstractions



1:1 correspondence between OS-level and user-level abstractions

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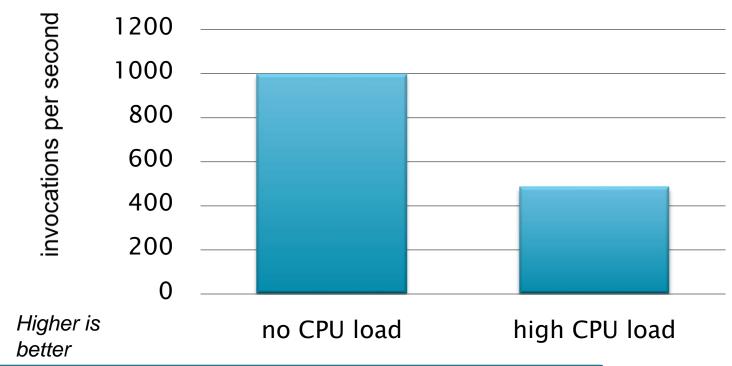
GPU Abstractions



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CPU-bound processes hurt GPUs



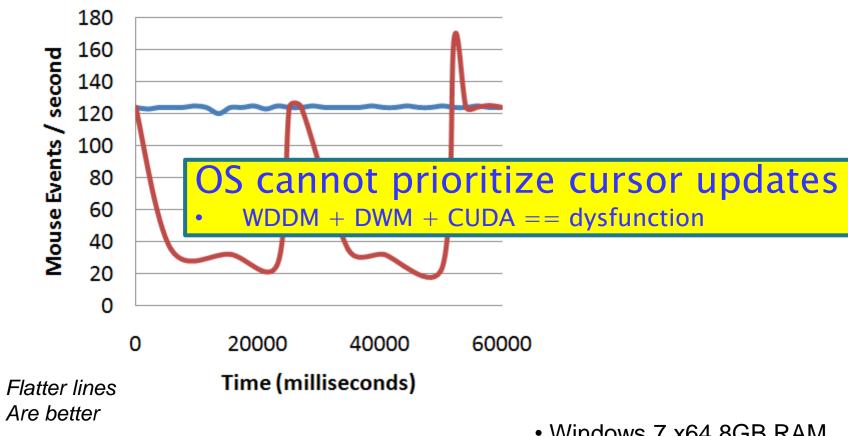


CPU scheduler and GPU scheduler not integrated!

age-convolution in CUDA ndows 7 x64 8GB RAM al Core 2 Quad 2.66GHz dia GeForce GT230

GPU-bound processes hurt CPUs

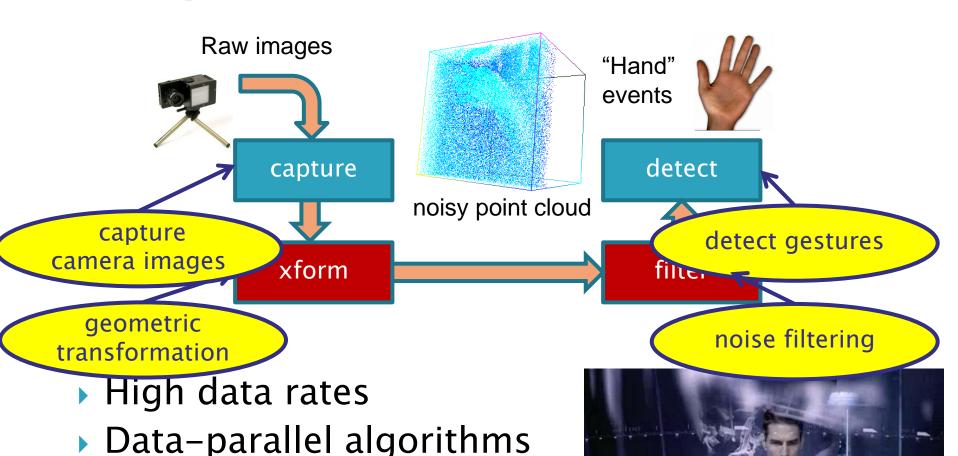
Mouse Move Frequency



- Windows 7 x64 8GB RAM
- Intel Core 2 Quad 2.66GHz
- nVidia GeForce GT230

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Composition: Gestural Interface



NOT Kinect: this is a harder problem!

... good fit for GPU

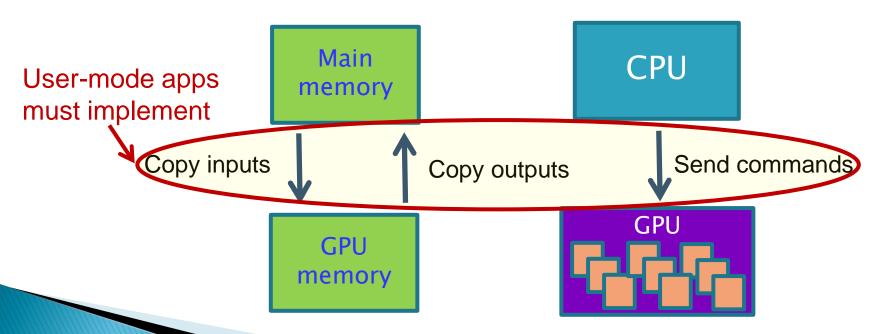
What We'd Like To Do

- Modular design
 - flexibility, reuse
- Utilize heterogeneous hardware
 - ▶ Data-parallel components → GPU
 - ▶ Sequential components → CPU
- Using OS provided tools
 - processes, pipes

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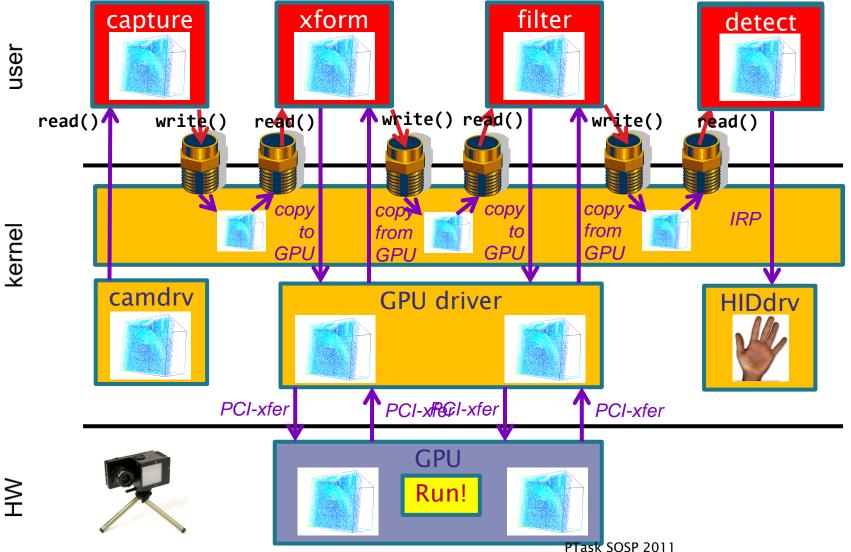
GPU Execution model

- GPUs cannot run OS: different ISA
- Disjoint memory space, no coherence
- Host CPU must manage GPU execution
 - Program inputs explicitly transferred/bound at runtime
 - Device buffers pre-allocated



Data migration

#> capture | xform | filter | detect &



GPUs need better OS abstractions

- GPU Analogues for:
 - Process API
 - IPC API
 - Scheduler hints
- Abstractions that enable:
 - Fairness/isolation
 - OS use of GPU
 - Composition/data movement optimization

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PTask OS abstractions: dataflow!

- ptask (parallel task)
 - Has *priority* for fairness
 - Analogous to a process for GPU execution
 - List of input/output resources (e.g. stdin, stdout...)

ports

- Can be mapped to ptask input/outputs
- A data source or sink

channels

- Similar to pipes, connect arbitrary ports
- Specialize to eliminate dou OS objects→OS RM possible
- Specialize to elilililiate dou

graph

- DAG: connected ptasks, ports, channels
- datablocks
 - Memory-space transparent buffers

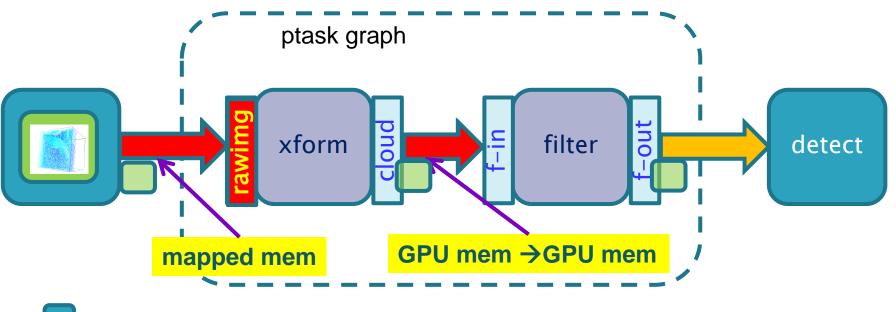
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• data: specify where, not how

PTask Graph: Gestural Interface

#> capture | xform | filter | detect &



- process (CPU)
- ptask (GPU)
- port
- **c**hannel
- ptask graph
- datablock

Data arrival triggers computation

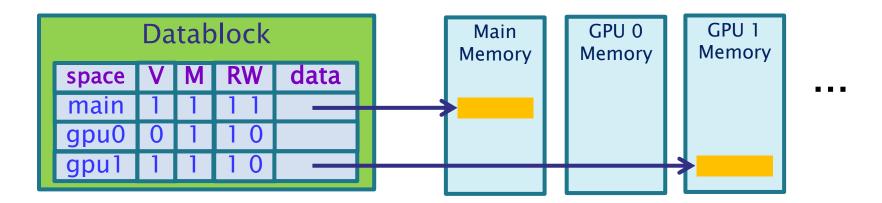
Optimized data movement

PTask Scheduling

- Graphs scheduled dynamically
 - ptasks queue for dispatch when inputs ready
- Queue: dynamic priority order
 - ptask priority user-settable
 - ptask prio normalized to OS prio
- Transparently support multiple GPUs
 - Schedule ptasks for input locality

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Location Transparency: Datablocks

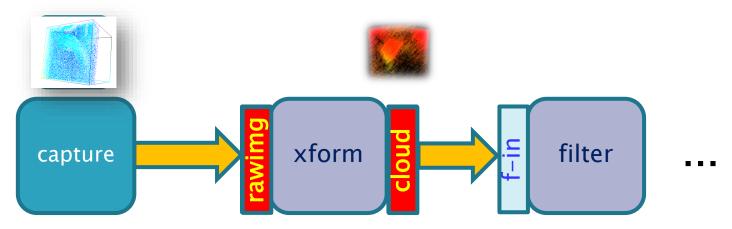


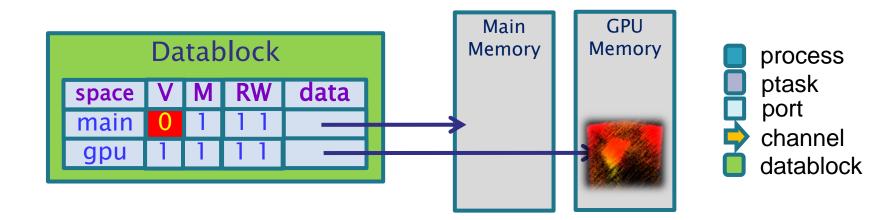
- Logical buffer
 - backed by multiple physical buffers
 - buffers created/updated lazily
 - mem-mapping used to share across process boundaries
- Track buffer validity per memory space
 - writes invalidate other views
- Flags for access control/data placement

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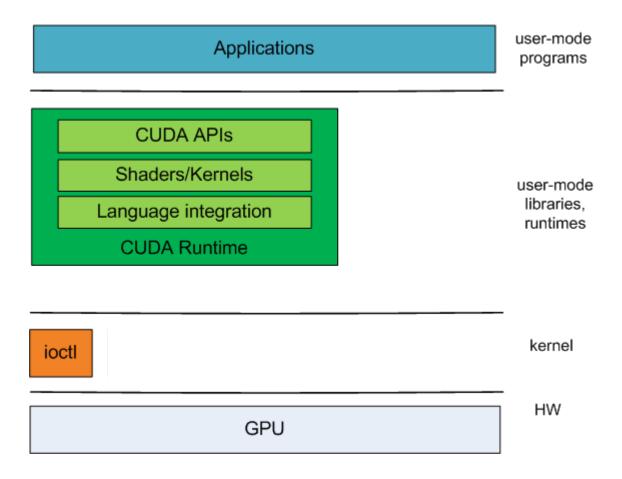
Datablock Action Zone

#> capture | xform | filter ...





Revised technology stack



- 1-1 correspondence between programmer and OS abstractions
- GPU APIs can be built on top of new OS abstractions

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Implementation

- Windows 7
 - Full PTask API implementation
 - Stacked UMDF/KMDF driver
 - Kernel component: mem-mapping, signaling
 - User component: wraps DirectX, CUDA, OpenCL
 - syscalls → DeviceIoControl() calls
- Linux 2.6.33.2
 - Changed OS scheduling to manage GPU
 - GPU accounting added to task_struct

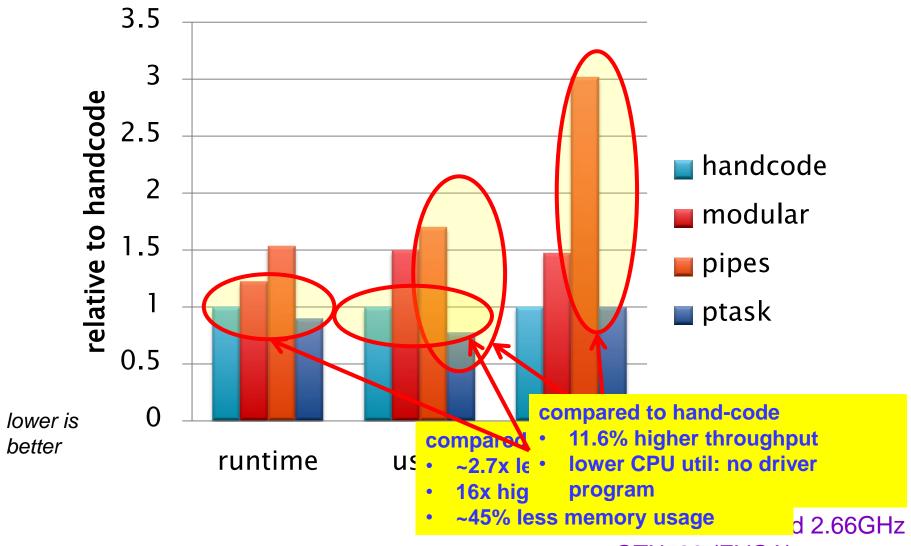
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Gestural Interface evaluation

- Windows 7, Core2-Quad, GTX580 (EVGA)
- Implementations
 - pipes: capture | xform | filter | detect
 - modular: capture+xform+filter+detect, 1process
 - handcode: data movement optimized, 1process
 - ptask: ptask graph
- Configurations
 - real-time: driven by cameras
 - unconstrained: driven by in-memory playback

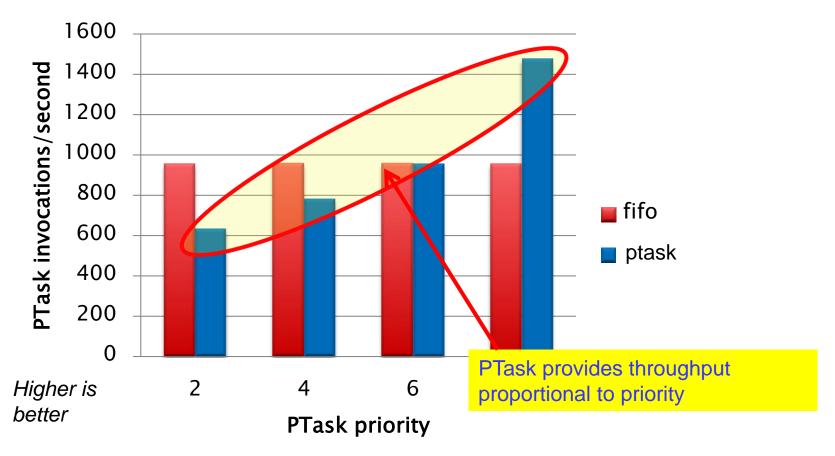
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Gestural Interface Performance



GTX580 (EVGA)

Performance Isolation



- FIFO queue invocations in arrival order
- ptask aged priority queue w OS priority
- graphs: 6x6 matrix multiply
- priority same for every PTask node

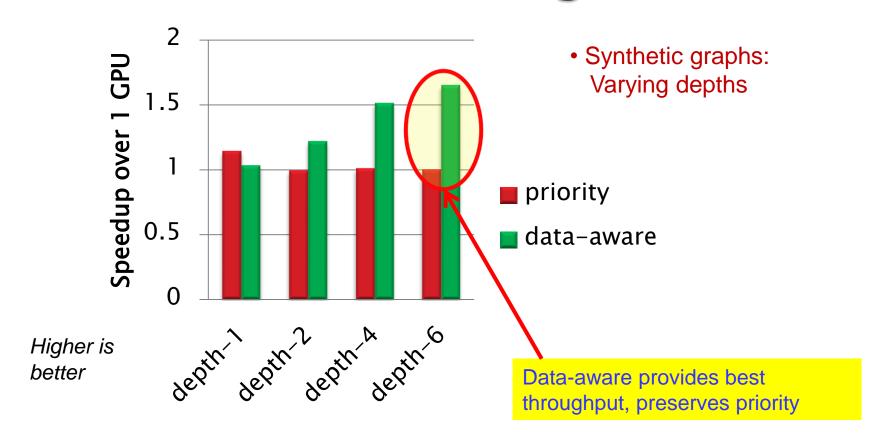
- Windows 7 x64 8GB RAM
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• GTX580 (EVGA)

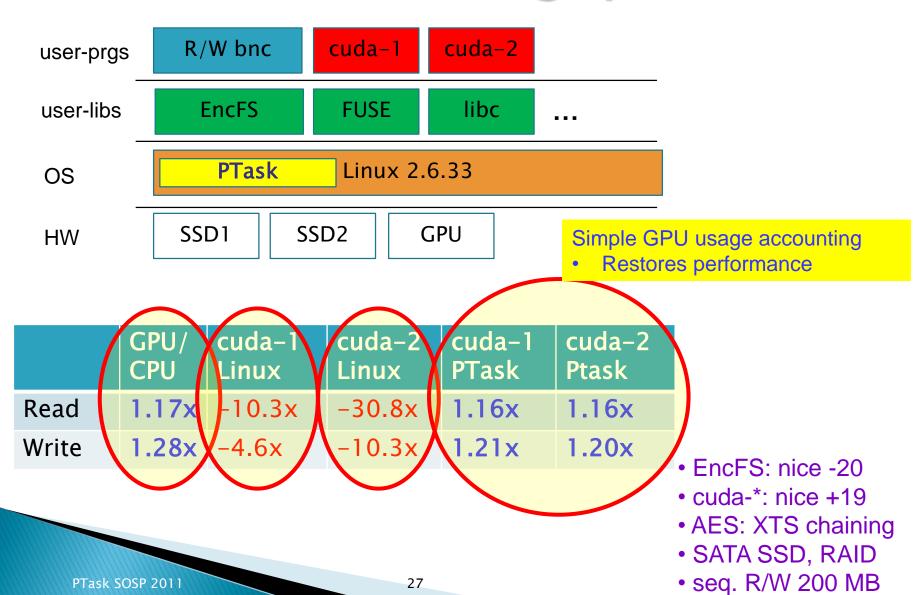
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Multi-GPU Scheduling



- Data-aware == priority + locality
- Graph depth > 1 req. for any benefit
- Windows 7 x64 8GB RAM
- Intel Core 2 Quad 2.66GHz
- 2 x GTX580 (EVGA)

Linux+EncFS Throughput



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Related Work

- OS support for heterogeneous platforms:
 - Helios [Nightingale 09], BarrelFish [Baumann 09], Offcodes [Weinsberg 08]
- GPU Scheduling
 - TimeGraph [κato 11], Pegasus [Gupta 11]
- Graph-based programming models
 - Synthesis [Masselin 89]
 - Monsoon/Id [Arvind]
 - Dryad [Isard 07]
 - StreamIt [Thies 02]
 - DirectShow
 - TCP Offload [Currid 04]
- Tasking
 - Tessellation, Apple GCD, ...

Conclusions

- OS abstractions for GPUs are critical
 - Enable fairness & priority
 - OS can use the GPU
- Dataflow: a good fit abstraction
 - system manages data movement
 - performance benefits significant

Thank you. Questions?

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