

# ELECTRICAL

## RESONANCE

- Resonance is the freq. response of a ckt / ulw when the ckt operates at its natural freq called resonance freq.
- Under resonance total supply vltg & supply current are in phase . So ,  $\phi = 0^\circ$   
 $\& \text{PF} = \cos \phi = 1 \text{ (UPF)}$

- Under resonance the net impedance of the ckt becomes purely resistive & max power will be transferred to the ckt from source
- 

- Resonance can occur in any electrical circuit provided we have 2 similar but opposite natured energy storage components, i.e.  $L \& C$ .
- To undergo a good observable resonance

→ To undergo a good observable resonance for practical applications we need a good quality in these energy storage components which is measured as Quality factor or figure of merit given by:

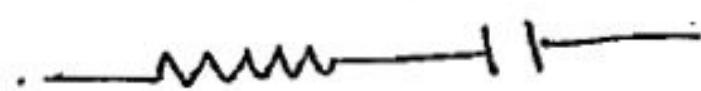
$$Q\text{-factor} = 2\pi * \frac{\left[ \begin{array}{l} \text{Max. energy stored per cycle} \\ \text{of supply} \end{array} \right]}{\left[ \begin{array}{l} \text{Energy dissipated per cycle} \\ \text{of supply} \end{array} \right]}$$

In practical applications

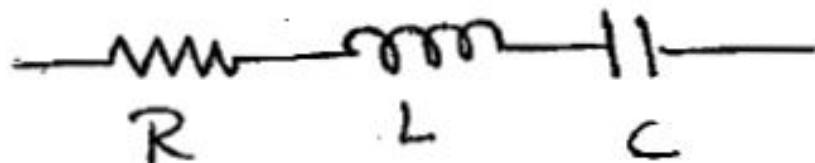
$$Q \geq 10$$

→ Resonance phenomenon is useful in designing of passive filters, & antennas, receivers, SONARS, etc

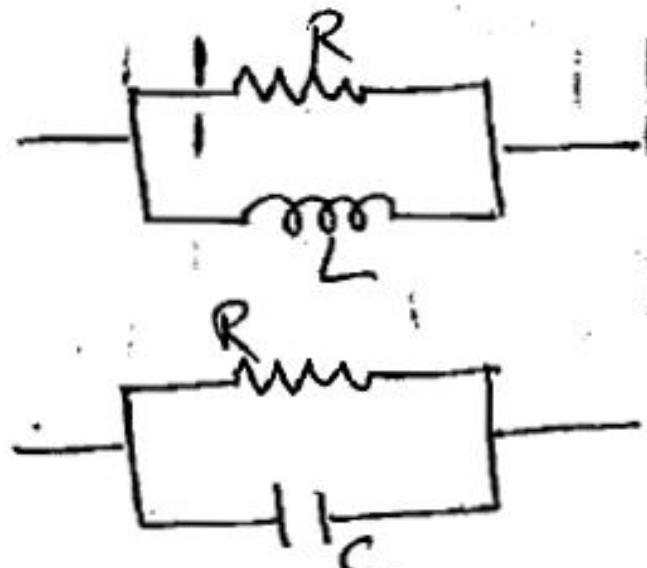
Element	Q-Factor
— w —	0
— m —	ꝝ
—    —	ꝝ
— w — m —	$\frac{\omega L}{R}$



$$\frac{1}{\omega RC}$$

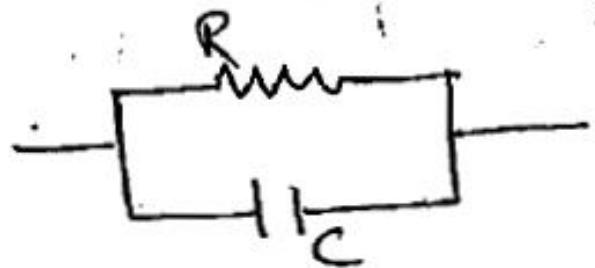


$$\omega_0 = \frac{1}{\sqrt{RC}}$$

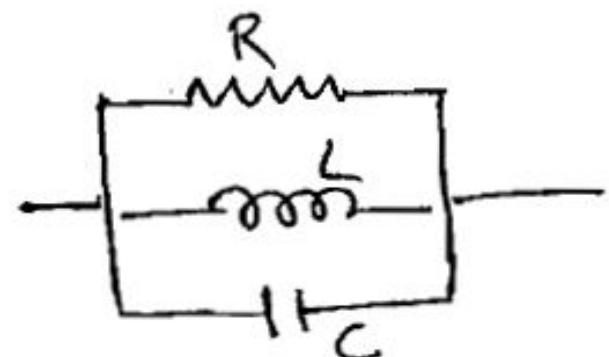


$$\frac{R}{\omega L}$$

$$\omega RC$$

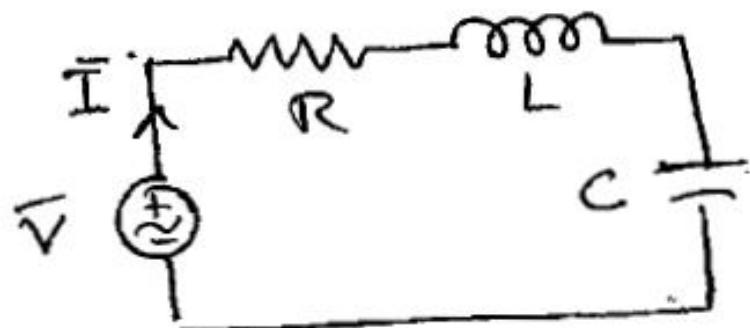


$$\omega_{RC}$$



$$Q_0 = R \sqrt{\frac{C}{L}}$$

**<1> Series Resonance :-**



At resonance ;  $\omega = \omega_0$

$\bar{V} \& \bar{I}$  in phase.

$$\phi = 0^\circ ; Z = R$$

$$\text{But, } Z = R + j[X_L - X_C]$$

but at resonance, net reactance = 0

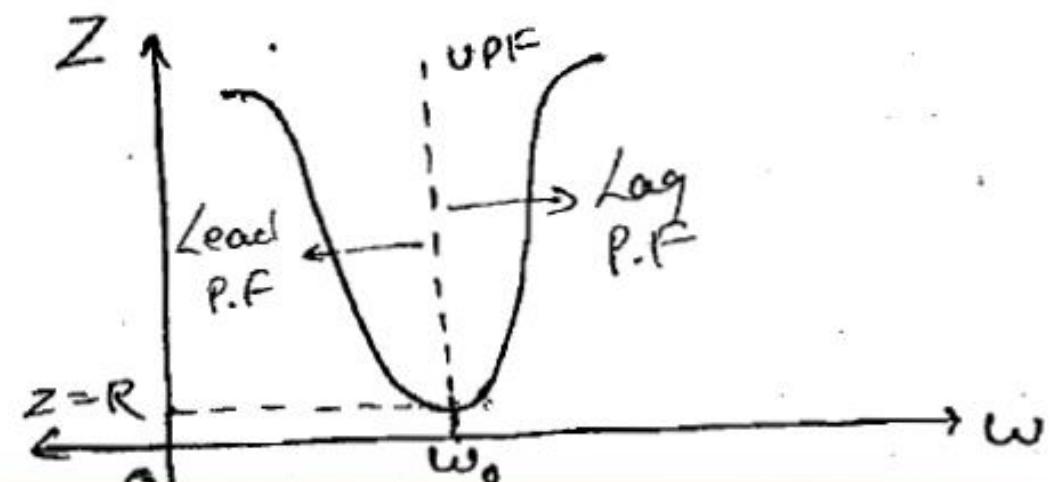
$$\therefore X_L - X_C = 0 \Rightarrow X_L = X_C$$

$$\therefore \omega_0 L = \frac{1}{\omega_0 C} \Rightarrow \omega_0^2 = \frac{1}{LC}$$

$$\omega_0 = \frac{1}{\sqrt{LC}} ; f_0 = \frac{1}{2\pi\sqrt{LC}} \text{ Hz}$$

Graph ①

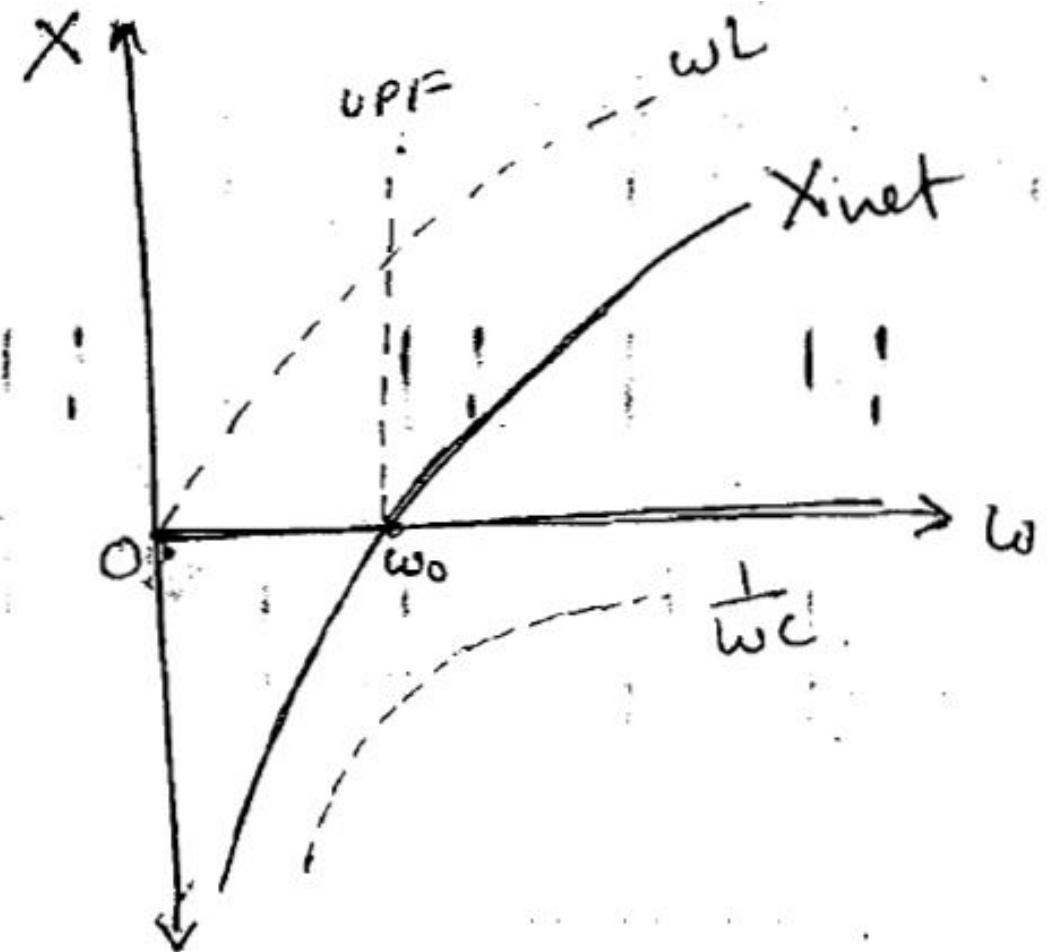
Z vs ω



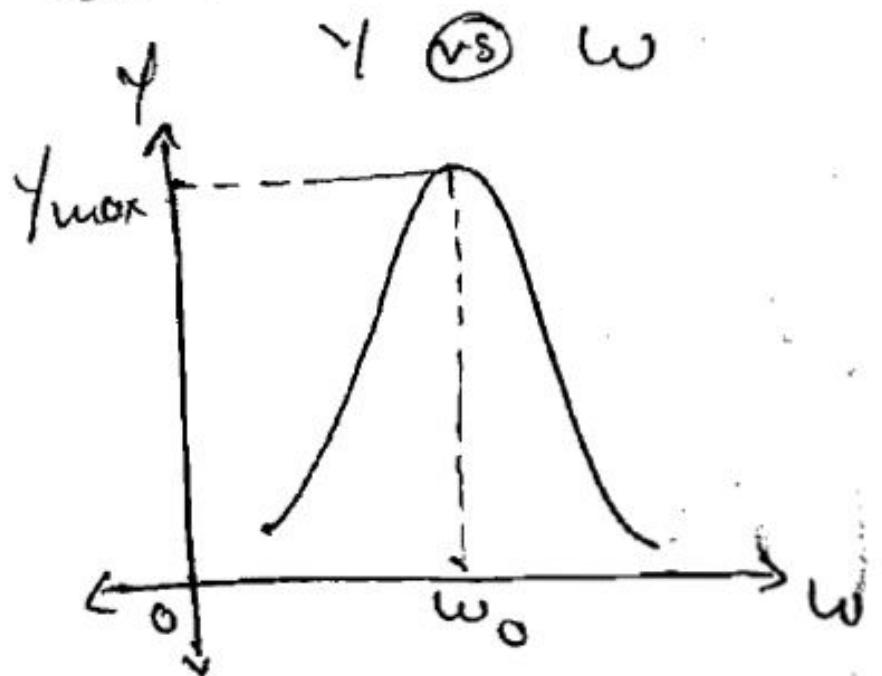
Graph 2  $\times \text{ vs } \omega$

$$Z = R + j \left[ \omega L - \frac{1}{\omega C} \right]$$

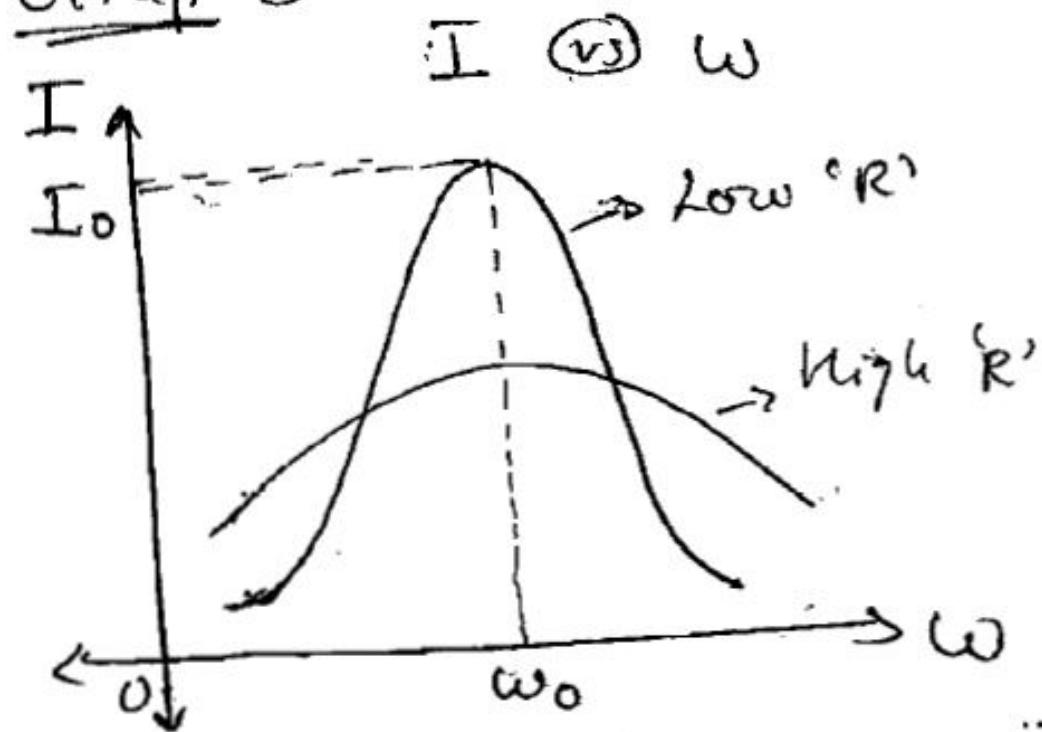
$\downarrow$   
 $X_{\text{net}}$



Graph ③



Graph ④



Phasor diagram :-

$$(a) \omega = \omega_0$$

$$Z = R$$

$$(b) \omega < \omega_0$$

$$Z = R - jX_{\text{net}}$$

$$(c) \omega > \omega_0$$

$$Z = R + jX_{\text{net}}$$

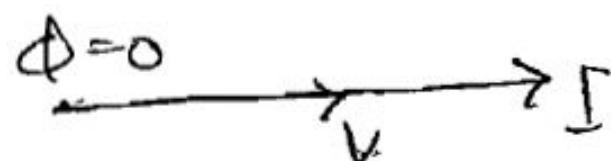
$$(a) \omega = \omega_0$$

$$Z = R$$

$\hookrightarrow$  purely resist.

'I' in phase with  
'V'

$$\phi = 0^\circ \text{ [UPF]}$$



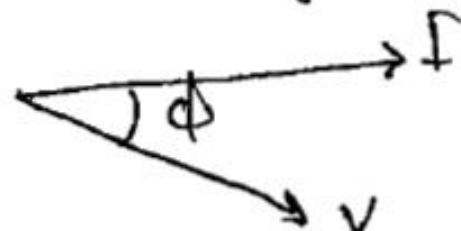
$$(b) \omega < \omega_0$$

$$Z = R - jX_{\text{net}}$$

$\hookrightarrow$  R-C chrt

'I' leads 'V'  
by  $\phi < 90^\circ$

(Leading PF)



$$(c) \omega > \omega_0$$

$$Z = R + jX_{\text{net}}$$

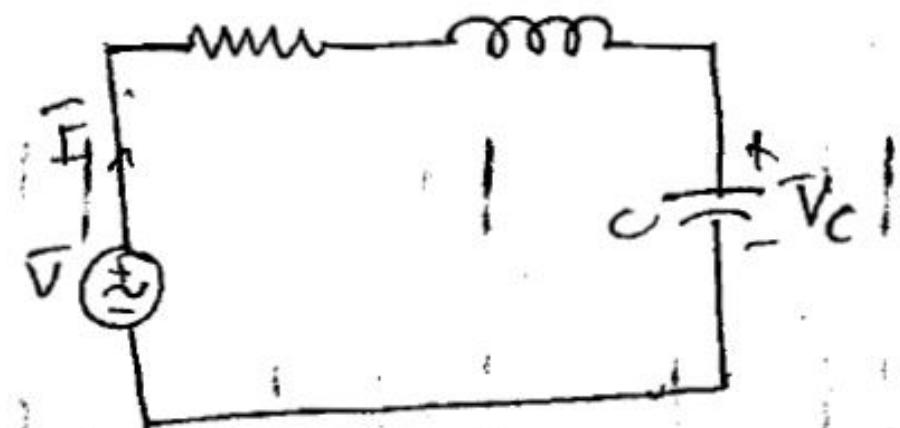
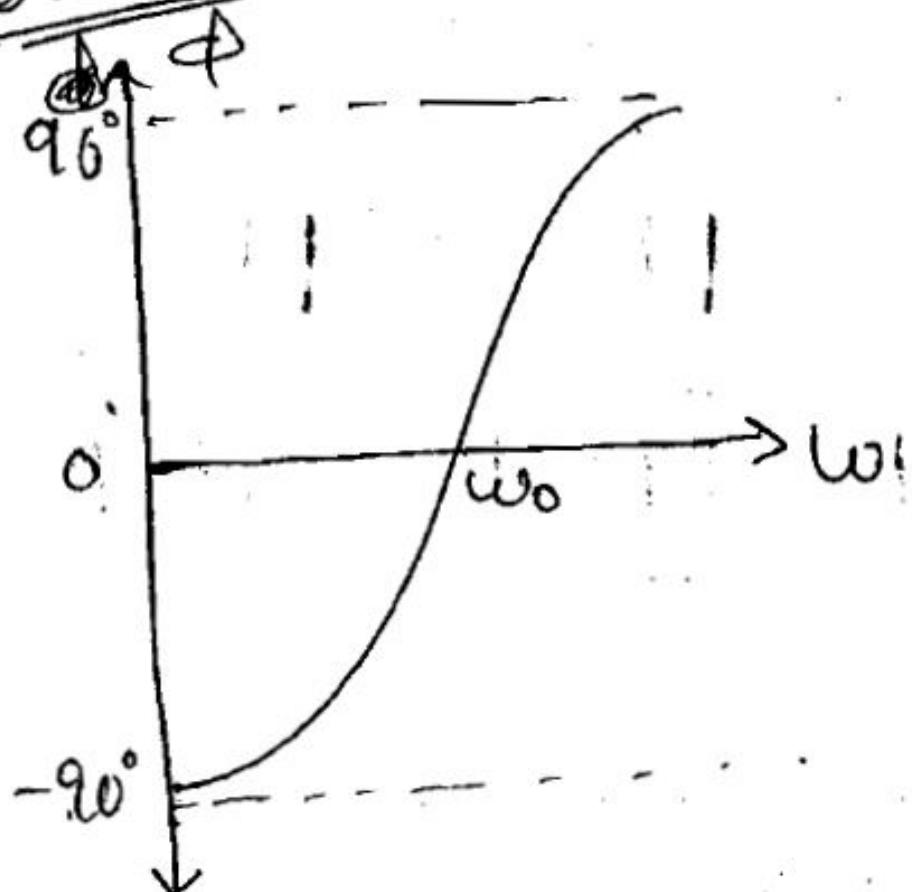
$\hookrightarrow$  R-L chrt

'I' lags 'V'  
by  $\phi \nrightarrow 90^\circ$

(Lagging PF)

$\phi$  is ve with  
~~respect to~~ I (ref.)

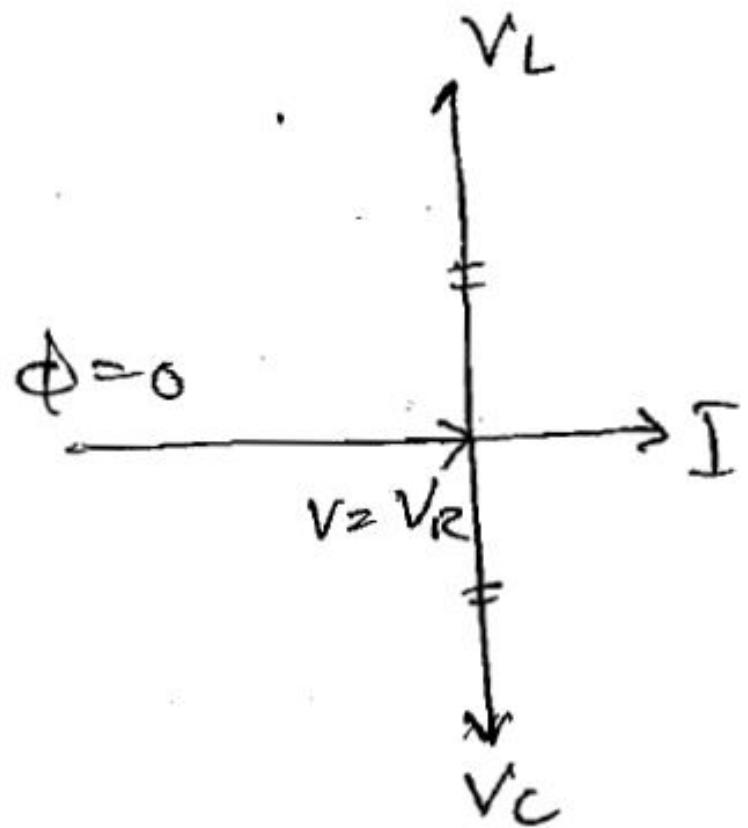
GRAPH - 5



at Resonance  $\omega = \omega_0$

$$|X_L| = |X_C|$$

$$|V_L| = |V_C|$$



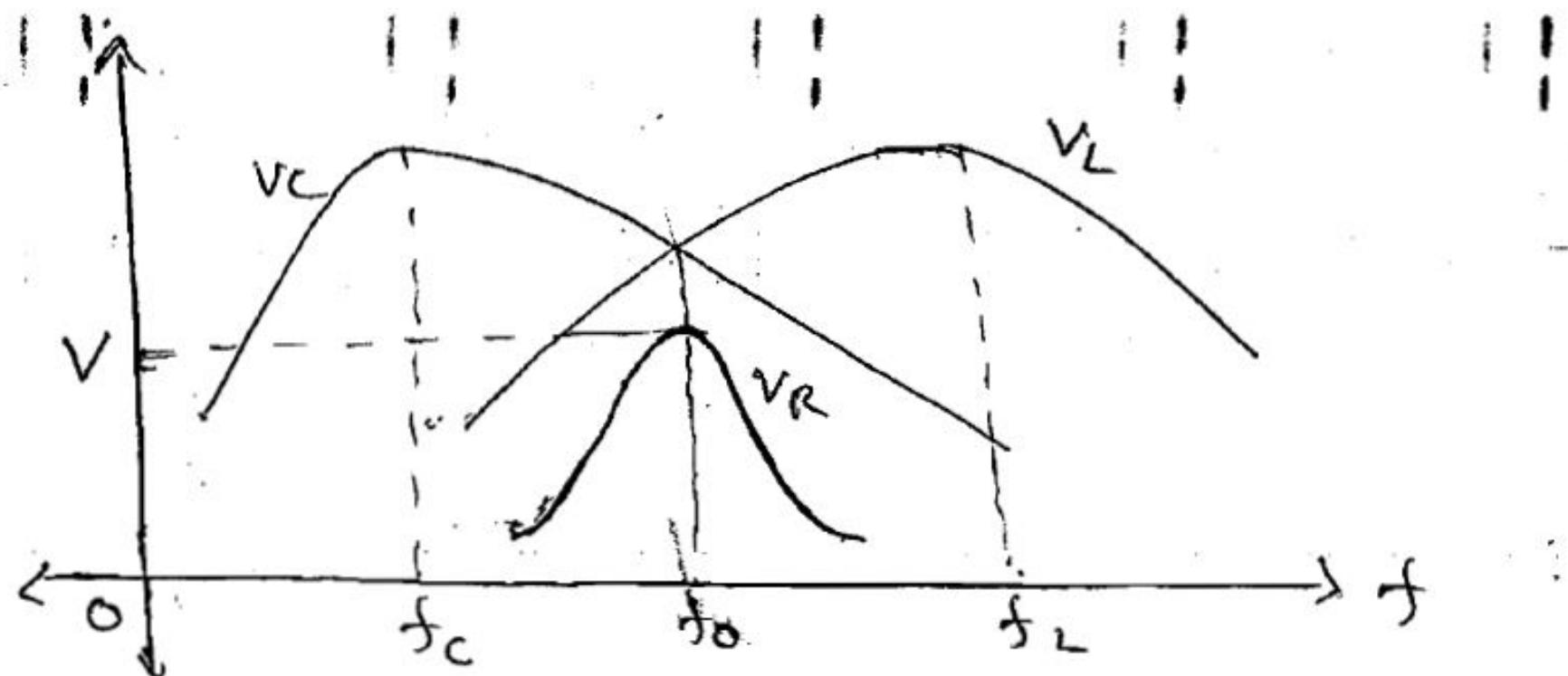
$Q$ -Factor at Resonance.

$$Q_0 = \frac{\omega_0 L}{R} = \frac{1}{\omega_0 C R}$$

$$\text{But } \omega_0 = \frac{1}{\sqrt{LC}}$$

→ Under series resonance net impedance is min, so current is max.  
Hence it is called as acceptor ch.

Variation of voltages across passive elements with change in freq.



The freq. at which max. vltg appears across capacitor

$$f_C = \frac{1}{2\pi} \sqrt{\frac{1}{LC} - \frac{R^2}{2L^2}} = \frac{1}{2\pi\sqrt{LC}} \sqrt{1 - \frac{R^2C}{2L}}$$

②

$$f_C = f_0 \sqrt{1 - \frac{R^2C}{2L}} \text{ Hz}$$

The freq. at which mag. vltg appears across inductor:

$$f_L = \frac{1}{2\pi \sqrt{LC - \frac{R^2C^2}{2}}} = \frac{1}{2\pi \sqrt{L} \left[ 1 - \frac{R^2C^2}{4L} \right]}$$

$$f_L = \frac{f_0}{\sqrt{1 - \frac{R^2C}{2L}}} \text{ Hz}$$

But at  $\omega = \omega_0$

$$|I|_1 = \frac{|V|}{\sqrt{R^2 + \omega^2}} \Rightarrow \text{Maximum}$$

So,

$$|I_0| = \frac{|V|}{R}$$

So, Power transfer also maximum

$$P_0 = I_0^2 R = \frac{|V|^2}{R} \quad \text{W}$$

## Bandwidth :

$$\omega_2 - \omega_1 = \frac{R}{4L} \text{ rad/sec}$$

B.W  $\propto R$

$$f_2 - f_1 = \frac{R}{2\pi L} \text{ Hz}$$

BWL is independent

of 'f<sub>0</sub>'

→ Resonance freq. is geometric mean of

Bandwidth freq.

$$\omega_0 = \sqrt{\omega_1 \omega_2}$$

$$f_0 = \sqrt{f_1 f_2}$$

## Selectivity :- (S)

- Selectivity is the ability of a ckt/ulw to distinguish or discriminate desired & undesired freq.
- $S \propto \frac{1}{|BW|}$

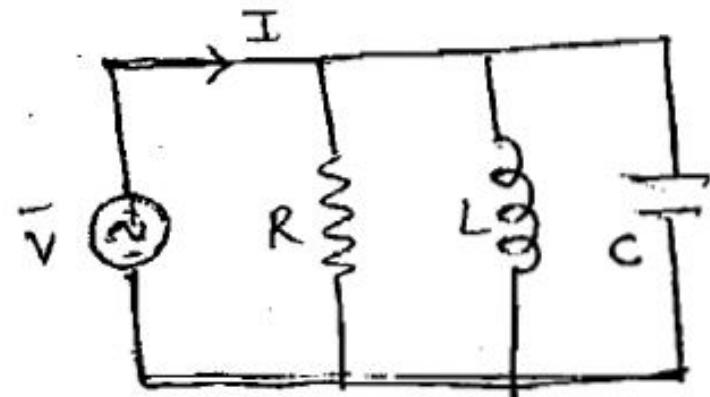
$$S = \frac{f_0}{|f_2 - f_1|} = \frac{\omega_0}{|\omega_2 - \omega_1|}$$

$$S = \frac{\frac{1}{2\pi\sqrt{LC}}}{\frac{R}{2\pi L}} = \frac{1}{R} \sqrt{\frac{L}{C}} = Q_0$$

→ The value of Selectivity is  $Q$ -factor under Resonance.

## <2> Parallel Resonance :-

① General ckt



At resonance

$V \& I$  (in phase)

$\phi = 0^\circ$ , PF = 1 (UPF)

$$Y_T = Y_R + Y_L + Y_C$$

$$Y_T = \frac{1}{R} + \frac{1}{jX_L} + \frac{1}{-jX_C}$$

$$= \frac{1}{R} + \frac{j}{X_C} - \frac{j}{X_L} = \frac{1}{R} + j \left[ \frac{1}{X_C} - \frac{1}{X_L} \right]$$

At resonance ( $\omega = \omega_0$ )

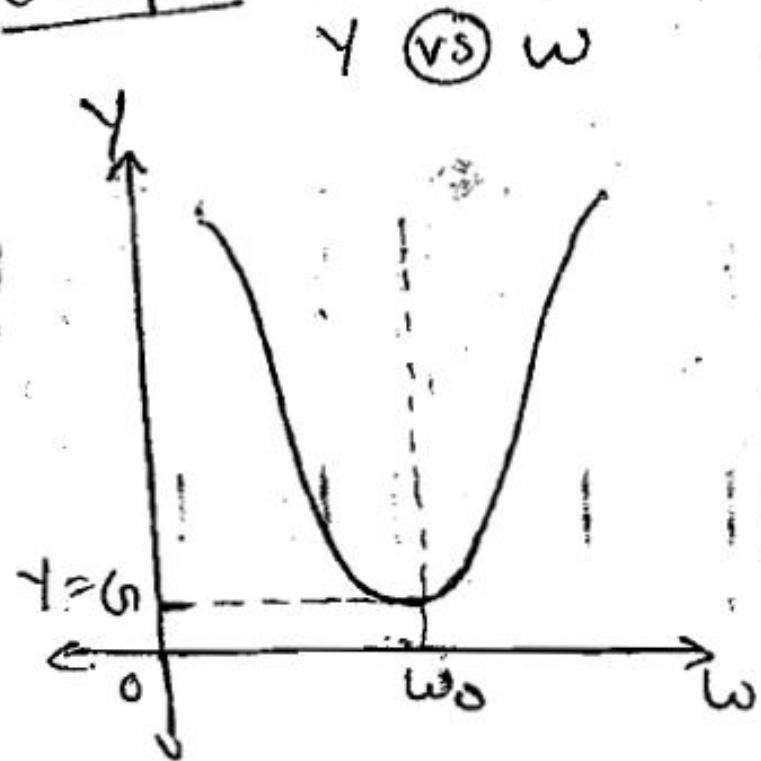
Net susceptance = 0

$$\frac{1}{X_C} - \frac{1}{X_L} = 0 \Rightarrow \omega_0 L = \frac{1}{\omega_0 C}$$

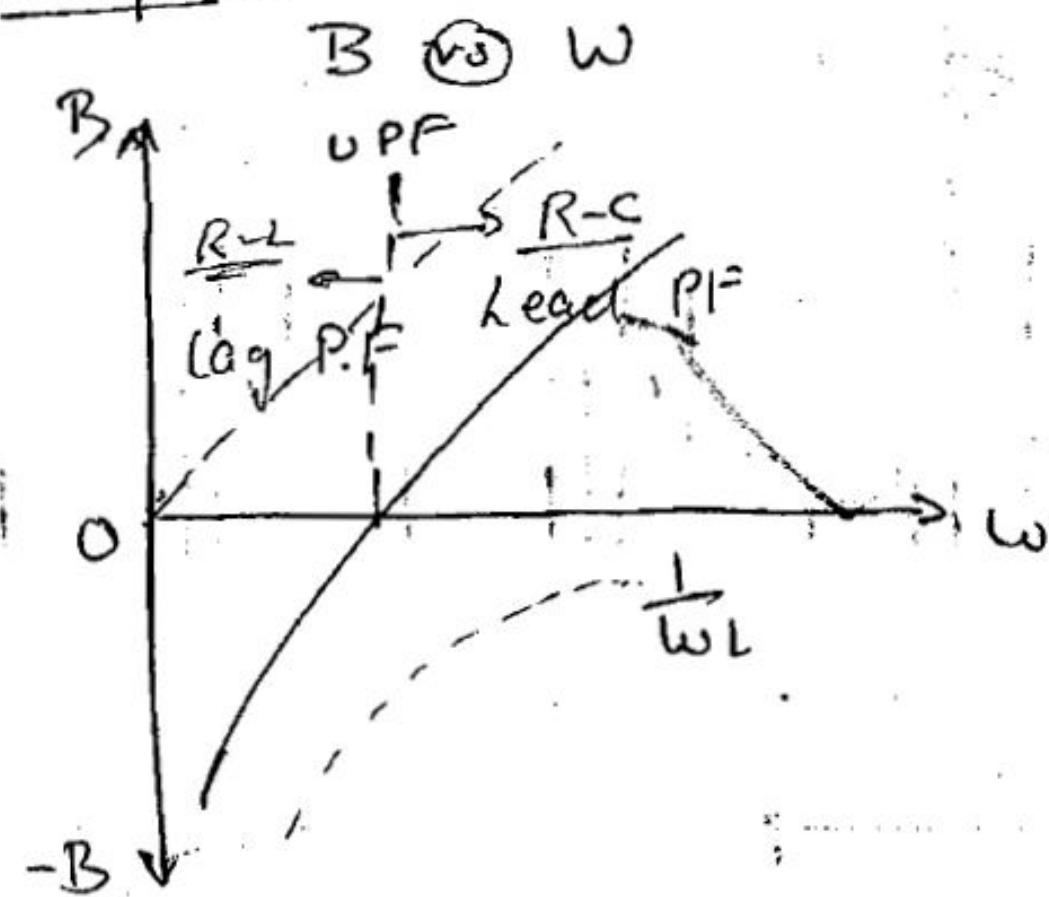
$$\omega_0 = \frac{1}{\sqrt{LC}} \text{ rad/sec}$$

$$f_0 = \frac{1}{2\pi\sqrt{LC}} \text{ Hz}$$

Graph ①

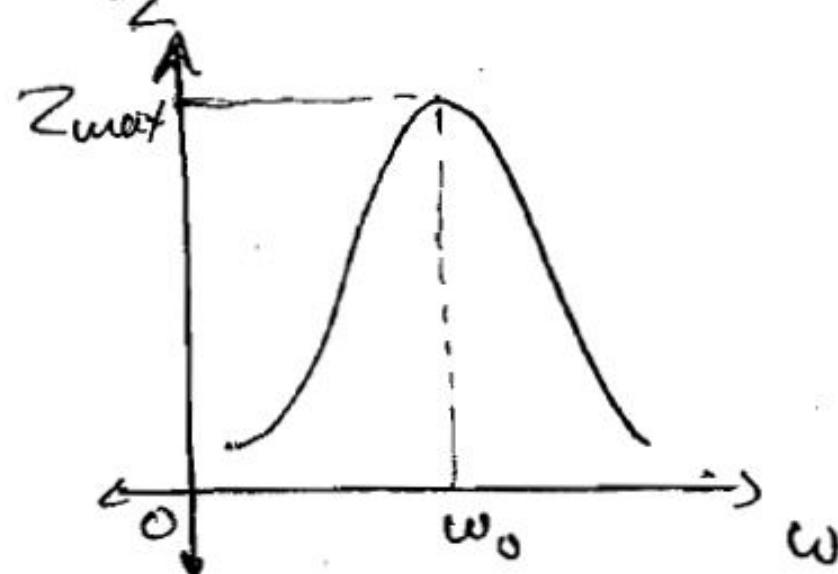


Graph ②



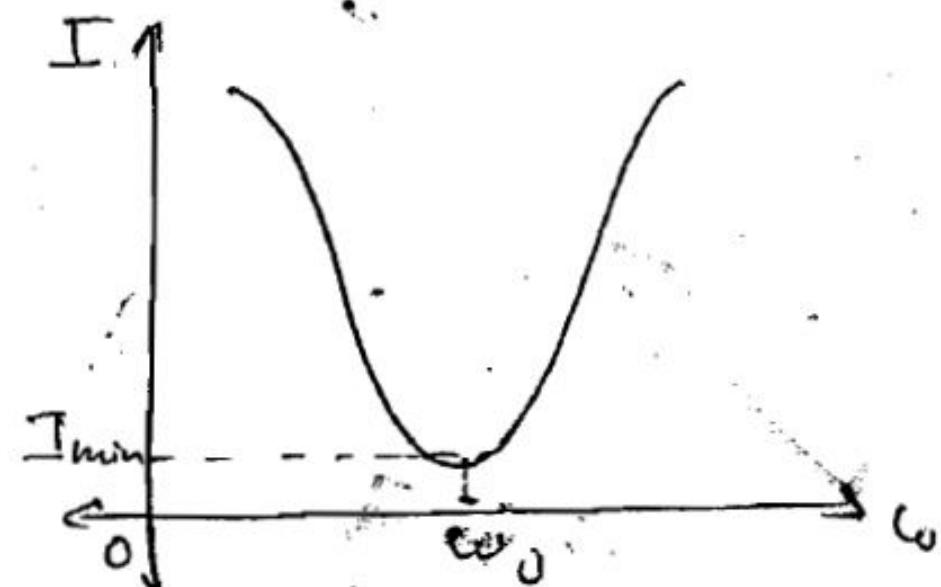
Graph ③

$Z \text{ vs } \omega$



Graph ④

$I \text{ vs } \omega$



Phasor diagrams :

(a)  $\omega = \omega_0$

$$Y = G_r$$

$\hookrightarrow$  pure resist.

(b)  $\omega < \omega_0$

$$Y = G_r - jB_{net}$$

$\hookrightarrow$  RL parallel

(c)  $\omega > \omega_0$

$$Y = G_r + jB_{net}$$

$\hookrightarrow$  RC  $u^{\text{ckt}}$

## Phasor diagrams:

(a)  $\omega = \omega_0$

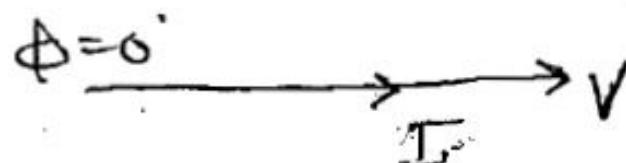
$$Y = G$$

$\hookrightarrow$  pure resist.

'I' in phase with 'V'.

$$\phi = 0^\circ$$

$$PF = 1 (UPF)$$



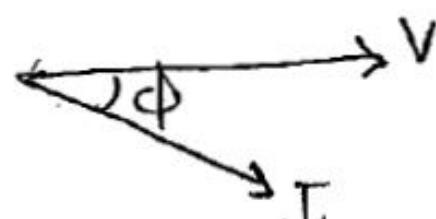
(b)  $\omega < \omega_0$

$$Y = G - jB_{net}$$

$\hookrightarrow$  RL parallel ckt

'I' lags 'V' by  $\phi < 90^\circ$

(lagging PF)



$\phi$  is -ve w.r.t

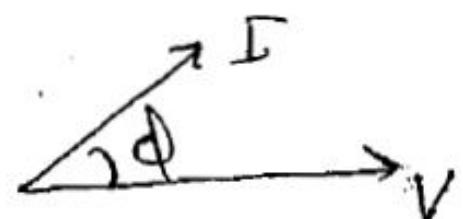
(c)  $\omega > \omega_0$

$$Y = G + jB_{net}$$

$\hookrightarrow$  RC  $\pi$  ckt

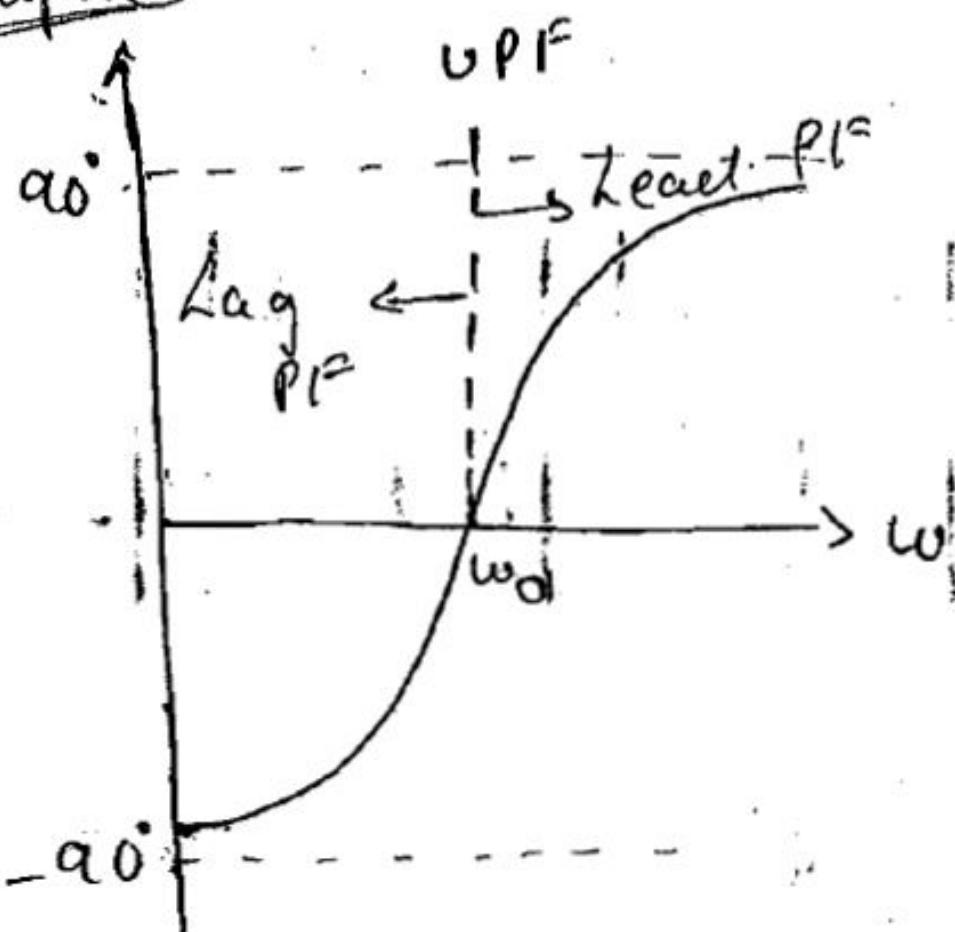
'I' leads 'V' by  $\phi < 90^\circ$

(leading PF)

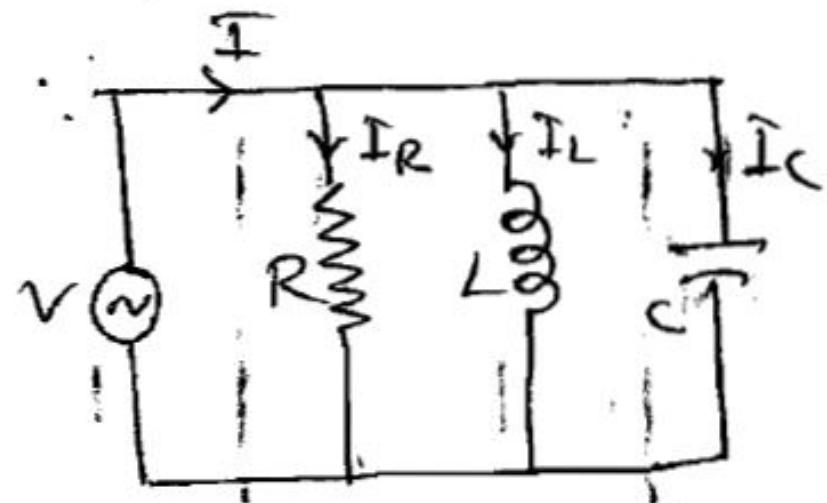


$\phi$  is +ve w.r.t

Graph ⑤



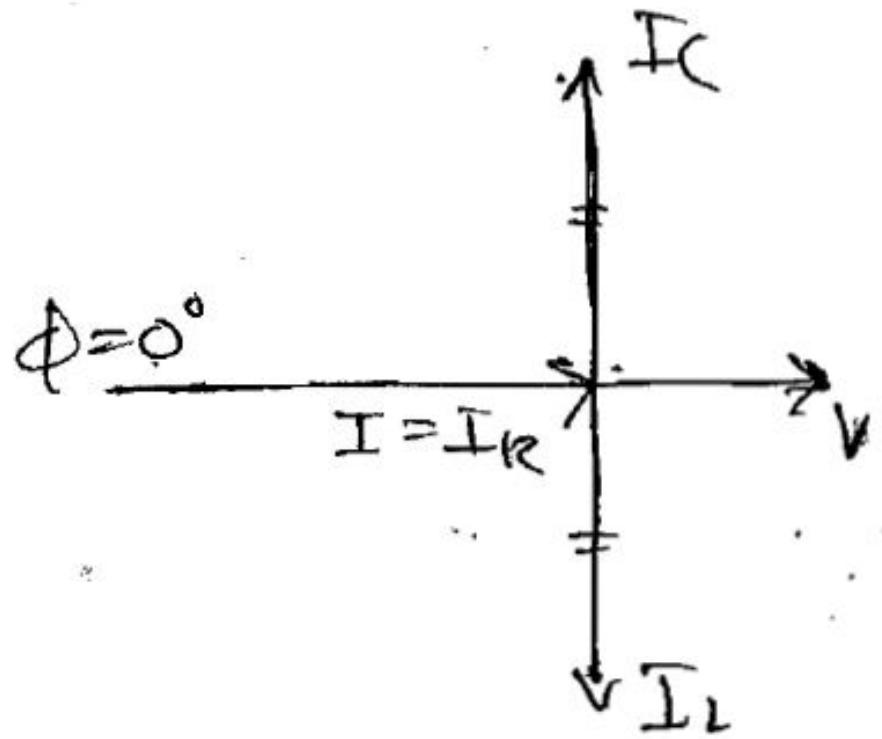
Complete phasor diagram



At resonance

$$|B_L| = |B_C|$$

$$|I_L| = |I_C|$$



$$I = I_R$$

$$\phi = 0^\circ$$

$$PF = \pm (V/PF)$$

Q-factor at Resonance

$$Q_0 = \frac{R}{\omega_0 L} = \omega_0 R C$$

$$\text{But } \omega_0 = \frac{1}{\sqrt{LC}}$$

$$Q_0 = \frac{R}{\frac{1}{\sqrt{LC}} \times L} = \frac{1}{\sqrt{LC}} \times RC$$

$$Q_0 = R \sqrt{\frac{C}{L}}$$

At parallel resonance condition, net impedance is max. so current is minimum. Hence it is called -  
rejector ckt.

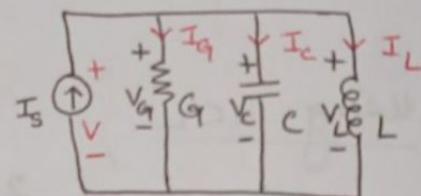
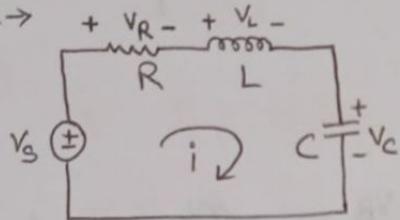
At  $1/f^2$  resonance freq. it is as if the total current flows only through reso. Hence it is called as current amplification circuit.

## CHAPTER 2 : DUALITY & NETWORK TOPOLOGY

### → DUAL & DUALITY

Two circuits are duals of each other if the mesh equation that characterize one of them has the same mathematical form as the nodal equation that characterize the other.

Ex →



#### Mesh KVL

$$-V_s + V_R + V_L + V_c = 0$$

$$V_s = iR + L \frac{di}{dt} + \frac{1}{C} \int i dt$$

#### Nodal KCL

$$-I_s + I_g + I_c + I_L = 0$$

$$I_s = V_g + C \frac{dv}{dt} + \frac{1}{L} \int v dt$$

#### Some dual elements

$$V \longleftrightarrow I$$

$$V(t) \longleftrightarrow i(t)$$

$$V_m \sin \omega t \longleftrightarrow I_m \sin \omega t$$

$$R \longleftrightarrow G$$

$$L \longleftrightarrow C$$

$$KVL \longleftrightarrow KCL$$

$$\text{Series} \longleftrightarrow \text{Parallel}$$

$$\text{Mesh} \longleftrightarrow \text{Node}$$

$$\pi \longleftrightarrow T$$

$$\frac{di}{dt} \longleftrightarrow \frac{dv}{dt}$$

$$\int v dt \longleftrightarrow \int i dt$$

$$OC \longleftrightarrow SC$$

$$\text{Thermin} \longleftrightarrow \text{Nortons}$$

$$Y \longleftrightarrow \Delta$$

$$\phi \longleftrightarrow q$$

Tree  $\longleftrightarrow$  Co tree

Twig  $\longleftrightarrow$  Link / chord

Cut set  $\longleftrightarrow$  Tie set

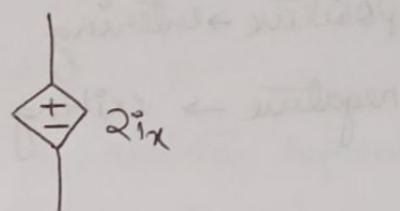
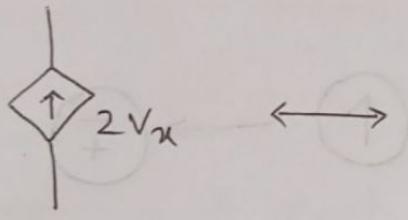
Z  $\longleftrightarrow$  Y

X  $\longleftrightarrow$  B

Switch in series  $\longleftrightarrow$  switch in parallel

(getting closed)

(getting opened)



Polarity  
voltage

$\longleftrightarrow$   
direction  
current

Ex  $\rightarrow$  Dual of 2- $\Omega$  resistance  $\Rightarrow$  2 siemens  
(conductance)

### To solve problems

- ① Place a dot within each loop. These dots will become nodes of dual n/w.
- ② Place a dot outside of n/w, this dot will be ground node of dual n/w.
- ③ Carefully draw lines b/w nodes such that each line cuts only one element.
- ④ If any element exclusively present in a loop then connect the dual element in

between node and ground.

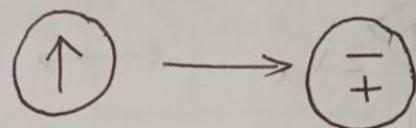
⑤ If an element is common b/w 2 loops, then dual element is placed in b/w two nodes.

⑥ For a voltage source producing clockwise current in a loop, its dual current source will have a direction from ground to non reference node.

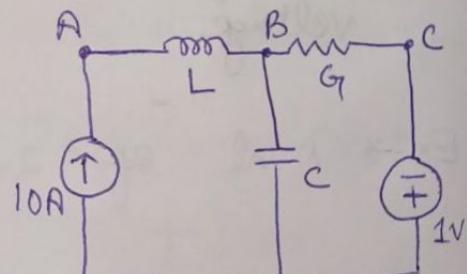
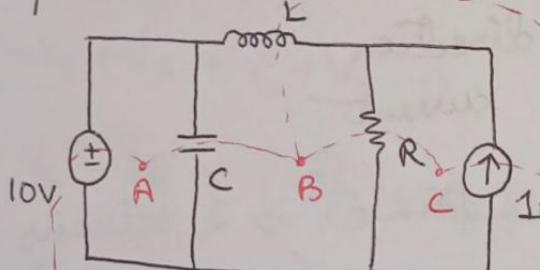
### Antidiadomise

positive → entering

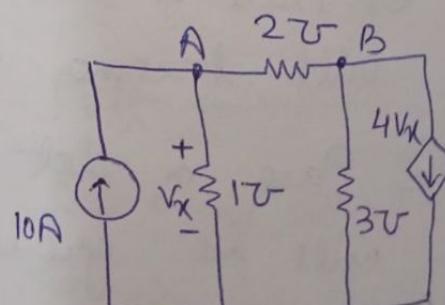
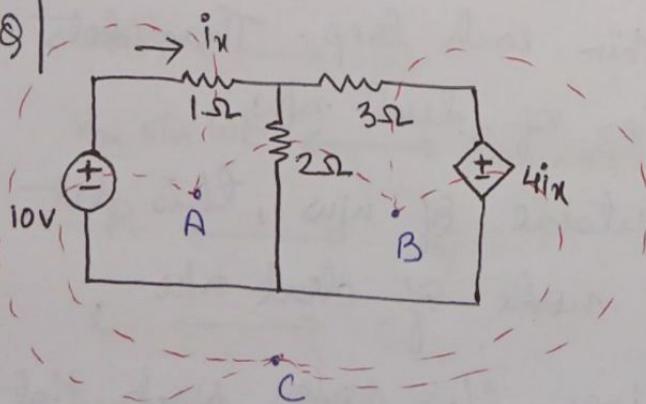
negative → exiting



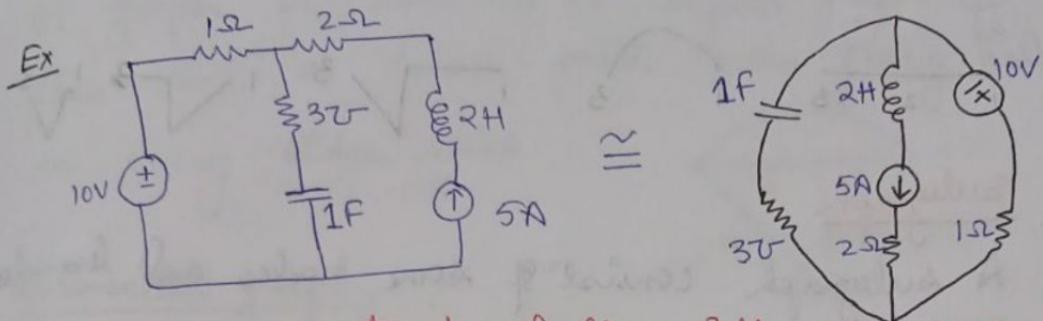
Q/



Q/



Topology - It is a branch of geometry applicable to electrical circuits where even by bending, stretching, swaping, tiling in knots, making the circuit upside down etc will not disturb the circuit property.



→ Shape of a network ideally will not affect circuit analysis.

Graph - A graph is a skeleton representation of a n/w where every element is supposed by its nature and represented as a simple line segment.

NOTE      Ideal voltage source  $\rightarrow$  SC  
 Ideal current source  $\rightarrow$  OC

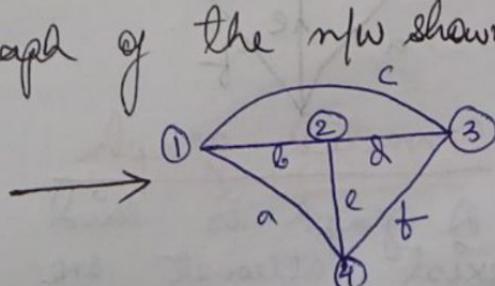
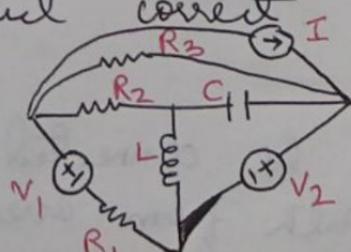
To simplify the order of matrix, we generally consider principle node only.

Nodes  $\rightarrow$  vertices      Branches  $\rightarrow$  edges.

→ Nodes are numbers — ①, ②, ③, ...

Branches are named — a, b, c, ...

Q) construct correct graph of the n/w shown below



Path — Path is a traversal from one node to another without crossing the same node twice.

Q) No. of possible path for the above graph from node 1 to 3 is 5.

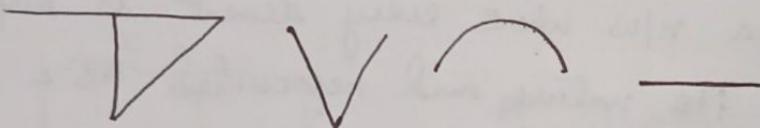


### Subgraph

A subgraph consist of some nodes and branches of main graph.

Even a single edge can be a subgraph of the main graph.

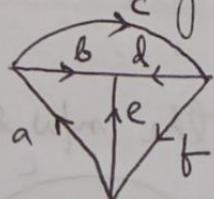
Ex



### Directed graph

A graph is said to be directed if every edge is given a reference direction which is indicated by placing an arrow on every branch.

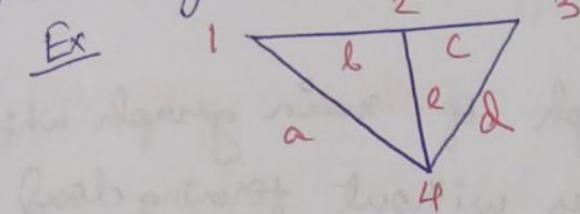
NOTE : This reference orientation need not necessarily indicate current dir<sup>n</sup>.



### Connected graph

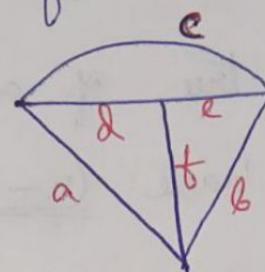
A graph is said to be connected if there exist atleast one path from every node

to every other node.



complete graph / completely connected graph

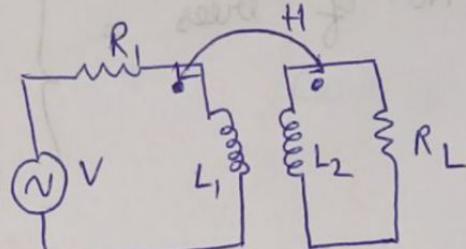
A graph is said to be complete graph if there exist a direct path from every node to every other node.



unconnected graph

① wireless communication.

② Magnetic circuits



Q) Minimum no. of edges to make a graph complete with n nodes is  $\boxed{nC_2}$ .

nodes

2

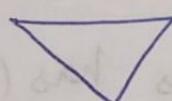
Graph

—

Edges

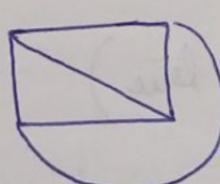
$$1 = \frac{2(2-1)}{2}$$

3



$$3 = \frac{3(3-1)}{2}$$

4



$$6 = \frac{4(4-1)}{2}$$

## # TREE

A tree is a subgraph of main graph which connects all the nodes without forming closed loops.

\* Rank of a tree with 'n' nodes =  $(n-1)$

Any tree of a given graph with n nodes will have  $(n-1)$  edges.

$$\text{No. of trees} = \begin{cases} n^{(n-2)} & ; \text{ for } n > 2 \rightarrow \text{complete graph only} \\ \det |[A_n] [A_n]^T| & ; \text{ for any graph} \end{cases}$$

where  $[A_n]$  = reduced incidence matrix

Twig

Branch of a tree is specifically called as twig which is indicated by thick line segment.

Any tree with n nodes has  $(n-1)$  twigs.

co tree (complement of tree)

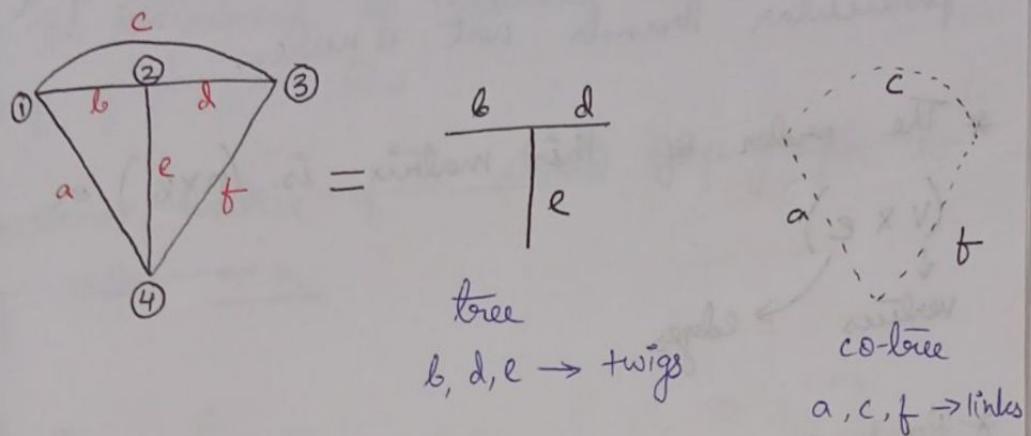
The set of branches other than tree branches in a graph forms co tree.

Link / chord

The branch of co tree is called as link

which is indicated by dotted line.

For any co-tree, we have  $b - (n-1)$  links



$$\begin{array}{lcl} \text{graph} & = & \text{Tree} + \text{co-tree} \\ \downarrow & & \downarrow \\ \text{edges} & & \text{twigs} \\ \downarrow & & \downarrow \\ b & = & (n-1) + b - (n-1) \end{array}$$

Q) The no. of possible trees for the n/w graph shown below is \_\_\_\_\_ and draw them separately.

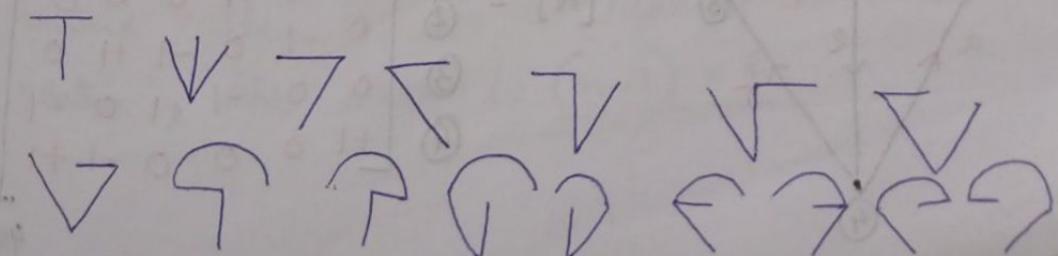


→ This is a complete graph

$$\begin{aligned} \therefore \text{No. of trees} &= n^{(n-2)} \\ &= 4^{(4-2)} \end{aligned}$$

Trees are

$$= 16$$



## Incidence matrix [A]

Matrix that gives relation b/w no. of nodes and no. of branches and orientation of a particular branch w.r.t a node.

\* The order of this matrix is  $(n \times b)$  or

$$(v \times e)$$

$\downarrow$   
vertices      edges

\* Rank of incidence matrix with  $n$  nodes is  $\underline{(n-1)}$

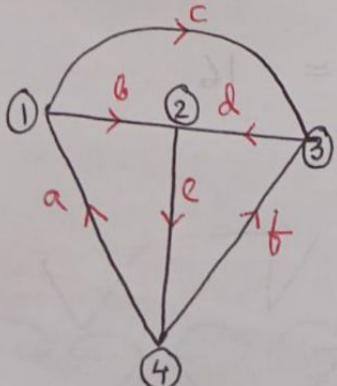
\* The elements of this matrix  $[A] = [a_{ij}]_{n \times b}$

where  $a_{ij} = +1$  ; if  $j^{\text{th}}$  branch is incident with  $i^{\text{th}}$  node and oriented away from it.

$a_{ij} = -1$  , if incident towards

$a_{ij} = 0$  , if not incident

Q) Construct the complete incidence matrix for the oriented graph shown below.



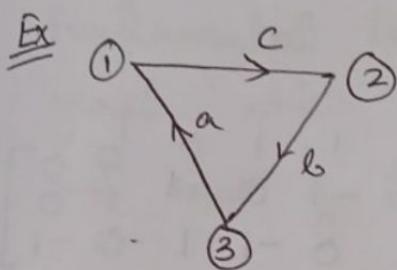
$$[A] = \begin{bmatrix} a & b & c & d & e & f \\ \textcircled{1} & -1 & +1 & +1 & 0 & 0 & 0 \\ \textcircled{2} & 0 & -1 & 0 & -1 & +1 & 0 \\ \textcircled{3} & 0 & 0 & -1 & +1 & 0 & -1 \\ \textcircled{4} & +1 & 0 & 0 & 0 & -1 & +1 \end{bmatrix}_{4 \times 6}$$

NOTE

The algebraic sum of the elements of every column vertically is zero.

- Q1 The determinant of incidence matrix of a closed loop graph is 0.

Incidence matrix of a closed loop graph is of order  $n \times n$ .



$$[A] = \begin{bmatrix} -1 & 0 & 1 \\ 0 & -1 & -1 \\ 1 & 1 & 0 \end{bmatrix}$$

$$\begin{aligned} \det |A| &= -1 [0 - (-1)] + \\ &\quad 1 (0 - (-1)) \\ &= -1 + 1 = 0 \end{aligned}$$

NOTE

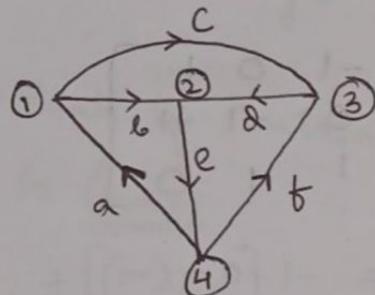
If the incidence matrices of 2 independent NW graphs are identical then they are said to obey the principle of Isomorphism.

### Reduced Incidence Matrix $[A_R]$

If one of the node in a given graph is considered as reference and that particular row is neglected while writing the incidence matrix, then it is a reduced incidence matrix, the order is  $(n-1) \times b$ .

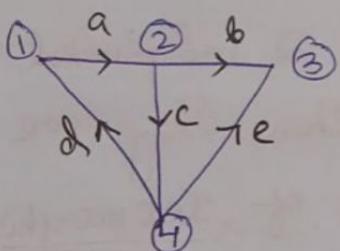
In computer methods of electrical circuits analysis by considering  $A_R$ , the memory space requirement and iteration time for solutions will be decreased.

Ex From the graph, if node ④ is considered as reference and that particular row ④ is neglected, then reduced incidence matrix is



$$[A_R] = \begin{bmatrix} -1 & 1 & 1 & 0 & 0 & 0 \\ 0 & -1 & 0 & -1 & 1 & 0 \\ 0 & 0 & -1 & 1 & 0 & -1 \end{bmatrix}$$

Q No. of possible trees from n/w graph shown below is \_\_\_\_\_ and draw them separately.



It is just connected graph

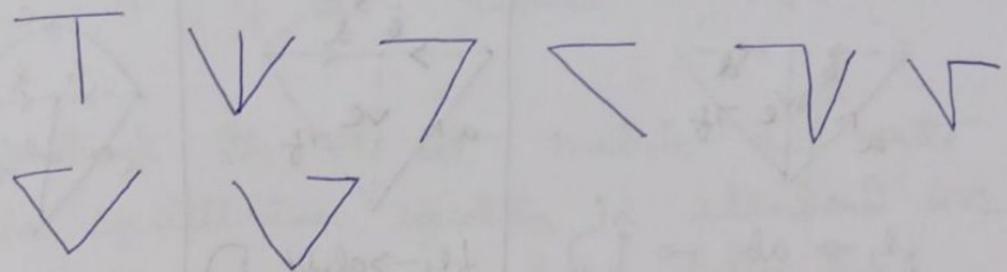
$$\text{No. of trees} = \det [A_R] [A_R]^T$$

$$= \begin{bmatrix} 1 & 0 & 0 & -1 & 0 \\ 0 & -1 & 0 & 0 & -1 \\ 0 & 0 & -1 & 1 & -1 \end{bmatrix} \begin{bmatrix} 1 & 0 & 0 \\ 0 & -1 & 0 \\ 0 & 0 & -1 \\ -1 & 0 & 1 \\ 0 & -1 & 1 \end{bmatrix}^T$$

$$= \begin{bmatrix} 2 & 0 & -1 \\ 0 & 2 & -1 \\ -1 & -1 & 3 \end{bmatrix}$$

$$\begin{aligned} \det [A_R] [A_R]^T &= 2(6-1) - 1(0-(-2)) \\ &= 10 - 2 = 8 \text{ possible trees} \end{aligned}$$

The trees are

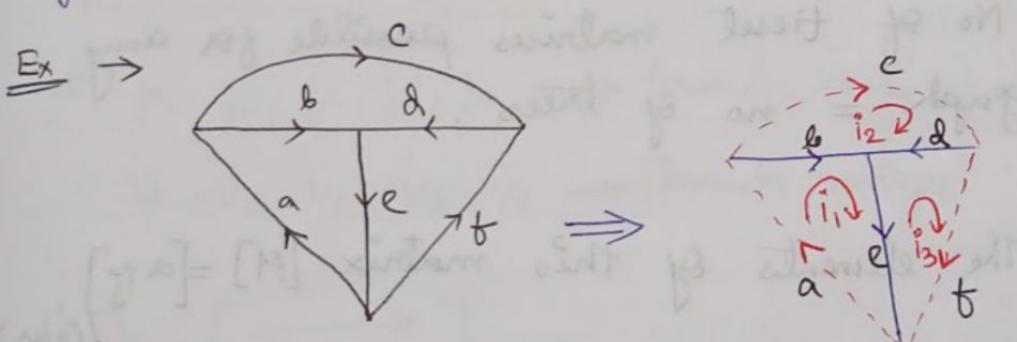


### # Concept of Fundamental Loops and Tie-set current

Fundamental loops are closed path of the graph which are formed by only one link and rest of them are twigs.

→ No. of fundamental loops for any given graph  
= No. of links i.e.  $b - (n - 1)$

→ These fundamental loop currents are called Tie-set current and their orientation is governed by the link in it.



$$\text{No. of f-loops} = b - (n - 1)$$

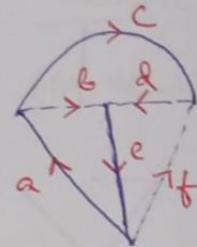
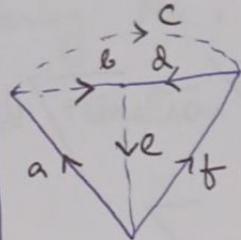
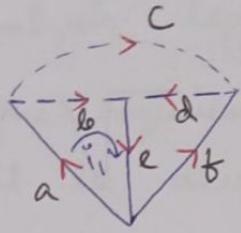
$$= 3$$

$$f_{l_1} = abea \rightarrow i_1 \downarrow$$

$$f_{l_2} = dcba \rightarrow i_2 \downarrow$$

$$f_{l_3} = defd \rightarrow i_3 \downarrow$$

Q) Find fundamental loop.



$$fl_1 \rightarrow abc \rightarrow i_1 \rightarrow$$

$$fl_2 \rightarrow def \rightarrow i_2 \rightarrow$$

$$fl_3 \rightarrow acf \rightarrow i_3 \rightarrow$$

$$fl_1 \rightarrow adef \rightarrow i_1 \rightarrow$$

$$fl_2 \rightarrow acf \rightarrow i_2 \rightarrow$$

$$fl_3 \rightarrow def \rightarrow i_3 \rightarrow$$

$$fl_1 \rightarrow abc \rightarrow i_1 \rightarrow$$

$$fl_2 \rightarrow acde \rightarrow i_2 \rightarrow$$

$$fl_3 \rightarrow acf \rightarrow i_3 \rightarrow$$

### # Tie Set Matrix [M]

It is a matrix that gives the relation b/w the branch currents and tie set currents where every branch current can be expressed in terms of TieSet currents.

Order of this matrix is links  $\times$  branches

i.e.  $\underline{[b-(n-1)] \times b}$

No. of tieSet matrices possible for any graph = no. of trees.

The elements of this matrix  $[M] = [a_{ij}]$

where

$$a_{ij} = +1 \text{ if } j^{\text{th}} \text{ branch current is}$$

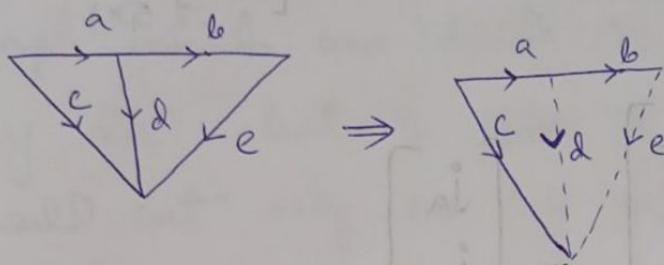
incident with  $i^{\text{th}}$  TieSet

current and oriented in same dir.

$a_{ij} = -1$ , if incident and opposite

$a_{ij} = 0$ , if not incident

- Q) Construct the tie set matrix and write the equilibrium equation in standard KVL form for n/w graph shown below by considering a, b, c as three branches.



$$\begin{array}{l} f_{l_1} \rightarrow a \rightarrow c \rightarrow d \rightarrow e \rightarrow i_1 \\ f_{l_2} \rightarrow a \rightarrow b \rightarrow e \rightarrow c \rightarrow d \rightarrow i_2 \end{array} \quad \left| \begin{array}{l} \text{No. of fundamental loops} \\ = 5 - (4-1) = 2 \end{array} \right.$$

$$[M] = \begin{bmatrix} i_1 & a & b & c & d & e \\ i_2 & +1 & 0 & -1 & +1 & 0 \\ & +1 & +1 & -1 & 0 & +1 \end{bmatrix}_{2 \times 5}$$

### Equilibrium equations

Let  $j_a, j_b, j_c, j_d, j_e \rightarrow$  branch currents

$v_a, v_b, v_c, v_d, v_e \rightarrow$  branch voltages

KVL  $\longrightarrow$

$$[M] [V_b] = [0] \Rightarrow \begin{bmatrix} 1 & 0 & -1 & 1 & 0 \\ 1 & 1 & -1 & 0 & 1 \end{bmatrix}_{2 \times 5} \begin{bmatrix} v_a \\ v_b \\ v_c \\ v_d \\ v_e \end{bmatrix}_{5 \times 1} = \begin{bmatrix} 0 \\ 0 \end{bmatrix}_{2 \times 1}$$

\* Relation b/w  $j$  and  $i$   $\left[\downarrow\right]$

$$[M]^T [I_L] = [J_b]$$

$$\Rightarrow \begin{bmatrix} 1 & 1 \\ 0 & 1 \\ -1 & -1 \\ 1 & 0 \\ 0 & 1 \end{bmatrix}_{5 \times 2} \begin{bmatrix} i_1 \\ i_2 \end{bmatrix}_{2 \times 1} = \begin{bmatrix} j_a \\ j_b \\ j_c \\ j_d \\ j_e \end{bmatrix}_{5 \times 1}$$

$$\Rightarrow \begin{bmatrix} i_1 + i_2 \\ i_2 \\ -i_1 - i_2 \\ i_1 \\ i_2 \end{bmatrix} = \begin{bmatrix} j_a \\ j_b \\ j_c \\ j_d \\ j_e \end{bmatrix}$$

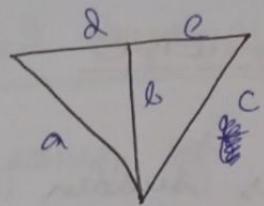
### # concept of cut set

A cut set represents set of branches which when removed in a graph can be divided into 2 parts.

#### NOTE

The no. of cutsets simply represent the no. of possible ways that a graph can be divided into 2 parts.

Q) which of the following set of branches represent a proper cutset for the graph shown.



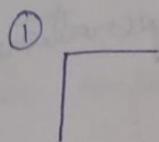
① acd

② ace

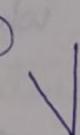
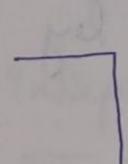
~~③ bdc~~

④ cde

Ans  $\rightarrow$  bdc



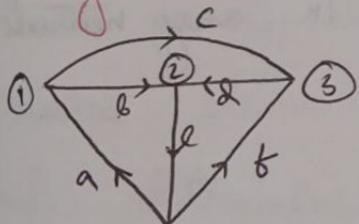
②



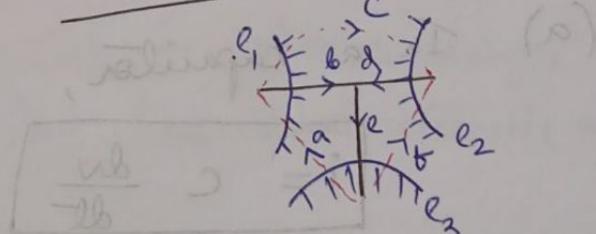
fundamental cutset are cut through of a graph which can divide in 2 parts in any direction. But in path of cutting it should cut only one twig and rest of them as links.

No. of f-cutset = No. of twigs (i.e.  $n-1$ )

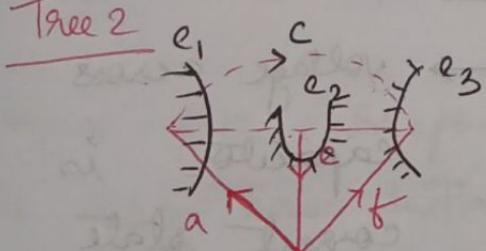
Orientation of f-cutset are governed by the twig in it.



Tree 1



Tree 2



$fc_1 \rightarrow a, b, c$

$fc_2 \rightarrow b, d, e$

$fc_3 \rightarrow c, d, f$

No. of f-cutsets =  $4-1=3$

$fc_1 \rightarrow a, b, c \rightarrow e_1$

$fc_2 \rightarrow c, d, f \rightarrow e_2$

$fc_3 \rightarrow a, e, f \rightarrow e_3$

\*\*

Tree 3 and 4 please  
try yourself

## ELECTRICAL TRANSIENTS

- Transients are considered as sudden change in the state of a circuit or network which are indicated by switch operations.
- Though transients occur for very short duration in time, their impact is huge in determining entire steady state response.

### # State Variables

(These) are the critical parameters that must be observed to determine the transient state solution in any network.

(a) In a capacitor,

$$i = C \frac{dv}{dt}$$

→ voltage across

KCL → Nodal

capacitor is  
current state  
variable.

(b) In an inductor

$$v = L \frac{di}{dt}$$

→ current through inductor  
is current state  
variable.

KVL → mesh

### Forced Response

Response of any circuit/n/w when a source is present is called forced response.

This response is independent to the nature of passive elements and can be different for different types of i/p's.

### Natural response

The response of any circuit/n/w without any source is called natural response.

This response is independent of input but purely depends upon the nature of passive elements. It is always unique and determined by the characteristic eq<sup>n</sup> governing the n/w.

Some free response is possible provided the n/w has initial stored energy.

$$\text{complete response of a system} = \text{forced response} + \text{Natural response}$$
$$\downarrow \qquad \qquad \qquad \downarrow$$
$$\text{zero state response} \qquad \qquad \text{zero i/p response}$$

Any n/w or system reaches steady state after overcoming transient state.

### Initial conditions

These are critical values of voltage across capacitor and current through inductors from previous steady state of a n/w :

$t = 0^-$  → instant just before switch operation

(steady state before s/w operation)

$t = 0^+$  → instant just after switch operation (TRANSIENT state)

$t \rightarrow \infty$  → steady state after switch operation.

### Order of a n/w

The no. of energy storage components available in undistributed form in any circuit.

Ex →  $R-L$ ,  $R-C$  → 1<sup>st</sup> order n/w

$R-L-C$ ,  $R-L-L$ ,  $C-C-R$  → 2<sup>nd</sup> order

NOTE

① A capacitor will never allow sudden change in voltage across it.

$$v_c(0^-) = v_c(0) = v_c(0^+)$$

② Inductor will never allow sudden change in current through it.

$$i_L(0^-) = i_L(0) = i_L(0^+)$$

# Behaviour of passive elements in transient state in comparison to steady state.

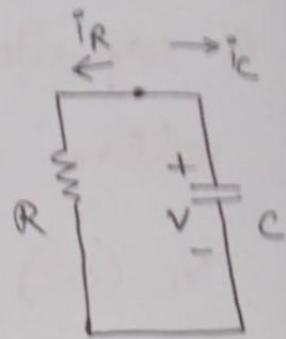
$t \rightarrow 0^+$  is considered as transient solution  
Also,  $s \rightarrow \infty$  is also considered as transient solution.

Element	Steady State		Transient State $(t \rightarrow 0^+)$ $s \rightarrow \infty$
	DC SS $(s=0)$	AC SS $(s=j\omega)$	
R	R	R	R
L	S.C.	i lags V by $\phi = 90^\circ$	O.C
C	O.C	i leads V by $\phi = 90^\circ$	S.C.

(I) source free 1st order

(a) RC circuit

$$\text{let } V(0) = V_0$$



KCL

$$i_R + i_C = 0$$

$$\Rightarrow \frac{V}{R} + C \frac{dV}{dt} = 0$$

$$\Rightarrow C \frac{dV}{dt} = -\frac{V}{R}$$

$$\Rightarrow \int \frac{dV}{dt} = \int -\frac{dt}{RC}$$

$$\Rightarrow \ln V = -\frac{t}{RC} + \ln(A)$$

$$\therefore \ln\left(\frac{V}{A}\right) = -\frac{t}{RC}$$

$$\Rightarrow \boxed{V = A e^{-t/RC}}$$

But at  $t=0$ ,  $V = V_0$  then  $A = V_0$

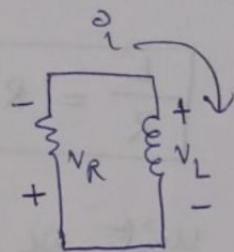
$$\therefore \boxed{V(t) = V_0 e^{-t/RC}}$$

$$\boxed{V(t) = V_0 e^{-t/\tau}}$$

$\tau = RC \rightarrow \text{time const. of RC w/w.}$

(b) RL circuit

$$i(0) = I_0$$



KVL

$$V_R + V_L = 0$$

$$\Rightarrow iR + L \frac{di}{dt} = 0$$

$$\Rightarrow L \frac{di}{dt} = -iR$$

$$\Rightarrow \int \frac{di}{i} = - \int \frac{R}{L} dt$$

$$\Rightarrow \ln[i] = -\frac{R}{L} t + \ln[A]$$

$$\Rightarrow \ln\left(\frac{i}{A}\right) = -\frac{R}{L} t$$

$$\Rightarrow i = A e^{-R/L t}$$

At  $t=0$ ,  $i=I_0$  then  $A=I_0$

$$i(t) = I_0 e^{-R/L t} = I_0 e^{-t/(4R)}$$

$$i(t) = I_0 e^{-t/\tau}$$

$$\tau = \frac{L}{R} \text{ time const. of RL n/w}$$

Time constant : It is the time taken by the response to reach 36.7% of its initial value (or) it is also defined as time taken by response to reach 63.4% to its final value.

→ Unit of time const. is seconds.

$$\boxed{\frac{L}{R} = RC}$$

unit of  $\frac{L}{RC} = \Omega$

unit of  $R^2 C = H$

unit of  $\frac{L}{R^2} = F$

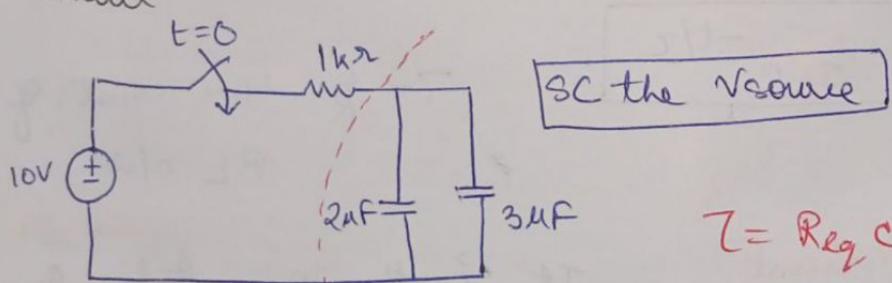
unit of  $\frac{RC}{L} = V$

unit of  $\frac{L}{C} = (\Omega)^2$

unit of  $\sqrt{LC} = \Omega$

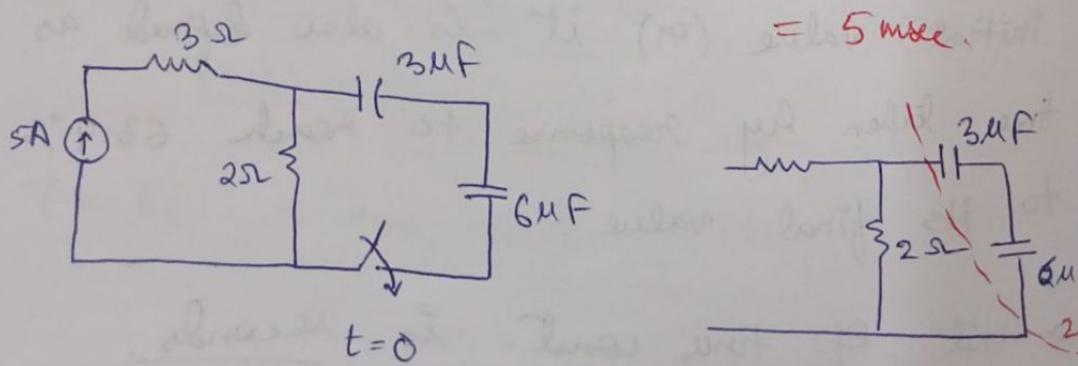
unit of  $\frac{R^2 C}{L} = I$

Q/ Determine the time constant of the circuit



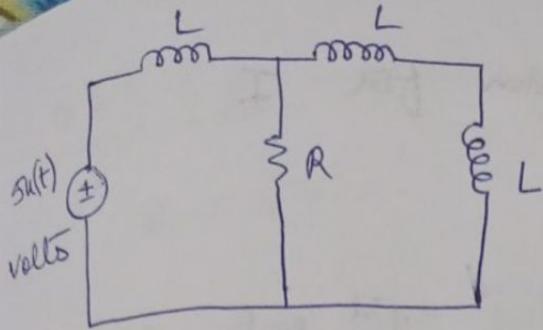
$$\tau = R_{eq} C_{eq}$$

$$= 1k \times 5\mu$$



$$= 5 \text{ msec.}$$

$$\tau = 2 \times 2 = 4 \text{ msec}$$

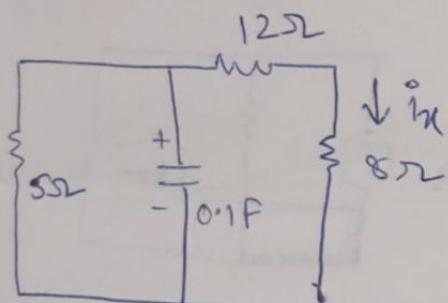


$$L_{eq} = L \parallel 2L$$

$$= \frac{2}{3}L$$

$$\tau = \frac{L}{R} = \frac{2}{3} \frac{L}{R}$$

If  $v(0) = 15V$ , find complete expression for  $i_R(t)$



This is a source free  
1st order RC n/w

State variable 'v'

$$v(t) = V_0 e^{-t/\tau}$$

$$\tau = R_{eq} \cdot C$$

$$V_0 = 15V \text{ (given)}$$

$$= (51120) \cdot 0.1$$

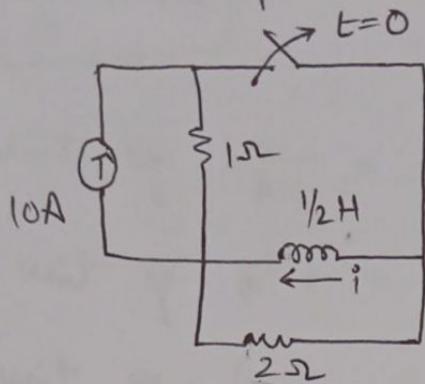
$$\tau = \frac{51120}{0.1} = 511200$$

$$= 0.4 \text{ sec.}$$

$$v(t) = 15 e^{-t/0.4} = 15 e^{-2.5t}$$

Now,  $i_R(t) = \frac{v(t)}{12 + 2} = 0.75 e^{-2.5t} A$

8) Find complete expression for  $I$ .

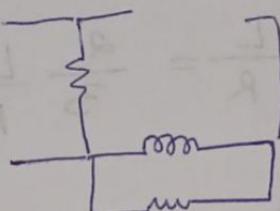


1st order RL  
circuit.

state variable  $\rightarrow i$

$$i(t) = I_0 e^{-t/\tau}$$

$$R_{eq} = 2\Omega$$

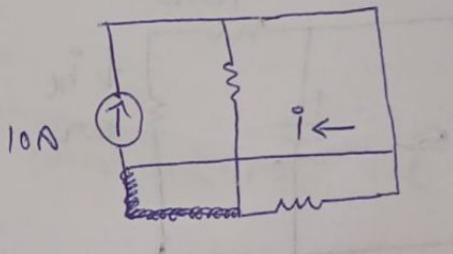


$$\tau = \frac{L}{R} = \frac{1/2}{2} = \frac{1}{4} \text{ sec}$$

$$i(0-) = 10 \text{ A} = I_0$$

$\uparrow$

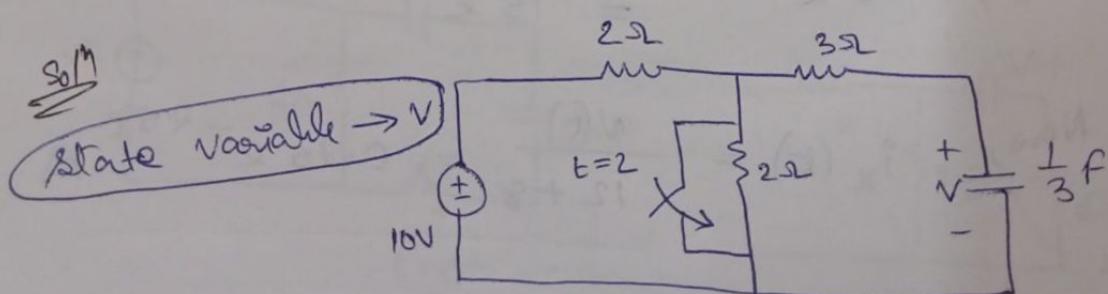
just steady state just  
before switch operation



$$\therefore i(t) = 10 e^{-t/1/4}$$

$$= 10 e^{-4t} \text{ A}$$

9) Determine the complete expression for  $V$  and determine energy stored in the capacitor upto 3rd second.



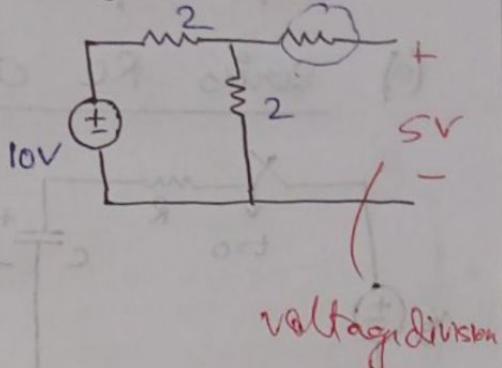
$0 < t \leq 2$   $\rightarrow$  steady state only.

$$V_0 = 5V$$

$t > 2$  transient solution

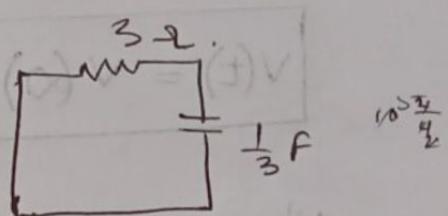
$$v = V_0 e^{-t/\tau}$$

Here  $V_0$  means  $v(2^-)$  from previous state.



$$\tau = R \cdot C$$

$$= \frac{1}{3} \times 3$$



$$v(t) = 5 e^{-(t-2)/1}$$

complete solution

$$v(t) = \begin{cases} 5V & , 0 < t \leq 2 \\ 5e^{-(t-2)} & , t \geq 2 \end{cases}$$

$$E = \frac{1}{2} C V^2$$

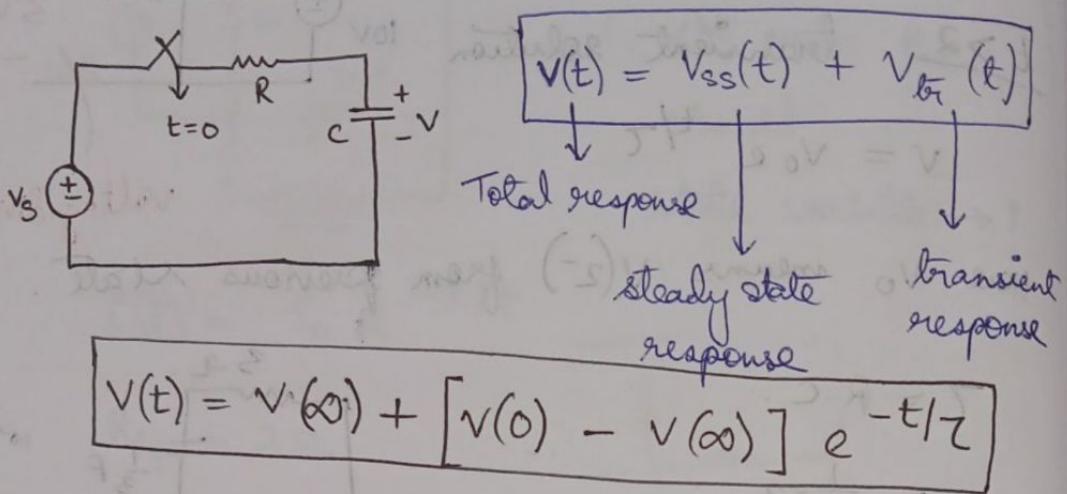
Now at  $t = 3$

$$v(t=3) = 5e^{-(3-2)} = 1.84V$$

$$E_C = \frac{1}{2} \times \frac{1}{3} \times (1.84)^2 = 0.56J$$

## # Step Response of 1st order circuit

### (a) Series RC circuit



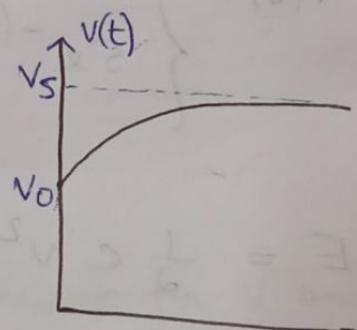
where

$V(0) \rightarrow$  voltage across capacitor before switch operation and steady state

$V(\infty) \rightarrow$  voltage across capacitor after switch operation and steady state.

$$\tau = RC$$

Case 1 : with initial condition given  
let  $V(0) = V_0$



$$V(t) = V_s + (V_0 + V_s) e^{-t/\tau}$$

Case 2 : w/o initial condition,

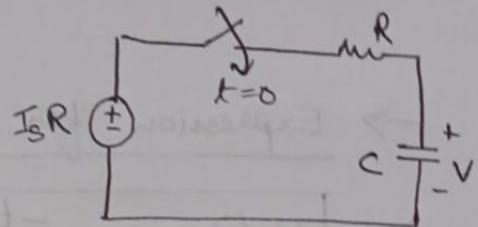
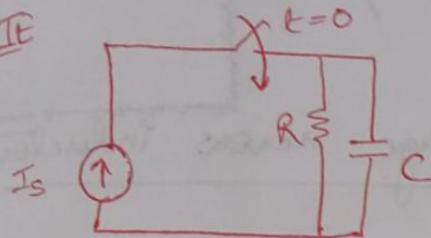
$$\text{let } V(0) = 0$$

$$V(t) = V_s (1 - e^{-t/\tau})$$

→ Expression for  $i_c(t)$

$$i_c(t) = \frac{V_s}{R} e^{-t/\tau}$$

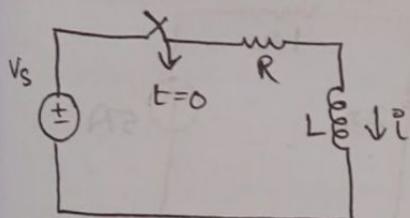
NOTE



$$V(t) = I_s R + [V_0 - I_s R] e^{-t/\tau}$$

→ Here we cannot connect a voltage source across capacitor as it will cause sudden change of voltage across capacitor which will result in large current which may damage the chl.

### (B) RL chl series



$$i(t) = i_{ss}(t) + i_{tr}(t)$$

$$i(t) = I(\infty) + [I(0) - I(\infty)] e^{-t/\tau}$$

$I(0) \rightarrow$  current through inductor before switch operation and steady state

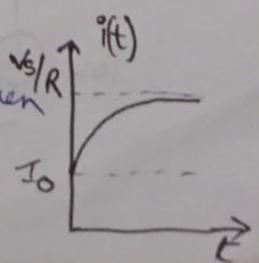
$I(\infty) \rightarrow$  current through inductor after switch operation and steady state

$$\tau = \frac{L}{R}$$

Case 1 : with initial condition given

$$\text{let } I(0) = I_0$$

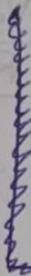
$$i_L(t) = \frac{V_s}{R} + [I_0 - \frac{V_s}{R}] e^{-t/\tau}$$



case 2: without initial condition

$$I(0) = 0$$

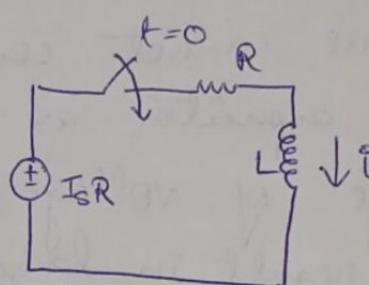
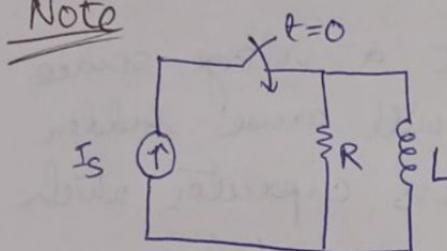
$$i_L(t) = \frac{V_s}{R} \left( 1 - e^{-t/\tau} \right)$$



→ Expression for voltage across inductor

$$V_L(t) = V_s e^{-t/\tau}$$

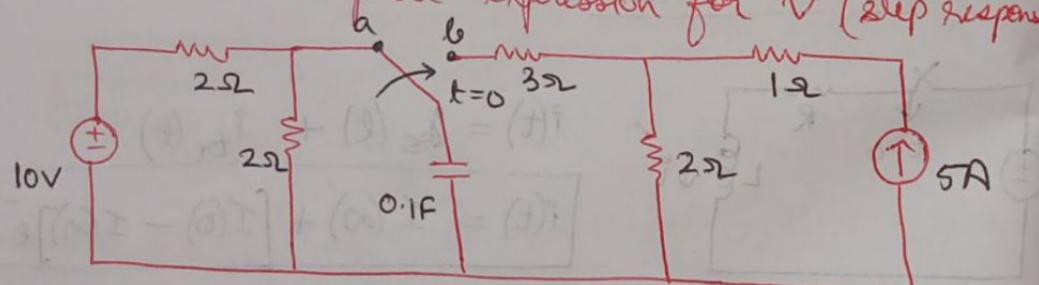
Note



$$i(t) = I_s + [I_0 - I_s] e^{-t/\tau}$$

$$\tau = L/R$$

Q) Find the complete expression for  $V$  (step response)

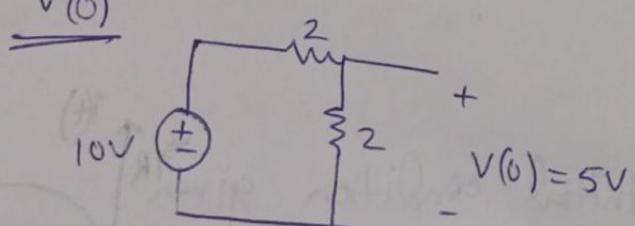


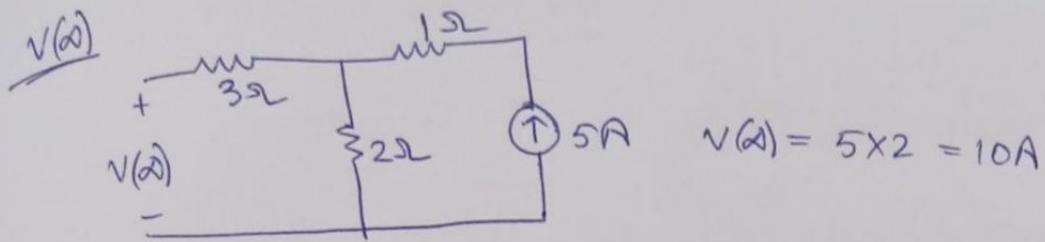
SolM

This is step response of 1st order RC circuit  
S.V.  $\rightarrow V$ .

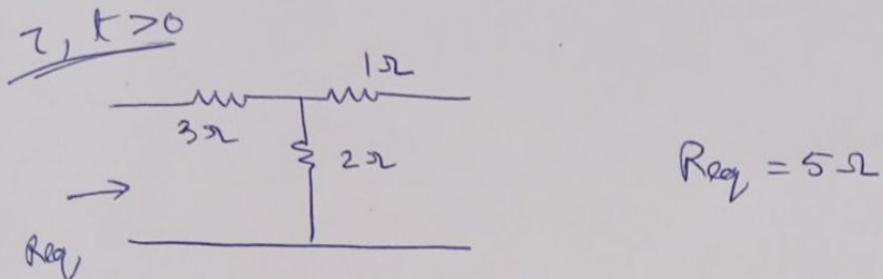
$$V(t) = V(\infty) + [V(0) - V(\infty)] e^{-t/\tau}$$

$V(0)$





$$V(0) = 5 \times 2 = 10\text{V}$$

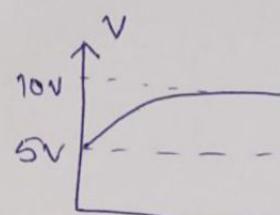


$$Req = 5\Omega$$

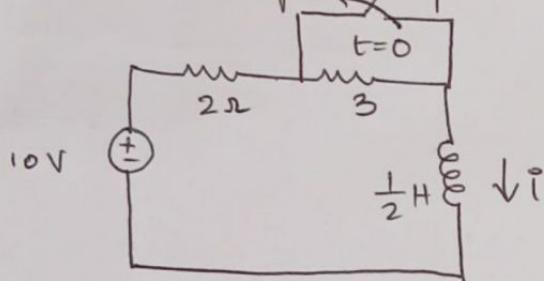
$$\tau = Req \cdot C = 5 \times 0.1 = \frac{1}{2} \text{ sec.}$$

$$v(t) = 10 + (5 - 10) e^{-t/\tau}$$

$$v(t) = 10 - 5e^{-2t}$$



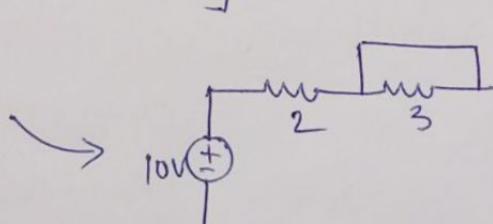
Q Find complete expression for  $I$  (step response)



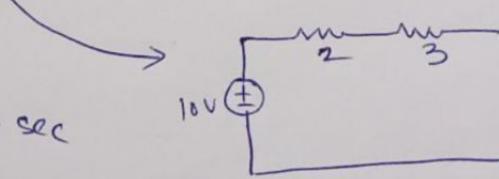
This is step response  
of 1st order RL w/o  $v$

$$i(t) = I(\infty) + [I(0) - I(\infty)] e^{-t/\tau}$$

$$I(0) = \frac{10}{2} = 5\text{A}$$



$$I(\infty) = \frac{10}{5} = 2\text{A}$$



$$\tau = \frac{L}{R} = \frac{1/2}{5} = \frac{1}{10} \text{ sec}$$

$$i(t) = 2 + 3e^{-10t}$$

