

Level-3

FPGAspeaks

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1 Introduction

This level consist of previous years GATE inspired questions. From here you need to code on your own no codes will be provided.

Check out questions.pdf in questions folder.

2 What you are supposed to do ?

In this level create a folder namely "codes" in that add Verilog code of solution for each Question along with the testbenches.

2.1 Note

Atlast you are needed to make single documentation consisting of all solutions. Also add Git-hub link of Verilog codes in your documentation.